

# Service Guide

## Agilent Technologies

### ESG Vector Signal Generator

This guide applies to the following signal generator model:

**E4438C ESG Vector Signal Generator**

Due to our continuing efforts to improve our products through firmware and hardware revisions, signal generator design and operation may vary from descriptions in this guide. We recommend that you use the latest revision of this guide to ensure you have up-to-date product information. Compare the print date of this guide (see bottom of page) with the latest revision, which can be downloaded from the following website:

*[www.agilent.com/find/esg](http://www.agilent.com/find/esg)*



**Part Number: E4400-90511**

**Printed in USA**

**May 2004**

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For signal generator warranty information, refer to the *Installation Guide* or *Service Guide*.

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# **1 Troubleshooting**

## Before Troubleshooting

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**WARNING** These troubleshooting instructions are for use by qualified personal only. To avoid electrical shock, do not perform any troubleshooting unless qualified.

---

**WARNING** The opening of covers or removal of parts is likely to expose dangerous voltages. Disconnect the signal generator from all voltage sources before it is opened.

---

**WARNING** The following techniques related to ESD and static-safe workstations should not be used when working on circuitry with a voltage potential greater than 500 volts.

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## ESD Information

### Protection from Electrostatic Discharge

Electrostatic discharge (ESD) can damage or destroy electronic components. All work on electronic assemblies should be performed at a static-safe workstation using two types of static-safe workstation protection:

- conductive table-mat and wrist-strap combination
- conductive floor-mat and heel-strap combination

Both types, when used together, provide a significant level of ESD protection. Of the two, only the table-mat and wrist-strap combination provides adequate ESD protection when used alone. To ensure user safety, the static-safe accessories must provide at least 1 meg ohm of isolation from ground.

### Handling of Electronic Components and ESD

---

**CAUTION** Many of the assemblies in this signal generator are very susceptible to damage from electrostatic discharge (ESD). Perform troubleshooting procedures only at a static-safe workstation and wear a grounding strap.

---

**CAUTION** Always handle printed circuit board assemblies by the edges. This will reduce the possibility of ESD damage to components and prevent contamination of exposed plating.

---

The possibility of unseen damage caused by ESD is present whenever components are transported, stored, or used. The risk of ESD damage can be greatly reduced by close attention to how all components are handled.

- Perform work on all components at a static-safe workstation.
- Keep static-generating materials at least one meter away from all components.
- Store or transport components in static-shielding containers.

## Getting Started with Troubleshooting

Determine the starting point in [Table 1-1](#) to begin troubleshooting.

- Problems should be worked on in the order they are presented in this table.
- Overviews of Self-Test, Frequency Synthesis, RF Path, Analog Modulation, and Digital Modulation are found in this chapter. The information in these overviews may help the reader better understand how the signal generator operates and the function of each assembly. Each overview is also intended to be used to troubleshoot the signal generator.

**Table 1-1**

If able to run self-test...	Go to this section...
Run Self-Test	<a href="#">“Self-Test Failures and Related Troubleshooting”</a> on page 1-17 and refer to the procedure <a href="#">“To Run Self-Test”</a> on page 1-17.
If unable to run self-test, check...	Go to this section...
Power Supply Failures	<a href="#">“ILT-1 Power Supply Failure and Self-Test”</a> on page 1-4
A12 CPU Power-on Failures	<a href="#">“ILT-2 A12 CPU Power-On Test”</a> on page 1-4
Front Panel Display or Keyboard Operations	<a href="#">“ILT3 Front Panel Display Troubleshooting”</a> on page 1-8
If other conditions occur...	Go to this section...
Unlocked Conditions	<a href="#">“Troubleshooting Unlocks”</a> on page 1-68
Unleveled Conditions	<a href="#">“Troubleshooting Unlevels and the RF Path”</a> on page 1-73
Adjustment Problems	<a href="#">“Troubleshooting Adjustment Problems”</a> on page 1-83
Performance Test Problems	<a href="#">“Troubleshooting Performance Test Problems”</a> on page 1-88

## Troubleshooting Aids

- semi-rigid cable-extender probe

The Agilent E8251-20088 semi-rigid cable-extender probe is used for measuring signals from connectors that are located inside the A19 Daughterboard card-cage.

- ribbon cable extender

The ribbon cable extender needs to be assembled by the user. It consists of two connector plugs (part number 1252-1010) and approximately eight inches of ribbon cable (part number 8120-2226). The ribbon cable contains 26 wires; six of these wires, on either the right or the left side, need to be stripped off before being connected to the plugs.

## ILT-1 Power Supply Failure and Self-Test

When the signal generator appears to be dead (no display or fan) there is often little evidence that points directly to the cause. This section provides steps and solutions to typical failure modes.

---

**CAUTION** Immediately unplug the signal generator from the ac power line if the unit shows any of the following symptoms:

- Smoke, arcing, or unusual noise from inside the signal generator.
- A circuit breaker or fuse on the main ac power line opens.

These potentially serious faults must be corrected before proceeding.

---

### Power Supply LED Checks

- ❑ Check the front panel LEDs (located next to the LINE switch) for normal operation in both standby and power-on conditions. Normal operation is indicated as follows:

**Table 1-2**

Power Switch	Yellow LED	Green LED
Standby	On	Off
Power-On	Off	On

- If normal operation is not observed, refer to [“Power Supplies” on page 1-20](#).
- If the fan does not operate when the signal generator is powered on, refer to the assembly level troubleshooting section and verify the fan voltage.
- If the fan operates when the signal generator is powered on, refer to [“ILT-2 A12 CPU Power-On Test” on page 1-4](#).

## ILT-2 A12 CPU Power-On Test

### A12 CPU Power-On Test

At power-on, the A12 CPU is designed to complete a series of operational checks. If all power supply voltages are present and the A12 CPU is working correctly, the operational checks are completed. Errors are reported if the A12 CPU has problems identifying other assemblies.

### Failure Symptoms

- A2 Display is not functioning
- A12 CPU fails to complete the power-on check
- no LAN activity
- no signal generator control from the front panel
- error message is displayed after power-on



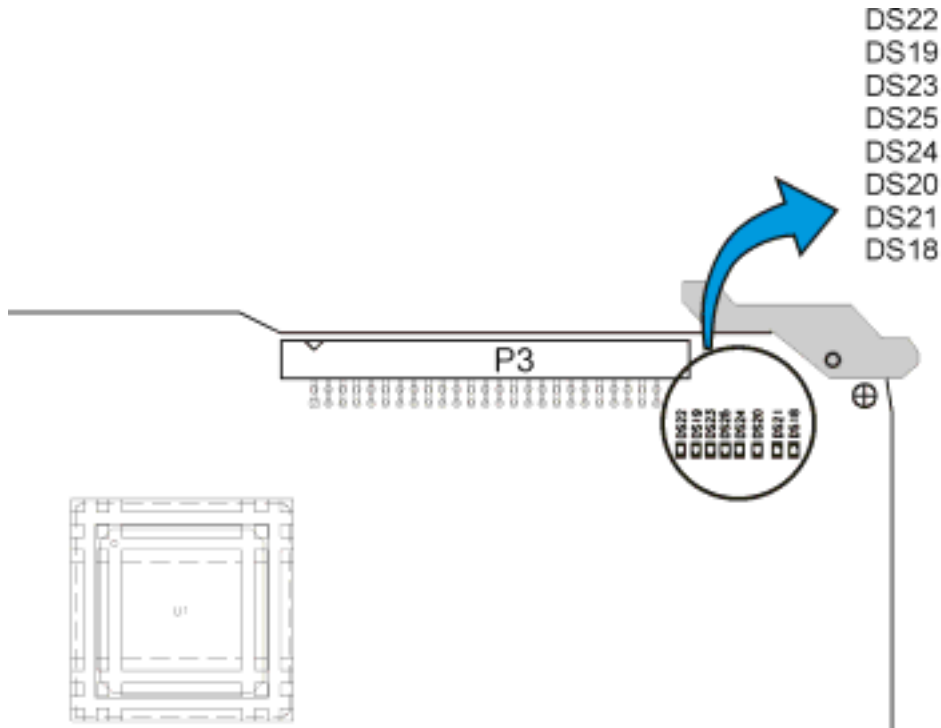
## A2 Display is Not Functioning

Refer to [ILT3 Front Panel Display Troubleshooting on page 1-8](#).

## A12 CPU Fails to Complete the Power-On Check

1. Turn the power off and remove outer and inner covers.
2. Turn the power on and verify that LEDs DS18 through DS25 of the A12 CPU are on. (Refer to [Figure 1-1](#) for the location of LEDs DS18 through DS25 of the A12 CPU.)
  - If any of the LEDs are off, turn the signal generator on its side and check the voltages in [Table 1-3](#) at the A23 Motherboard.
  - If the voltages are not present on connector J15 and J5 of the A23 Motherboard, refer to [“Power Supplies” on page 1-20](#).
  - If the voltages are present on connector J15 and J5 of the A23 Motherboard, turn the power off and remove the A12 CPU.
3. Use a DVM and ohm out J15 to J8 and J5 to J7.
  - If the connector is open, change the A23 Motherboard.
  - If the connector ohms out good, change the A12 CPU.

**Figure 1-1** LEDs DS18 through DS25 of the A12 CPU



a12-cpu\_ds18-25

**Table 1-3**

Power Supply Voltage (Vdc)	Connector Pins on the		Minimum Value (Vdc)	Maximum Value (Vdc)	LED on top-right corner of A12 CPU
	A12 CPU	A23 Motherboard			
+15	J8-103	J15-103	+14.1	+15.9	DS22
-15	J8-43	J15-43	-15.9	-14.1	DS19
+32	J8-44	J15-44	+31.04	+32.96	DS23
+5.2	J7-59, 60, 119, 120	J5-59, 60, 119, 120	+4.68	+5.72	DS25
+3.4	J7-55, 56, 57, 58, 115, 116, 117, 118	J5-55, 56, 57, 58, 115, 116, 117, 118	+3.298	+3.502	DS24
+9	J8-58	J15-58	+8.73	+9.27	DS20
-6	J8-57	J15-57	-6.36	-5.64	DS21
-5.2	J8-101	J15-101	-5.356	-5.044	DS18

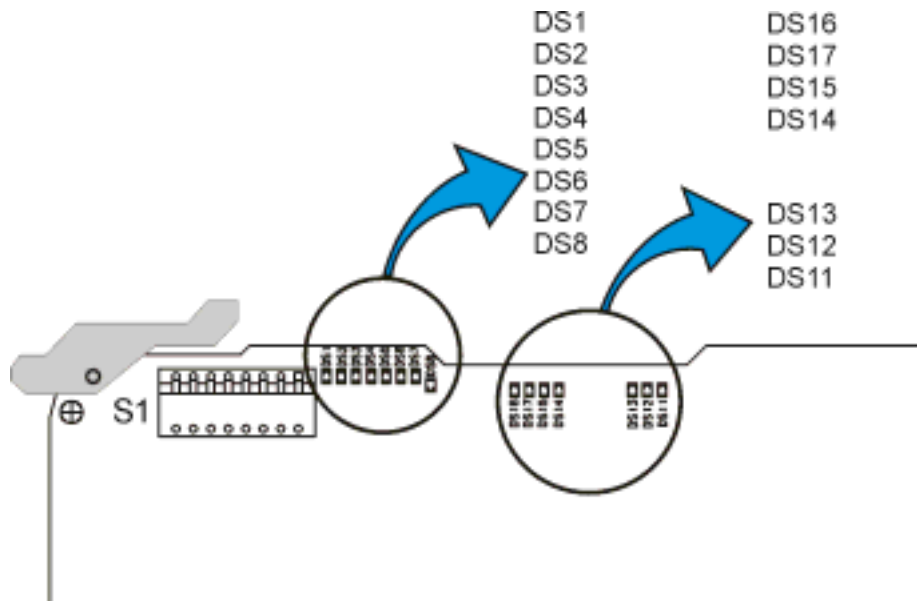
### Verifying the A12 CPU Power-On Sequence

1. Before verifying the power-on sequence, ensure that all eight DIP switches of S1 are in the CLOSED (up) position. (Refer to [Figure 1-2](#) and locate S1 in the upper left-hand corner of the A12 CPU.)
2. Verify the A12 CPU power-on sequence by observing DS1 through DS8.

When the power-on sequence is complete, DS1 through DS7 should be on and DS8 should be off.

- If the lights fail to step through the sequence, replace A12 CPU.  
(Refer to [“A12 CPU”](#) on page 2-35.)

**Figure 1-2** S1 and LEDs DS1 through DS8 and DS11 through DS17 of the A12 CPU



a12-cpu\_ds1-17

### Verifying the A5 10 GB Hard Drive (Option 005)

At power on, the hard drive's file structure is verified.

- If the hard drive is not formatted, the A12 CPU formats it.
- If the file structure is corrupted, the A12 CPU recreates the structure; this may take up to 30 seconds during the power-on sequence.
- If the power-on sequence does not complete, perform the following:
  - a. Turn power off to the signal generator.
  - b. Disconnect the hard drive's W26 ribbon cable from P3 of the A12 CPU.
  - c. Turn power on to the signal generator.
    - If the power-on sequence does not complete, refer to [A12 CPU Fails to Complete the Power-On Check](#) on page 1-5.
    - If the power-on sequence does complete, either the A5 10 GB Hard Drive (Option 005) or the A12 CPU hard drive interface is bad; these can be isolated by substitution.

### **No LAN Activity**

The signal generator supports 10-Base T-LAN. When the instrument is connected to an active LAN, a green LED (DS16) should blink on the A12 CPU; the blinking LED represents data communication over the LAN. (Refer to [Figure 1-2](#) and locate DS16 in the upper middle of the A12 CPU.)

### **No Instrument Control From the Front Panel**

Refer to [ILT3 Front Panel Display Troubleshooting on page 1-8](#).

### **Error Messages Displayed After Power-On**

If error messages are reported after the power-on check is completed, refer to the Error Messages manual for details.

## **ILT3 Front Panel Display Troubleshooting**

The Front Panel Display consists of the following four assemblies:

- A1 Keyboard
- A2 Display (640 x 320 pixel liquid crystal display)
- A3 Front Panel Auxiliary Board
- A4 Inverter

The A1 Keyboard contains the front panel hardkeys and softkeys, the power on/off switch along with the standby LED, and the power-on LED. The eight LCD data lines for the A2 Display are routed through the A3 Front Panel Auxiliary Board; the A12 CPU controls these eight LCD data lines.

The A4 Inverter turns the A2 Display on and off using control lines routed through the A3 Front Panel Auxiliary Board, which originate on the A12 CPU. The A4 Inverter is used to convert a +5.2 Vdc signal into a 40 kHz 290 Vac signal (approximately); this ac signal is then used to drive the A2 Display.

### **A2 Display Contrast and Brightness Controls**

There are two contrast hardkeys below the A2 Display. The left up-arrow key increases the contrast while the right down-arrow key decreases the contrast.

The A2 Display brightness can be adjusted from 50 (high brightness) to 1 (low brightness) using the RPG, arrow keys, or the numeric keypad.

Softkey Location: **Utility > Display > Brightness**

### **A2 Display Tests**

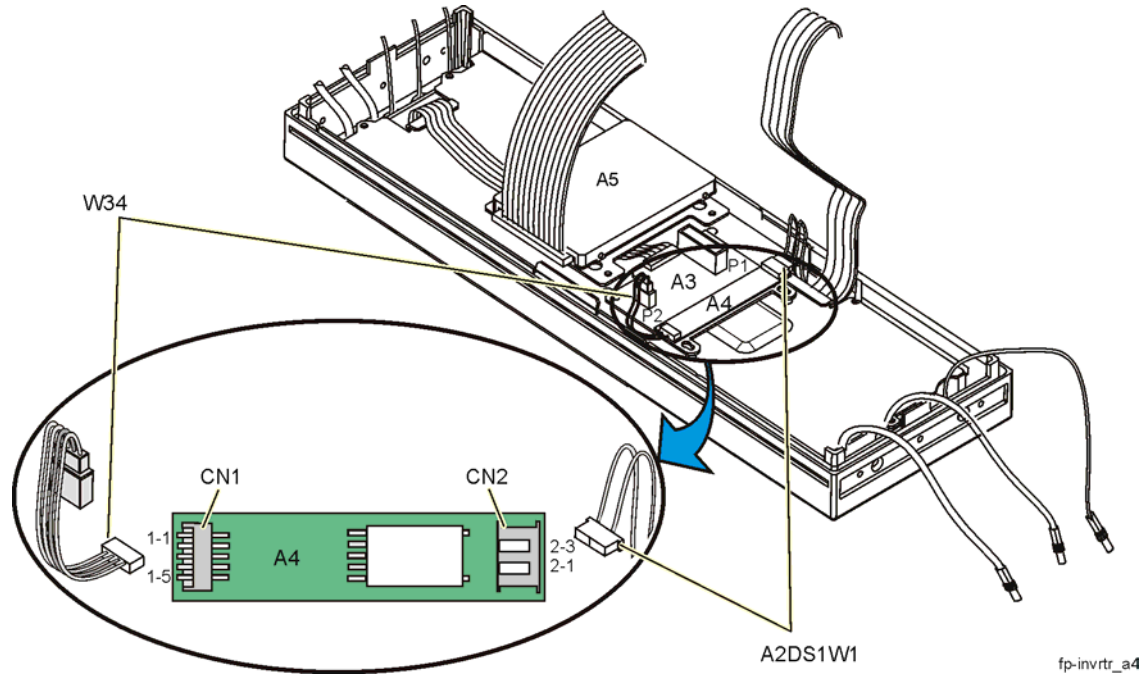
There are two screen tests for the A2 Display: the Black Pixel Screen Test and the White Pixel Screen Test. The Black Pixel Screen Test turns the display dark and the White Pixel Screen Test turns the display bright. To exit either test, press any front panel key.

Softkey Location: **Utility > Display > More (1 or 2)**

### **Symptom: If the Display is Dark**

The control lines for the A4 Inverter are routed from the A12 CPU through the A23 Motherboard and the A3 Front Panel Auxiliary Board to the A4 CN1 connector. The +5.2 Vdc is routed from the A6 Power Supply through the A23 Motherboard and the A3 Front Panel Auxiliary Board to the CN1 connector of the A4 Inverter.

Figure 1-3 A4 Inverter Connections



- ❑ Check that the screen saver is not on.
  - Press any front panel key to disable the screen saver.
- ❑ Check that the signal generator is powered-on and the front-panel green LED is lit.
  - If the green LED is not lit, refer to [Troubleshooting Assembly Level Problems on page 1-20](#).

---

**WARNING** High voltage is present at the A4 Inverter output (CN2-1). Also, while measuring CN2, be careful of the fan blades on B1, B2, and B3. Keep hands and fingers away from the fan blades to avoid injury!

---

- ❑ Check the voltage, using a DVM, at the A4 Inverter output (CN2-1).
  - To access this connector and measure the voltage:
    1. Disconnect the front panel assembly from the chassis frame.
    2. Slide the front panel display away from the chassis while ensuring that the ribbon cables to the A23 Motherboard stay connected.

**Troubleshooting Performance Test Problems**

- ❑ Check that there is approximately 290 Vac, 40 kHz signal at CN2-1. (This check should be made with the A4 Inverter connected to the A2 Display.)
  - If the signal is present at the A4 Inverter output, replace the A2 Display. (Refer to “A2 Display” on page 2-11.)
  - If the signal is not present at CN2, use a DVM to measure the signals shown in Table 1-4 at CN1 on the A4 Inverter. Probe CN1 from the top of the connector while it is still connected to the A3 Front Panel Auxiliary Board.

**Table 1-4**

Signal	State
CN1-1	5.2 Vdc
CN1-3 LIGHT_EN	>3 Vdc
CN1-4 VDISP	Brightness = 50, VDSP < 50 mVdc Brightness = 1, VDSP = 3 Vdc

- If the signals in Table 1-4 are correct, replace the A4 Inverter. (Refer to “A4 Inverter” on page 2-19.)
- If any of the signals are incorrect:
  - a. Using a DVM, measure the signals shown in Table 1-5 at J30 of the A23 Motherboard.
    - If the signals in Table 1-5 are correct, replace the A3 Front Panel Auxiliary Board. (Refer to “A3 Front Panel Auxiliary Board” on page 2-17.)
    - If the 5.2 VD\_LCD line is incorrect, refer to the “ILT-1 Power Supply Failure and Self-Test” on page 1-4.
    - If either the LIGHT\_EN or VDISP are incorrect:
      - a. Using a DVM, measure the signals at J15-37 (LIGHT\_EN) or J15-97 (VDISP) of the A23 Motherboard.
        - If either signal is incorrect, replace the A12 CPU. (Refer to “A12 CPU” on page 2-35.)

**Table 1-5**

Signal	State
J30-32 5.2 VD_LCD	5.2 Vdc
J30-20 LIGHT_EN	>3 Vdc
J30-30 VDISP	Brightness = 50, VDSP < 50 mVdc Brightness = 1, VDSP = 3 Vdc

**Symptom: If the Display is Bright, but without Characters**

- ❑ Check that the signal generator is powered-on and the front panel green LED is lit. If the green LED is not lit, refer to the Power Supply Troubleshooting section in this manual. Press any front panel key to disable the screen saver.
  - If the green LED is lit and the display is too bright, try adjusting the contrast controls on the front panel. Refer to “Symptom: If the Display is Dark” on page 1-8.
  - If adjusting the contrast controls has no effect, use an oscilloscope to check the signals in Table 1-6. The pulse-state signals listed are control, clock, and data lines for the A2 Display. Probe the signals on the back of the ribbon cable on P1 of the A3 Front Panel Auxiliary Board.

**Table 1-6**

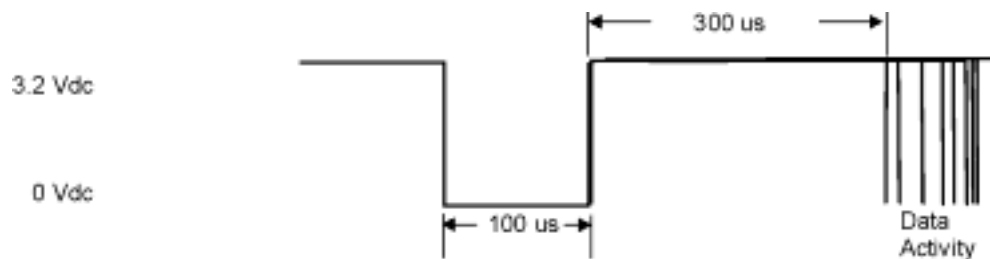
Signal	State
J1-7 VLCD	approximately 21 Vdc
J1-4 LCD_EN_L	>3 Vdc
J1-5	5.2 Vdc
J1-8 to 15	Refer to Figure 1-4.

- If the signals in Table 1-6 are correct, replace the A2 Display. (Refer to “A2 Display” on page 2-11.)
- If the signals are incorrect, use an oscilloscope to check J30 on the A23 Motherboard for signals shown in Table 1-7.

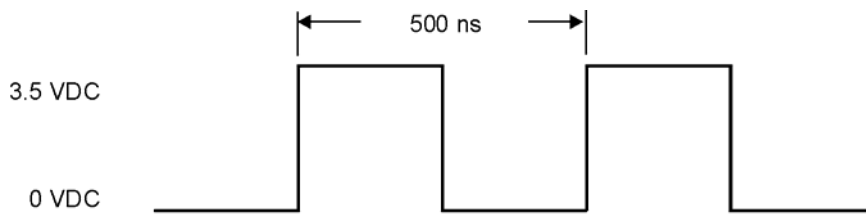
**Table 1-7**

Signal	State
J30-3 VLCD	approximately 21 Vdc
J30-5 LCD_EN_L	>3 Vdc
Pulsing Activity LCD_DATA J1-8 to 15; J30-13, 14, 17, 18, 23, 24, 27, 28; J15-24, 25, 28, 29, 32, 33, 86, 90	Refer to Figure 1-4.
Pulsing Activity LCD_CLK J1-3; J30-4; J15-94	Refer to Figure 1-5.
Pulsing Activity LCD_LOAD J1-2; J30-8; J15-36	Refer to Figure 1-6.
Pulsing Activity LCD_FRAME J1-1; J30-7; J15-93	Refer to Figure 1-7.

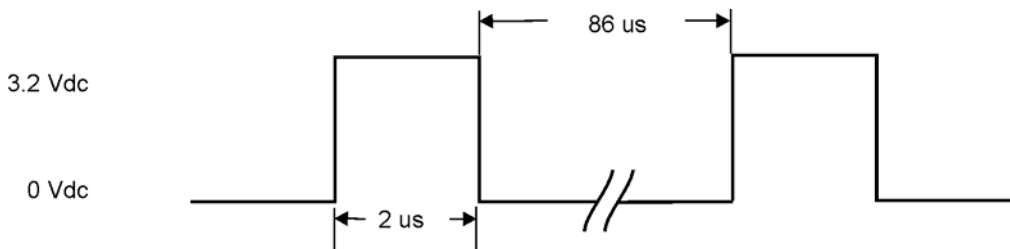
**Figure 1-4 Pulsing Activity LCD\_DATA  
 J1-8 to 15; J30-13, 14, 17, 18, 23, 24, 27, 28; J15-24, 25, 28, 29, 32, 33, 86, 90**



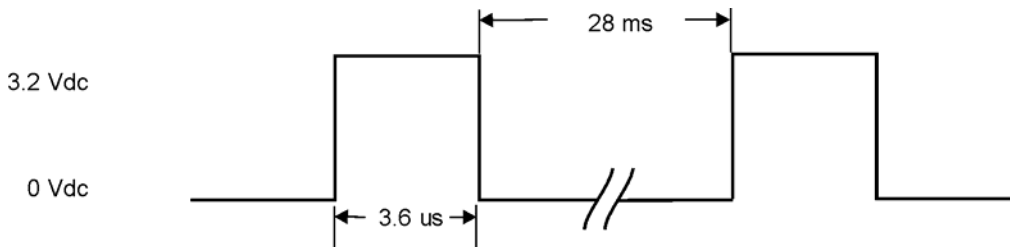
**Figure 1-5 Pulsing Activity LCD\_CLK  
 J1-3, J30-4, J15-94**



**Figure 1-6 Pulsing Activity  
 J1-2, J30-8, J15-36**



**Figure 1-7 Pulsing Activity  
 J1-1, J30-7, J15-93**



- If the signals in [Table 1-7](#) are correct, replace the A3 Front Panel Auxiliary Board. (Refer to [“A3 Front Panel Auxiliary Board”](#) on page 2-17.)
- If any of the signals are incorrect, use an oscilloscope to check J15 on the A23 Motherboard for signals shown in [Table 1-8](#).

**Table 1-8**

Signal	State
J15-35 VLCD	approximately 21 Vdc
J15-95 LCD_EN_L	>3 Vdc
J15-24, 25, 28, 29, 32, 33, 36, 86, 90, 93, 94	Refer to <a href="#">Figure 1-4</a> .

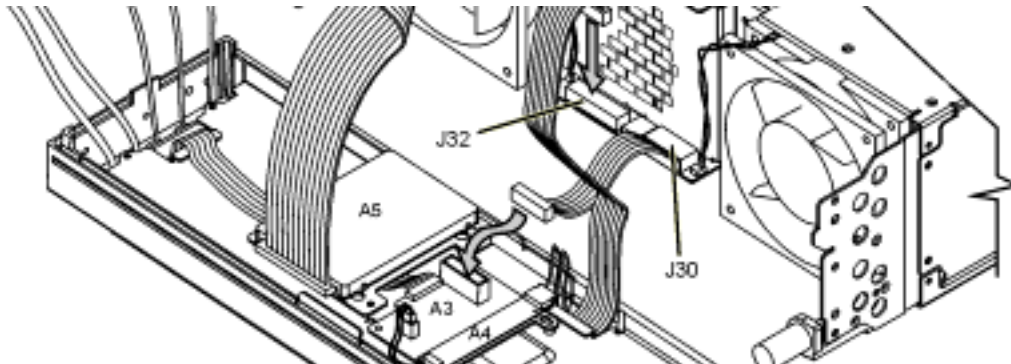
- If the signals in [Table 1-8](#) are incorrect, replace the A12 CPU (Refer to [“A12 CPU”](#) on page 2-35).



## Symptom: If Hardkeys or Softkeys are Not Functioning

The A1 Keyboard contains the switches for the hardkeys and softkeys. These hardkeys and softkeys are arranged in a matrix, as shown in [Table 1-9](#), with the control lines KEYCOLx and KEYROWy on the x and y-axis. When a hardkey or softkey is pressed, one cell of the matrix is activated. The normally TTL low KEYROWy, of the active cell, will pulse high and the normally TTL high KEYCOLx, of the active cell, will pulse low. These TTL signals may be measured with an oscilloscope at J32 on the A23 Motherboard. Use [Table 1-9](#) for hardkey and softkey matrix locations.

**Figure 1-8** J30 and J32 of the A23 Motherboard



- Ensure that the signal generator is powered-on, that the front panel green LED is lit, and that the signal generator is not being controlled remotely.
  - If the signal generator is powered-on and the green LED is lit:
    - a. Press the Local hardkey to place the signal generator in local-control mode.
    - b. Press any suspected defective hardkeys or softkeys while monitoring their corresponding KEYCOLx and KEYROWy line at J32 of the A23 Motherboard.
  - If either signal is incorrect, refer to the [Table 1-10](#) and check the signal at J15 on the A23 Motherboard.

J15 is where the signal leaves the A12 CPU.

    - If the signal is incorrect at J15, replace the A12 CPU.  
(Refer to [“A12 CPU”](#) on page 2-35.)
    - If both signals at the J32 and J15 connectors are correct, replace the A1 Keyboard.  
(Refer to [“A1 Keyboard”](#) on page 2-8.)
  - If the signal generator is powered-on and the green LED is not lit, refer to [“Power Supplies”](#) on page 1-20.

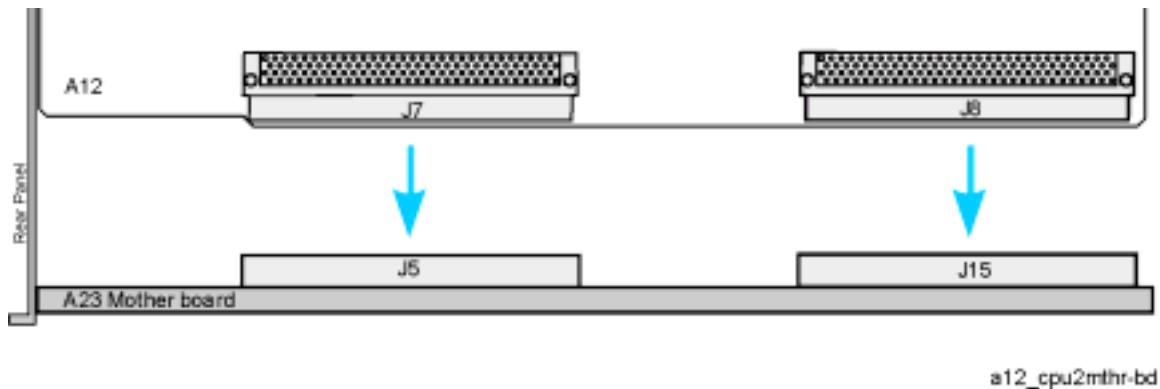
**Table 1-9 A1 Keyboard Control Lines (KEYCOLx and KEYROWy)**

	<b>KEYCOL0 J32-1</b>	<b>KEYCOL1 J32-3</b>	<b>KEYCOL2 J32-5</b>	<b>KEYCOL3 J32-7</b>	<b>KEYCOL4 J32-9</b>	<b>KEYCOL5 J32-11</b>	<b>KEYCOL6 J32-13</b>	<b>KEYCOL7 J32-15</b>
KEYROW0 J32-17	Softkey 3	NA	Cont Up	NA	NA	Mode Menu	Mode Sel	NA
KEYROW1 J32-19	Softkey 2	Incr/Set	Cont Down	Ampl	Ampl Menu	AM	Pulse	Utility
KEYROW2 J32-21	Softkey 1	Up	NA	Freq	Freq Menu	FM/ΦM	Dig Mod	Cal
KEYROW3 J32-23	Softkey 4	NA	NA	Right Arrow	Help	Save	Recall	Trigger
KEYROW4 J32-25	Softkey 6	NA	Local	Hold	Mod On/Off	4	5	6
KEYROW5 J32-26	Softkey 5	NA	Return	Down Arrow	NA	7	8	9
KEYROW6 J32-24	NA	NA	NA	Left Arrow	RF On/Off	0	.	+/-
KEYROW7 J32-22	Softkey 7	NA	Preset	NA	NA	1	2	3

**Table 1-10 J15 Control Lines (KEYCOLx and KEYROWy)**

KEYROW0	J32-17	J15-111	KEYCOL0	J32-1	J15-46
KEYROW1	J32-19	J15-51	KEYCOL1	J32-3	J15-47
KEYROW2	J32-21	J15-112	KEYCOL2	J32-5	J15-108
KEYROW3	J32-23	J15-52	KEYCOL3	J32-7	J15-48
KEYROW4	J32-25	J15-53	KEYCOL4	J32-9	J15-109
KEYROW5	J32-26	J15-114	KEYCOL5	J32-11	J15-49
KEYROW6	J32-24	J15-54	KEYCOL6	J32-13	J15-110
KEYROW7	J32-22	J15-55	KEYCOL7	J32-15	J15-50

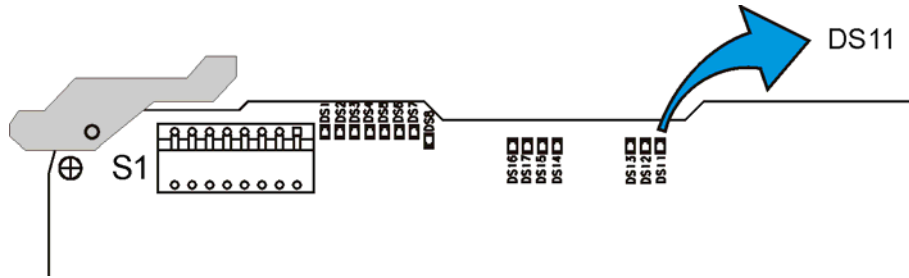
**Figure 1-9 J15 of the A23 Motherboard**



**Symptom: If the RPG Knob is Not Functioning**

When the RPG Knob is functioning properly, DS11 on the A12 CPU should blink each time that the RPG knob is rotated.

**Figure 1-10**      **Blinking of DS11 Indicates RPG Knob Activity**



- Ensure that the signal generator is powered-on and that the front panel green LED is lit.
  - If the green LED is lit and the RPG knob is not functioning:
    - a. Check J32-12 for +5.2 VD\_LCD.
      - If the voltage is present on J32-12:
        - a. Press the Frequency hardkey on the front panel.
        - b. Using an oscilloscope, monitor J32-14 and J32-10 while rotating the RPG knob.

The display frequency value should change and both signals at J32-14 and J32-10 should pulse to a TTL high.

- If either of the signals do not pulse to a TTL high, replace the A1 Keyboard. (Refer to “A1 Keyboard” on page 2-8.)

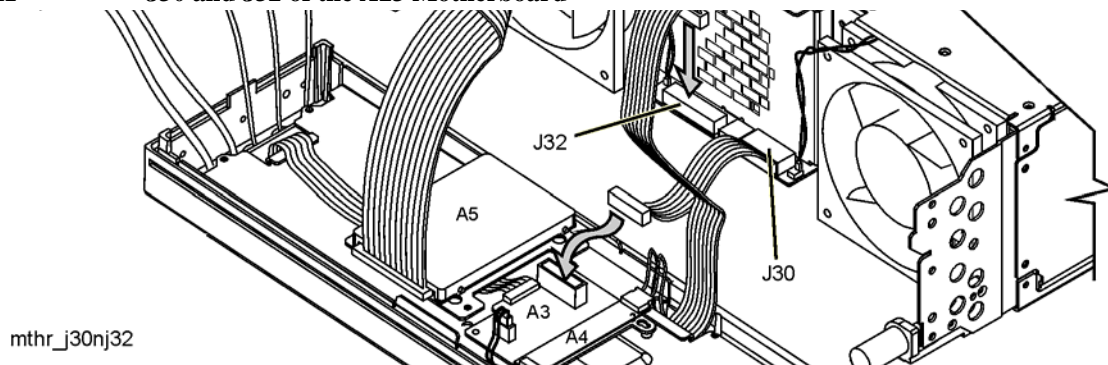
- If the signals are pulsing to a TTL high:

- a. Check the signal at J15-106 and J15-107 of the A23 Motherboard.
  - If the signals are pulsing to a TTL high, replace the A12 CPU. (Refer to “A12 CPU” on page 2-35.)

- If the voltage is not present on J32-12, refer to “Power Supplies” on page 1-20.

- If the green LED is not lit, refer to “Power Supplies” on page 1-20.

**Figure 1-11**      **J30 and J32 of the A23 Motherboard**



mthr\_j30nj32

**Symptom: If the Screen Saver is Not Functioning**

The screen saver delay may be set for any integer from 1 to 12 hours. The A12 CPU controls the screen saver by pulling the LIGHT\_EN line low.

- Ensure that the signal generator is powered-on and that the front panel green LED is lit.
  - If the green LED is lit, the delay time has elapsed, and the display hasn't gone dark:
    - a. Check the LIGHT\_EN signal at J30-20. (Refer to [Figure 1-11](#).)
      - If the signal is a TTL low, replace the A4 Inverter. (Refer to [“A4 Inverter” on page 2-19](#).)
      - If the signal is a TTL high:
        - a. Check J15-37 of the A23 Motherboard. (Refer to [Figure 1-9](#).)
          - If the signal is a TTL high, replace the A12 CPU. (Refer to [“A12 CPU” on page 2-35](#).)
  - If the green LED is not lit, refer to [“Power Supplies” on page 1-20](#).

## Self-Test Failures and Related Troubleshooting

### Overview

The philosophy behind self-test is, “*It is better to pass a defective signal generator than to fail a good signal generator.*” Self-Test is designed as a diagnostic tool used to check signal generator hardware. Self-tests do not require the signal generator to be calibrated before self-tests will pass. This philosophy implies that some failures will not be detected and reported as bad, but will pass. If the signal generator powers on and self-tests can be run, troubleshooting should always start by running self-tests and resolving reported problems first.

Embedded in the signal generator’s firmware is a group of tests (self-tests) that can be used to evaluate the condition of the circuitry on many of the assemblies. Self-tests check hardware performance under controlled conditions and compare test results to predetermined limits. If the results fall within the limits, the signal generator passes. If one or more test results fall outside the limits, the self-test routine evaluates the failures and reports the most independent failure.

1. When the signal generator is tested at the factory, self-test limits are loaded into the A12 CPU memory from devices on each assembly or from fixed files. Most assemblies contain limit information while microcircuit limits must be loaded.
2. When the program runs, latches and DACs are set to a known state.
3. An analog multiplexer routes the detected dc signals from the selected test node to the A12 CPU.
4. The A12 CPU compares the detected dc level to the test limit. If the detected value is within the limits, the A12 CPU reports a pass.
5. If one or more detected values fall outside the limits, the most independent failure is determined and reported.

Independent failures can best be understood by using an example where there are three assemblies in a series: A1 is the first assembly, A2 the second assembly, and A3 the third assembly. A1 output is the input for A2 and A2 output is the input for A3. If A1 output is bad, A2 and A3 outputs are also bad. If A2 output is bad, A3 output is bad.

A file in the signal generator contains this dependency knowledge for the signal generator to use when identifying the most independent failure. If A1, A2, and A3 self-tests all fail, the signal generator reports A1 as the most independent failure. If A1 passes, but A2 and A3 fail, self-test reports A2 as the most independent failure. If A2 passes and A3 fails, self-test reports A3 as the most independent failure. If two independent failures occur, self-test reports only one failure at a time and reports the second failure after the first reported failure is repaired. The troubleshooting procedure is based on the most independent failure and no lower level information is needed. Although not needed for troubleshooting the signal generator, the self-test overhead provides features to view lower level detailed information about the subroutines and select specific routines to run.

These features include:

- Select/Deselect Test
- Select/Deselect All
- Run Highlighted Tests
- Run Selected Tests
- View Details

When View Details is selected, the measured value, lower, and upper limits are displayed on the highlighted test.

### To Run Self-Test

1. Press the Utility key.
2. Press the Instrument Info/Help Mode softkey.
3. Press the Self Test softkey.
4. Press the Run Complete Self Test softkey.

**Troubleshooting Performance Test Problems**

In the following table, the first column lists self-test errors, and the second column points to the section that describes assembly-level troubleshooting procedures related to the self test.

**Table 1-11 Self-Test Failures and Related Troubleshooting**

If the following self-test fails...	Go to this assembly troubleshooting section...
100 Power Supply 101 Cal Source 102 ExtIn Offset DAC and Switch 103 Path 1&2 and ExtOut 104 Path1 Atten+Summing Amp 105 Path2 Atten+Summing Amp 106 DC-Coupled Mode Test 107 Drift Canceller Test 108 IQ Polarity Switch 109 RMS to DC Conv Test	<a href="#">“Self-Tests 1xx for the A10 I/Q Multiplexer” on page 1-27</a>
200 Power Supply 201 Tuning + Bias Test 203 YO Loop Detector	<a href="#">“Self-Tests 2xx for the A15 Sampler (Option UNJ or Option 506)” on page 1-29</a>
400 Ground and PTAT Test 401 Prelevel Loop Test 402 Quadrature DAC Test 403 VBLO Test 404 Marble Test 405 Gain Adjustment Test 406 Prelevel Bypass Test 407 Switched Filters Test 408 ALC Reference Test 409 ALC Modulation Test 410 Burst Modulation Test 411 ALC Detector Test 412 Het Band Test 413 Bulk R DAC Test 414 Log Offset Test	<a href="#">“Self-Tests 4xx for the A13 Output” on page 1-32</a>
500 Power Supply 501 Loop Gain 502 Tuning Voltage 503 RF Detector 504 FM Inversion	<a href="#">“Self-Tests 5xx for the A17 Synthesizer (Option 501, 502, 503, 504)” on page 1-36</a>
600 Post Regulators 601 DACs Test 602 PLL Interface 603 FM Driver	<a href="#">“Self-Tests 6xx for the A21 YTO Driver (Option UNJ or Option 506)” on page 1-39</a>
700 Power Supply 701 Loop Gain 702 Tuning Voltage 703 Output Voltage 704 Filter Test 705 F/2 Test 706 FM Path Test	<a href="#">“Self-Tests 7xx for the A16 Frac-N (Option UNJ or Option 506)” on page 1-42</a>

**Table 1-11 Self-Test Failures and Related Troubleshooting (Continued)**

If the following self-test fails...	Go to this assembly troubleshooting section...
800 1 GHz Detector 801 Tuning Voltage 802 AM MUX and DACs 803 FM MUX and DACs	“Self-Tests 8xx for the A18 Reference” on page 1-47
1100 Power Supply	“Self-Tests 11xx for the A12 CPU” on page 1-49
1400 Power Supply	“Self-Tests 14xx for the A11 Internal Bit Error Rate Analyzer (Option UN7)” on page 1-51
1500 Power Supply	“Self-Tests 15xx for the A9 Digital Demodulator (Option 501, 502, 503, 504 with Option 300)” on page 1-53
1600 Host <-> BBG Comm 1601 Apps1 <-> Apps2 Comm 1602 Apps1 <-> SH4 Comm 1603 Apps2 <-> SH4 Comm 1604 Fliegen <-> Apps2 Comm 1605 SRAM Test 1606 SDRAM Test 1607 Paren <-> SH4 Comm 1608 VCO Tune Test 1609 IQ DAC Output Test 1610 PLL Test	“Self-Tests 16xx for the A7 Baseband Generator (Option 001/601 or Option 002/602)” on page 1-55
2000 Ground and PTAT Test 2001 Prelevel Loop Test 2002 Quadrature DAC Test 2003 VBLO Test 2004 Marble Test 2005 Gain Adjustment Test 2006 ALC Reference Test 2007 ALC Modulation Test 2008 Burst Modulation Test 2009 ALC Detector Test 2010 Bulk R DAC Test 2011 Log Offset Test	“Self-Tests 20xx for the A14 Extended Frequency Output (Option 506)” on page 1-58
2100 Power Supply 2101 Sweep Diag 2102 LCD Bias	“Self-Tests 21xx for the A23 Motherboard” on page 1-62

## Troubleshooting Assembly Level Problems

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**WARNING** Wait 30 seconds after unplugging the instrument to allow the power supplies to discharge before removing or installing any assemblies.

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### Power Supplies

The power supply is a switching supply that has an automatic line-voltage and frequency selection. There are no switches to change for 115 Vac or 240 Vac operation. The power line fuse is not replaceable. If the fuse opens, the power supply must be replaced. To troubleshoot the signal generator's power supplies, refer to [Table 1-12](#), [Table 1-13](#), [Table 1-14](#), and [Table 1-15](#).

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**NOTE** Many of the power supplies voltages shown in [Table 1-12](#), [Table 1-13](#), [Table 1-14](#), and [Table 1-15](#) are filtered or regulated on the A23 Motherboard. In some cases, the voltage level may be correct when measured at the A6 Power Supply, but wrong when measured at a destination connector. If this type of situation occurs, it could be due to an open inductor, a shorted capacitor in a filtering circuit, or a bad regulator located on the A23 Motherboard; this would indicate that the A23 Motherboard needs to be changed.

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**Table 1-12**

<b>A23 Motherboard test points for:</b>	<b>+32</b>	<b>+15</b>	<b>MB LED +12.5<sup>ab</sup></b>	<b>MB LED -12.5<sup>ab</sup></b>	<b>+9 VF<sup>c</sup>  Filtered +9</b>	<b>+5.2 VA<sup>c</sup>  Filtered +5.2 VD</b>	<b>MB LED +5.2 VD<sup>ac</sup></b>
A2 Display							
A3 Front Panel Auxiliary Board							
A6 Power Supply	J28-7	J28-25, 36			J28-19, 42	J28-14, 15, 46, 47	J28-14, 15, 46, 47
Slot 1 <sup>d</sup> A7 Baseband Generator (Option 001/601 or Option 002/602)	J11-4	J11-28				J11-2, 27	J1-64, 65, 129, 130
Slot 2 A9 Digital Demodulator (Option 501, 502, 503, 504 with Option 300)	J12-4	J12-28				J12-2, 27	J2-64, 65, 129, 130
Slot 3 A10 I/Q Multiplexer	J13-4	J13-28				J13-2, 27	J3-64, 65, 129, 130
Slot 4 A11 Internal Bit Error Rate Analyzer (Option UN7)		J14-28				J14-2, 27	J4-64, 65, 129, 130
Slot 5 A12 CPU	J15-44	J15-103			J15-58	J15-42	J5-59, 60, 119, 120
A19 Daughterboard	J10-83	J10-32, 82			J10-34, 84	J10-30, 31, 80, 81	
A20 Downconverter (Option 501, 502, 503, 504 with Option 300)							
A21 YTO Driver (Option UNJ or Option 506)							
A25 Rear Panel Board or A26 Rear Panel LVDS Board (Option 1EM) and A27 Rear Panel SMB Board (Option 1EM)							
AT1 Electronic Attenuator (Option 501, 502, 503, 504 or Option UNJ)		J25-1	J25-14	J25-13			J25-15
or AT1 High-Power Mechanical Attenuator (Option UNB) and A28 Reverse Power Protection (Option 501, 502, 503, 504 or Option UNJ)		J25-1 J22-5	J25-14 J22-3	J25-13 J22-4			J25-15
B1 Power Supply Fan							
B2 Small Fan							
B3 Daughterboard/Card Cage Fan							

- a. MB LED means that an LED for this power supply is visible from the top-side of the A23 Motherboard.
- b. The voltage regulator for this power supply is located on the A23 Motherboard.
- c. The voltage for this power supply is filtered on the A23 Motherboard.
- d. Slot numbers one through five (where one is on the left) refer to the position of an assembly in the top card-cage of the signal generator when viewed from the top.

Troubleshooting  
**Troubleshooting Performance Test Problems**

**Table 1-13**

A23 Motherboard test points for:	MB LED +3.4 VDF <sup>a</sup>	MB LED +2.6 VD_1 <sup>ab</sup>	MB LED +2.6 VD_2 <sup>ab</sup>	Self-Test Node +1.8 VD_1 <sup>bc</sup>	Self-Test Node +1.8 VD_2 <sup>bc</sup>	Self-Test Node +1.95 VD_1 <sup>bc</sup>	Self-Test Node +1.95 VD_2 <sup>bc</sup>
A2 Display							
A3 Front Panel Auxiliary Board							
A6 Power Supply	J28-1, 2, 5, 6, 9, 10, 51, 52, 55, 56, 59, 60						
Slot 1 A7 Baseband Generator (Option 001/601 or Option 002/602)	J1-60, 61, 62, 63, 125, 126, 127, 128	J1-57, 58, 59, 123, 124		J1-55, 56, 120, 121, 122		J1-52, 118	
Slot 2 A9 Digital Demodulator (Option 501, 502, 503, 504 with Option 300)	J2-60, 61, 62, 63, 125, 126, 127, 128		J2-57, 58, 59, 123, 124		J2-55, 56, 120, 121, 122		J2-52, 118
Slot 3 A10 I/Q Multiplexer	J3-60, 61, 62, 63, 125, 126, 127, 128		J3-57, 58, 59, 123, 124				
Slot 4 A11 Internal Bit Error Rate Analyzer (Option UN7)	J4-60, 61, 62, 63, 125, 126, 127, 128	J4-57, 58, 59, 123, 124					
Slot 5 A12 CPU	J5-55, 56, 57, 58, 115, 116, 117, 118						
A19 Daughterboard							
A20 Downconverter (Option 501, 502, 503, 504 with Option 300)							
A21 YTO Driver (Option UNJ or Option 506)							
A25 Rear Panel Board or A26 Rear Panel LVDS Board (Option 1EM) and A27 Rear Panel SMB Board (Option 1EM)	J36-1						
AT1 Electronic Attenuator (Option 501, 502, 503, 504 or Option UNJ) or AT1 High-Power Mechanical Attenuator (Option UNB) and A28 Reverse Power Protection (Option 501, 502, 503, 504 or Option UNJ)							
B1 Power Supply Fan							
B2 Small Fan							
B3 Daughterboard/Card Cage Fan							

- a. MB LED means that an LED for this power supply is visible from the top-side of the A23 Motherboard.
- b. The voltage regulator for this power supply is located on the A23 Motherboard.
- c. Self-Test Node means that the voltage for this power supply is measured from the analog bus during Self-Test.

**Table 1-14**

<b>A23 Motherboard test points for:</b>	<b>MB LED -6 VF<sup>ab</sup>  Filtered -6</b>	<b>-15 VF<sup>b</sup>  Filtered -15</b>	<b>MB LED -5.2 VF<sup>ab</sup>  Filtered -5.2</b>	<b>+15 STBY</b>	<b>+15 STBY LCD<sup>c</sup>  Filtered +15.2 STBY</b>	<b>+5.2 VD LCD<sup>b</sup>  Filtered +5.2 VD</b>	<b>Fan +8.5 to +12 Vdc</b>
A2 Display						J32-12	
A3 Front Panel Auxiliary Board					J30-31	J30-32	
A6 Power Supply	J28-38	J28-23	J28-21, 40	J28-34			
Slot 1 <sup>d</sup> A7 Baseband Generator (Option 001/601 or Option 002/602)		J11-3	J11-1, 26				
Slot 2 A9 Digital Demodulator (Option 501, 502, 503, 504 with Option 300)		J12-3	J12-1, 26				
Slot 3 A10 I/Q Multiplexer		J13-3	J13-1, 26				
Slot4 A11 Internal Bit Error Rate Analyzer (Option UN7)		J14-3	J14-1, 26				
Slot 5 A12 CPU	J15-57	J15-43	J15-101	J15-102			
A19 Daughterboard	J10-37, 87	J10-36, 86	J10-35, 85	J10-33			
A20 Downconverter (Option 501, 502, 503, 504 with Option 300)							
A21 YTO Driver (Option UNJ or Option 506)							
A25 Rear Panel Board or A26 Rear Panel LVDS Board (Option 1EM) and A27 Rear Panel SMB Board (Option 1EM)							
AT1 Electronic Attenuator (Option 501, 502, 503, 504 or Option UNJ) or AT1 High-Power Mechanical Attenuator (Option UNB) and A28 Reverse Power Protection (Option 501, 502, 503, 504 or Option UNJ)							
B1 Power Supply Fan							J33-1
B2 Small Fan							J27-1
B3 Daughterboard/Card Cage Fan							J29-1

- a. MB LED means that an LED for this power supply is visible from the top-side of the A23 Motherboard.
- b. The voltage for this power supply is filtered on the A23 Motherboard.
- c. The voltage for this power supply is generated by filtering the +15\_STBY on the A23 Motherboard.
- d. Slot numbers one through five (where one is on the left) refer to the position of an assembly in the top card-cage of the signal generator when viewed from the top.

**Table 1-15**

A23 Motherboard test points for:	+32 VY <sup>a</sup>	+15 VY <sup>a</sup>	+5.2 VY <sup>a</sup>	+3.4 VDY <sup>a</sup>	+5.2 VDY <sup>a</sup>	-15 VY <sup>a</sup>	-5.2 VY <sup>a</sup>
	Filtered +32 VF	Filtered +15 VF	Filtered +5.2 VA	Filtered +3.4 VDF	Filtered +5.2 VD	Filtered -15 VF	Filtered -5.2 VF
A2 Display							
A3 Front Panel Auxiliary Board							
A6 Power Supply							
Slot 1 <sup>b</sup> A7 Baseband Generator (Option 001/601 or Option 002/602)							
Slot 2 A9 Digital Demodulator (Option 501, 502, 503, 504 with Option 300)							
Slot 3 A10 I/Q Multiplexer							
Slot 4 A11 Internal Bit Error Rate Analyzer (Option UN7)							
Slot 5 A12 CPU							
A19 Daughterboard							
A20 Downconverter (Option 501, 502, 503, 504 with Option 300)	J26-14	J26-13	J26-12				
A21 YTO Driver (Option UNJ or Option 506)	J34-55, 57	J34-54, 56	J34-51	J34-45	J34-44	J34-49	J34-50
A25 Rear Panel Board or A26 Rear Panel LVDS Board (Option 1EM) and A27 Rear Panel SMB Board (Option 1EM)							
AT1 Electronic Attenuator (Option 501, 502, 503, 504 or Option UNJ) or AT1 High-Power Mechanical Attenuator (Option UNB) and A28 Reverse Power Protection (Option 501, 502, 503, 504 or Option UNJ)							
B1 Power Supply Fan							
B2 Small Fan							
B3 Daughterboard/Card Cage Fan							

- a. The voltage for this power supply is filtered on the A23 Motherboard.
- b. Slot numbers one through five (where one is on the left) refer to the position of an assembly in the top card-cage of the signal generator when viewed from the top.

**If the Amber Standby LED is Not Working**

With the power turned off and the signal generator plugged in, the front panel amber standby LED should be on. The amber Standby LED is controlled by a +15 Vdc standby line supplied by the power supply, which is routed through the A23 Motherboard to the front panel.

To troubleshoot the +15 Vdc Standby line, turn the signal generator off and follow the signal path. Refer to [Table 1-12](#), [Table 1-13](#), [Table 1-14](#), and [Table 1-15](#).

**If the Green Line Power LED is Not Working**

When the power switch is turned on, the amber LED goes out and the green LED comes on. The fans start rotating and the front panel display becomes illuminated.

To troubleshoot, refer to [Table 1-12](#), [Table 1-13](#), [Table 1-14](#), and [Table 1-15](#) and verify the power supply voltages at J28 of the A23 Motherboard. If the supply voltages are correct, replace the A3 Front Panel Auxiliary Board. (Refer to “[A3 Front Panel Auxiliary Board](#)” on page 2-17.)

**If the Fans are Not Working**

There are three fans located near the front of the instrument that are connected to the A23 Motherboard. The fan control voltages are supplied by the A6 Power Supply and may be measured at J28 of the A23 Motherboard. The fan control voltage range is +8.5 Vdc (at 23 degrees C) to +12.0 Vdc (at 55 degrees C). At higher temperatures, the fan voltage increases along with the rotation of the fan.

To troubleshoot, disconnect each fan (one at a time) from the A23 Motherboard and check the fan voltage. Refer to [Table 1-12](#), [Table 1-13](#), [Table 1-14](#), and [Table 1-15](#). If the fan voltage is correct, replace the fan. (Refer to “[B1 Power Supply Fan](#)” on page 2-76, “[B2 Small Fan](#)” on page 2-78, or “[B3 Daughterboard/Card Cage Fan](#)” on page 2-80.)

**If the Signal Generator Does Not Power-on and the Power Supply LEDs are Not Working**

Each of the power supplies has an LED located on the bottom of the A23 Motherboard. If the power supply is functioning, the green LED is on. Use a voltmeter to measure the supplies on the A23 Motherboard to ensure the voltages meet the power supply specifications in the following table.

With the exceptions of the +15 Vdc and the +15 Vdc Standby power supply, each power supply should have a maximum of 10 mV<sub>p-p</sub> ripple. The +15 Vdc and the +15 Vdc Standby power supply have a maximum ripple specification of 20 mV<sub>p-p</sub>. The ripple can be measured using an oscilloscope. If any of the supplies LEDs are not on or the measured voltage is less than the acceptable value, one of the assemblies may be loading down the power supply. To find where each power supply is used, refer to [Table 1-12](#), [Table 1-13](#), [Table 1-14](#), and [Table 1-15](#).

To troubleshoot and isolate a defective assembly, turn power off to the signal generator and remove one of the assemblies being biased by the faulty power supply.

Power Supply	Acceptable Voltage
+32 Vdc	32.0 ± 0.96 Vdc
+15 Vdc	15.0 ± 0.45 Vdc
+15 Vdc Standby	15.0 ± 0.45 Vdc
+9.0 Vdc	9.0 ± 0.27 Vdc
+5.2 Vdc	5.2 ± 0.16 Vdc
+3.3 Vdc	3.3 ± 0.10 Vdc
-6.0 Vdc	-6.0 ± 0.18 Vdc
-15 Vdc	-15.0 ± 0.45 Vdc

---

**NOTE** It is important to have a minimum number of assemblies removed at one time. The power supply expects to see a minimum load, otherwise the power supply voltage will increase until an over-voltage condition exists.

---

Turn power back on to the signal generator and check the suspected faulty power supply. If the condition is still bad, turn power off to the signal generator and re-install the first assembly that was removed. Remove one of the other assemblies being

biased by the faulty power supply and see if the problem is alleviated. Continue this process until the power supply is functioning properly. Replace the last assembly that was removed.

If the Front Panel Power Switch is Not Functioning

When the front-panel power switch is switched on, the control line (ON/OFF) from the A1 Keyboard is momentarily grounded and the A6 Power Supply is enabled. The ON/OFF signal is routed from the A1 Keyboard through J32-20 to the A23 Motherboard. Circuitry on the A23 Motherboard uses the ON/OFF signal to generate the Pwr\_On\_L. This line is routed to the A6 Power Supply and the A12 CPU; the A6 Power Supply is disabled when the Pwr\_On\_L line is a TTL high or open.

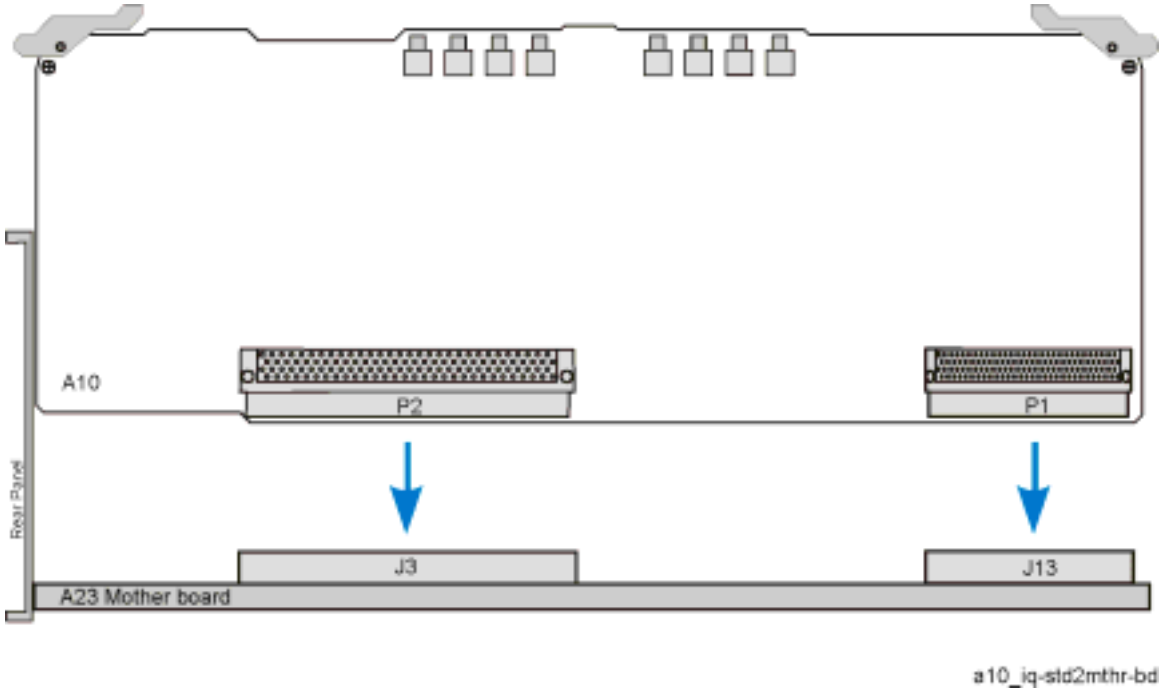
To troubleshoot

1. Measure J28-34 with a DVM to assure that the +15 Vdc Standby voltage (+15 STBY) is correct.
  - If the +15 Vdc Standby voltage is good, use the DVM to measure J32-20 (ON/OFF). The DVM should read approximately +5 Vdc.
    - a. While monitoring the DVM, depress and hold the front panel power switch. This grounds the ON/OFF signal and the DVM should read less than a few millivolts.
    - b. Release the front panel power switch. The ON/OFF control signal should again be approximately +5 Vdc.
2. With the front panel power switch off, the Pwr\_On\_L line at J28-32 of the A23 Motherboard should be at approximately +5.8 Vdc; this signal is generated on the A23 Motherboard.
  - If the voltage is less than +3.5 Vdc, remove the A6 Power Supply and ohm the connection from J28-32 to ground; the impedance should be  $\geq 1$  Megaohm.
    - If the impedance is correct, replace the A6 Power Supply.
3. When the front panel power switch is turned on, the Pwr\_On\_L line goes to  $< +50$  mV.
  - If the A1 Keyboard is functioning properly and the Pwr\_On\_L line is not, replace the A23 Motherboard.
  - If the Pwr\_On\_L line is functioning properly, replace the A6 Power Supply.

**Self-Tests 1xx for the A10 I/Q Multiplexer**

**NOTE** These self-test errors apply to the A10 I/Q Multiplexer (Option 506) and the A10 I/Q Multiplexer (Option 501, 502, 503, 504 or Option UNJ).

**Figure 1-12**



**Table 1-16**

Power Supply Voltage (Vdc)	Connector Pins on the A23 Motherboard	Minimum Value (Vdc)	Maximum Value (Vdc)	Origin
+15	J13-28	14.55	15.45	Main Supply
-15	J13-3	-15.45	-14.55	Main Supply
+5.2	J13-2, 27	5.044	5.356	Main Supply
-5.2	J13-1, 26	-5.356	-5.044	Main Supply
+5 VD	J3-64, 65, 129, 130	5.044	5.356	Main Supply

**100 Power Supply**

- If the power supply voltages are good on the connector pins indicated:
  - a. Remove the A10 I/Q Multiplexer and ohm the connection between the appropriate pins of J3/J13 (A23 Motherboard side) and sockets of J3/J13 (A10 I/Q Multiplexer side).
  - b. If the connection is good, replace the A10 I/Q Multiplexer.  
(Refer to “A10 I/Q Multiplexer” on page 2-30.)
  - c. If the connection is bad, replace the A23 Motherboard.  
(Refer to “A23 Motherboard” on page 2-57.)
- If the supply voltages are bad on any of the connector pins indicate:
  - a. Check the origin of the power supply.
  - b. If the power supply voltages are good at the origin, replace the A23 Motherboard.  
(Refer to “A23 Motherboard” on page 2-57.)
  - c. If the power supply voltages are bad at the origin, troubleshoot the problem using the assembly’s troubleshooting procedure.

**101 Cal Source**

- Replace the A10 I/Q Multiplexer.

**102 ExtIn Offset DAC and Switch**

- Replace the A10 I/Q Multiplexer.

**103 Path1&2 and ExtOut**

- Replace the A10 I/Q Multiplexer.

**104 Path1 Atten+Summing Amp**

- Replace the A10 I/Q Multiplexer.

**105 Path2 Atten+Summing Amp**

- Replace the A10 I/Q Multiplexer.

**106 DC-Coupled Mode Test**

- Replace the A10 I/Q Multiplexer.

**107 Drift Canceller Test**

- Replace the A10 I/Q Multiplexer.

**108 IQ Polarity Switch**

- Replace the A10 I/Q Multiplexer.

**109 RMS to DC Conv Test**

- Replace the A10 I/Q Multiplexer.



Self-Tests 2xx for the A15 Sampler (Option UNJ or Option 506)

Figure 1-13

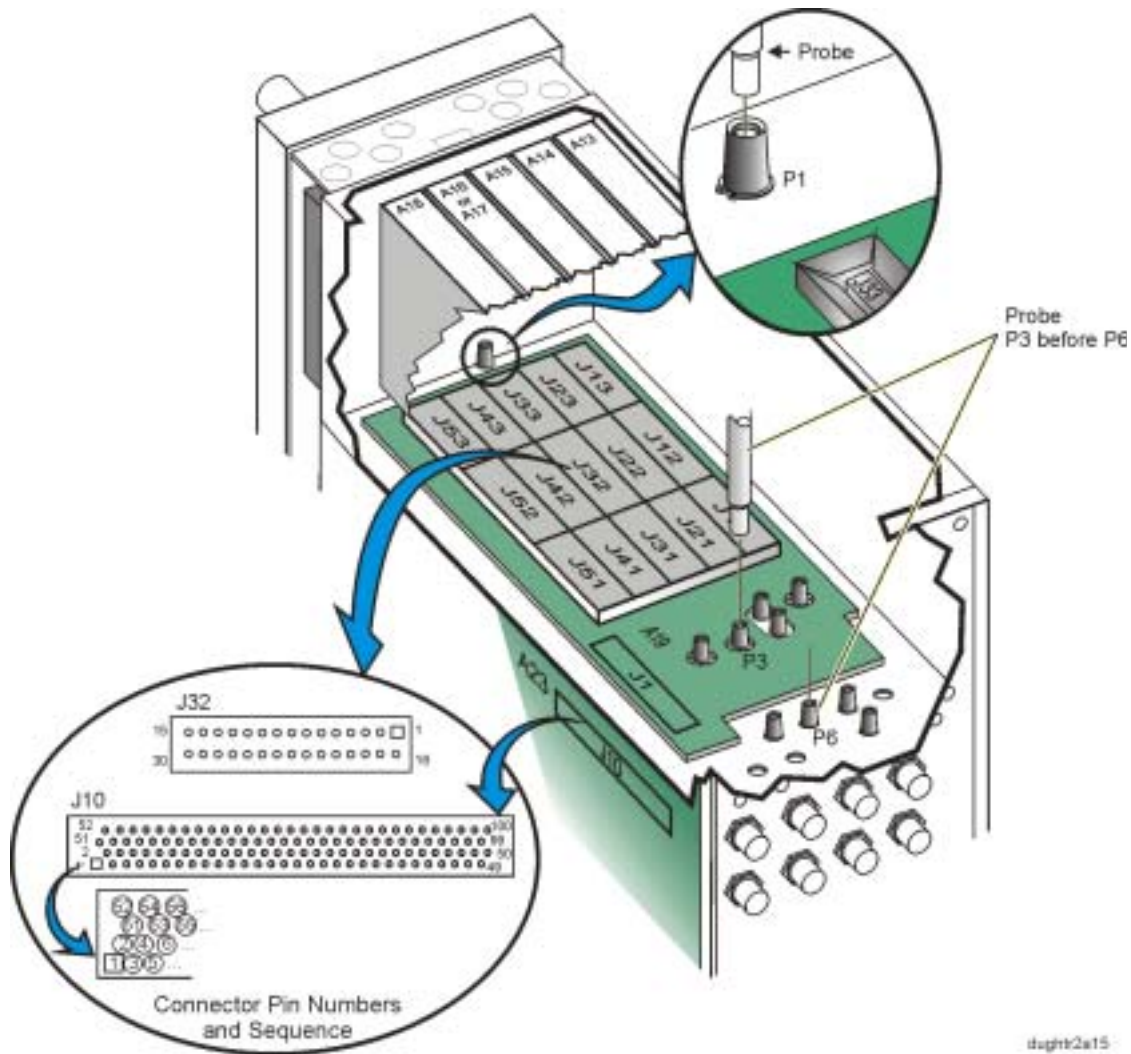


Table 1-17

Power Supply Voltage (Vdc)	Connector Pins on the		Minimum Value (Vdc)	Maximum Value (Vdc)	Origin
	A19 Daughterboard	A23 Motherboard			
+32	J32-7	J10-83	+31.04	+32.96	Main Supply
+15	J32-6, 21	J10-32, 82	+14.1	+15.9	Main Supply
-15	J32-2, 17	J10-36, 86	-15.9	-14.1	Main Supply
+9	J32-5, 20	J10-34, 84	+8.73	+9.27	Main Supply
+5.2 VD	J32-14, 29	J10-35, 85	+4.68	+5.72	Main Supply

## 200 Power Supply

- If the supply voltages are good on the J10 connector pins indicated:
  - a. Turn power off to the signal generator.
  - b. Remove the A15 Sampler (Option UNJ or Option 506) and ohm the connection between the appropriate J32 and J10 pins.
    - If the connection is good, replace the A15 Sampler (Option UNJ or Option 506). (Refer to “[A15 Sampler \(Option UNJ or Option 506\)](#)” on page 2-41.)
    - If the connection is bad, replace the A19 Daughterboard (Refer to “[A19 Daughterboard](#)” on page 2-49).
- If the supply voltages are bad on the J10 connector pins indicated:
  - a. Check the origin of the supply.
    - If the supply voltages are good at the origin, replace the A23 Motherboard. (Refer to “[A23 Motherboard](#)” on page 2-57.)
    - If the supply voltages are bad at the origin, troubleshoot the problem using that assembly's troubleshooting procedure.

## 201 Tuning + Bias Test

---

**CAUTION** Do not install the A18 Reference or the A15 Sampler (Option UNJ or Option 506) with the signal generator powered up or serious damage to the signal generator may be caused.

---

- With the signal generator powered up, carefully remove the A15 Sampler (Option UNJ or Option 506) from the signal generator.
- Connect a semi-rigid cable-extender probe to P1 and connect a signal analyzer to the probe.

P1 delivers a 1 GHz signal from J3 of the A18 Reference to J1 of the A15 Sampler (Option UNJ or Option 506) through W44.
- Check for a 1 GHz signal with a power level  $\geq 0$  dBm.
  - If the signal is present and at the correct power, replace the A15 Sampler (Option UNJ or Option 506) (Refer to “[A15 Sampler \(Option UNJ or Option 506\)](#)” on page 2-41.)
  - If the signal is not present or at the correct power:
    - a. Turn power off to the signal generator.
    - b. Remove the A18 Reference.
    - c. Ohm the center pin of W44; it should measure as a short.
    - d. Ohm between the center pin and the shield of W44; it should measure as an open.
      - If the cable measures good (as defined in the previous two steps), refer to the A18 Reference troubleshooting procedure.
      - If the cable measures bad, replace the cable.

## 203 YO Loop Detector

---

**CAUTION** Do not install the A13 Output with the signal generator powered up or serious damage to the signal generator may be caused.

---

- With the signal generator powered up, carefully remove the A15 Sampler (Option UNJ or Option 506) from the signal generator.
- Set the signal generator to a frequency of 625 MHz.
- Connect a semi-rigid cable-extender probe to P3 and connect a signal analyzer to the probe.

P3 delivers the YTO output signal from J8 of the A21 YTO Driver (Option UNJ or Option 506) to J3 of the A15 Sampler (Option UNJ or Option 506) through W37.

- Check for a 5 GHz ( $\pm 5$  MHz) signal at a power level  $> -10$  dBm.
  - If the signal is present and at the correct power:
    - a. Connect a semi-rigid cable-extender probe to P6 and connect a signal analyzer to the probe.

P6 delivers the VCO output signal from J2 of the A16 Frac-N (Option UNJ or Option 506) to J6 of the A15 Sampler (Option UNJ or Option 506) through W11.
    - b. Set the signal generator to a frequency of 625 MHz.
    - c. Check for an 859.375 MHz signal at a power level  $\geq +5$  dBm.
      - If the signal is good, replace the A15 Sampler (Option UNJ or Option 506). (Refer to [“A15 Sampler \(Option UNJ or Option 506\)”](#) on page 2-41.)
      - If the signal is bad, refer to the A16 Frac-N (Option UNJ or Option 506) troubleshooting procedure.
  - If the signal is not present or at the correct power, refer to the A21 YTO Driver (Option UNJ or Option 506) troubleshooting procedure.

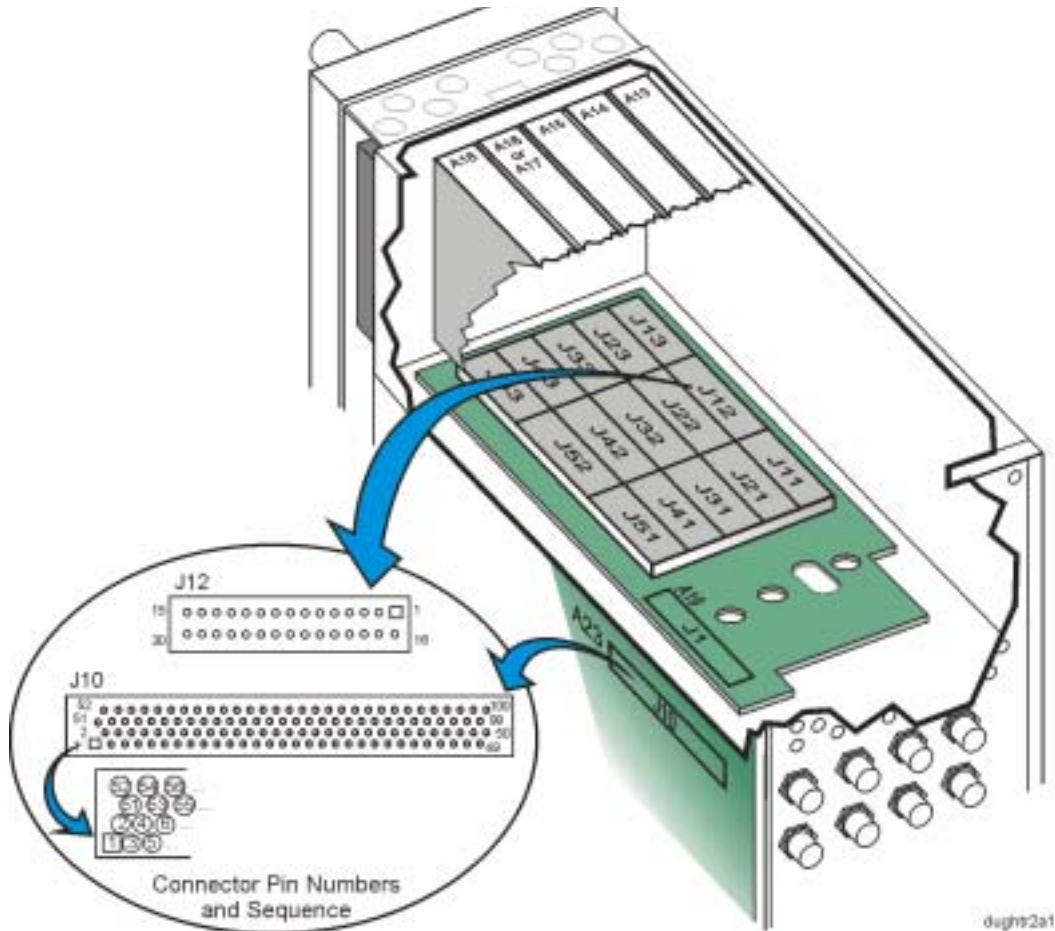
**Self-Tests 4xx for the A13 Output**

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**NOTE** These self-test errors apply to the A13 Output (Option 501, 502, 503, 504 or Option UNJ) and A13 Output (Option UNB).

---

**Figure 1-14**



**Table 1-18**

Power Supply Voltage (Vdc)	Connector Pins on the		Minimum Value (Vdc)	Maximum Value (Vdc)	Origin
	A19 Daughterboard	A23 Motherboard			
+15	J12-6, 21	J10-32, 82	+14.1	+15.9	Main Supply
-15	J12-2, 17	J10-36, 86	-15.9	-14.1	Main Supply
+5.2	J12-14, 29	J10-30, 31, 80, 81	+4.68	+5.72	Main Supply
+9	J12-5, 20	J10-34, 84	+8.73	+9.27	Main Supply
-6	J12-3, 18	J10-37, 87	-6.36	-5.64	Main Supply
-5.2	J12-4, 19	J10-35, 85	-5.356	-5.044	Main Supply

#### 400 Ground and PTAT Test

- Using a DVM, measure the voltages on J10 (the PTAT voltage is derived from the  $\pm 15$  Vdc):
  - If the supply voltages are good on the J10 connector pins indicated:
    - a. Turn off the power and remove the A13 Output.
    - b. Ohm the connection between the appropriate pins of J12 and J10.
      - If the connection is good, replace the A13 Output.  
(Refer to “A13 Output” on page 2-37.)
      - If the connection is bad, replace the A19 Daughterboard.  
(Refer to “A19 Daughterboard” on page 2-49.)
  - If the supply voltages are bad on any of the J10 connector pins indicated:
    - a. Check the origin of the supply.
      - If the supply voltages are good at the origin, replace the A23 Motherboard.  
(Refer to “A23 Motherboard” on page 2-57.)
      - If the supply voltages are bad at the origin, troubleshoot the problem using that assembly's troubleshooting procedure.

#### 401 Prelevel Loop Test

---

**CAUTION** Do not install the A13 Output with the signal generator powered up or serious damage to the signal generator may be caused.

---

- With the signal generator powered up, carefully remove the A13 Output from the signal generator.
- Set the signal generator to a frequency of 900 MHz.
- At J4 of the A13 Output, connect a spectrum analyzer to the A16 Frac-N (Option UNJ or Option 506) or A17 Synthesizer (Option 501, 502, 503, 504) RF output and measure the signal; it should be 900 MHz at an amplitude  $\geq 5$  dBm.
  - If the signal is not present or at the correct amplitude, refer to the A16 Frac-N (Option UNJ or Option 506) or A17 Synthesizer (Option 501, 502, 503, 504) troubleshooting.
  - If the signal is present, replace the A13 Output.  
(Refer to “A13 Output” on page 2-37.)

#### 402 Quadrature DAC Test

- Replace the A13 Output.

#### 403 VBLO Test

- Replace the A13 Output.

**404 IQ Test**

- Measure the Q signal from the A10 I/Q Multiplexer.
  - a. Disconnect the cable from J3 of the A19 Daughterboard and connect it to a DVM.
  - b. Set the CW frequency to 2 GHz.
  - c. Turn on the I/Q modulation and adjust the Q offset.
  - d. Press the following keys:
    - **I/Q hardkey / I/Q On / I/Q Adjustments / I/Q Adjustments On / Q Offset**
  - e. Rotate the RPG and observe the DVM. The voltage should vary from approximately +0.25 Vdc for an offset of 50% to approximately -0.25 Vdc for an offset of -50%.
    - If the signal is bad, check it at J10 of the A10 I/Q Multiplexer.
      - If the signal is bad at J10, refer to A10 I/Q Multiplexer troubleshooting.
      - If the signal is good at J10, change the cable.
- Measure the I signal from the A10 I/Q Multiplexer.
  - a. Disconnect the cable from J2 of the A19 Daughterboard and connect it to a DVM.
  - b. Set the CW frequency to 2 GHz.
  - c. Turn on the I/Q modulation and adjust the I offset.
  - d. Press the following keys:
    - **I/Q hardkey / I/Q On / I/Q Adjustments / I/Q Adjustments On / I Offset**
  - e. Rotate the RPG and observe the DVM. The voltage should vary from approximately +0.25 Vdc for an offset of 50% to approximately -0.25 Vdc for an offset of -50%.
    - If the signal is bad, check it at J9 of the A10 I/Q Multiplexer.
      - If the signal is bad at J9, refer to A10 I/Q Multiplexer troubleshooting.
      - If the signal is good at J9, change the cable.
- If the I and Q signals are good, replace the A13 Output.  
(Refer to “[A13 Output](#)” on page 2-37.)

**405 Gain Adjustment Test**

- Replace the A13 Output.

**406 Prelevel Bypass Test**

- Replace the A13 Output.

**407 Switched filters Test**

- Replace the A13 Output.

**408 ALC Reference Test**

- Replace the A13 Output.

#### 409 ALC Modulation Test

- Replace the A13 Output.

#### 410 Burst Modulation Test

- Replace the A13 Output.

#### 411 ALC Detector Test

- Replace the A13 Output.

#### 412 Het Band Test

- Measure the 1 GHz signal from the A18 Reference.
  - a. With the power off, remove the A13 Output and insert a ribbon cable extender into J11 of the A19 Daughterboard.
  - b. Power on the signal generator and tune to a CW frequency of 200 MHz.
  - c. Using a spectrum analyzer, probe pin 19 of the ribbon cable extender<sup>1</sup> for a 1 GHz signal with an amplitude > -20 dBm:
    - If the signal is present, replace the A13 Output.  
(Refer to “A13 Output” on page 2-37.)
    - If the signal is not present, power down the signal generator and remove the A18 Reference.  
(Refer to “A18 Reference” on page 2-47.)
  - d. Using a DVM, ohm between J11-19 and J51-19:
    - If the path is open, replace the A19 Daughterboard.  
(Refer to “A19 Daughterboard” on page 2-49.)
    - If the path is good (zero ohms), refer to the A18 Reference troubleshooting for the 1 GHz VCO.

#### 413 Bulk R DAC Test

- Replace the A13 Output.

#### 414 Log Offset Test

- Replace the A13 Output.

---

1. The ribbon cable extender needs to be assembled by the user. It consists of two connector plugs (part number 1252-1010) and approximately eight inches of ribbon cable (part number 8120-2226). The ribbon cable contains 26 wires; six of these wires need to be stripped off before being connected to the plugs.

Self-Tests 5xx for the A17 Synthesizer (Option 501, 502, 503, 504)

Figure 1-15

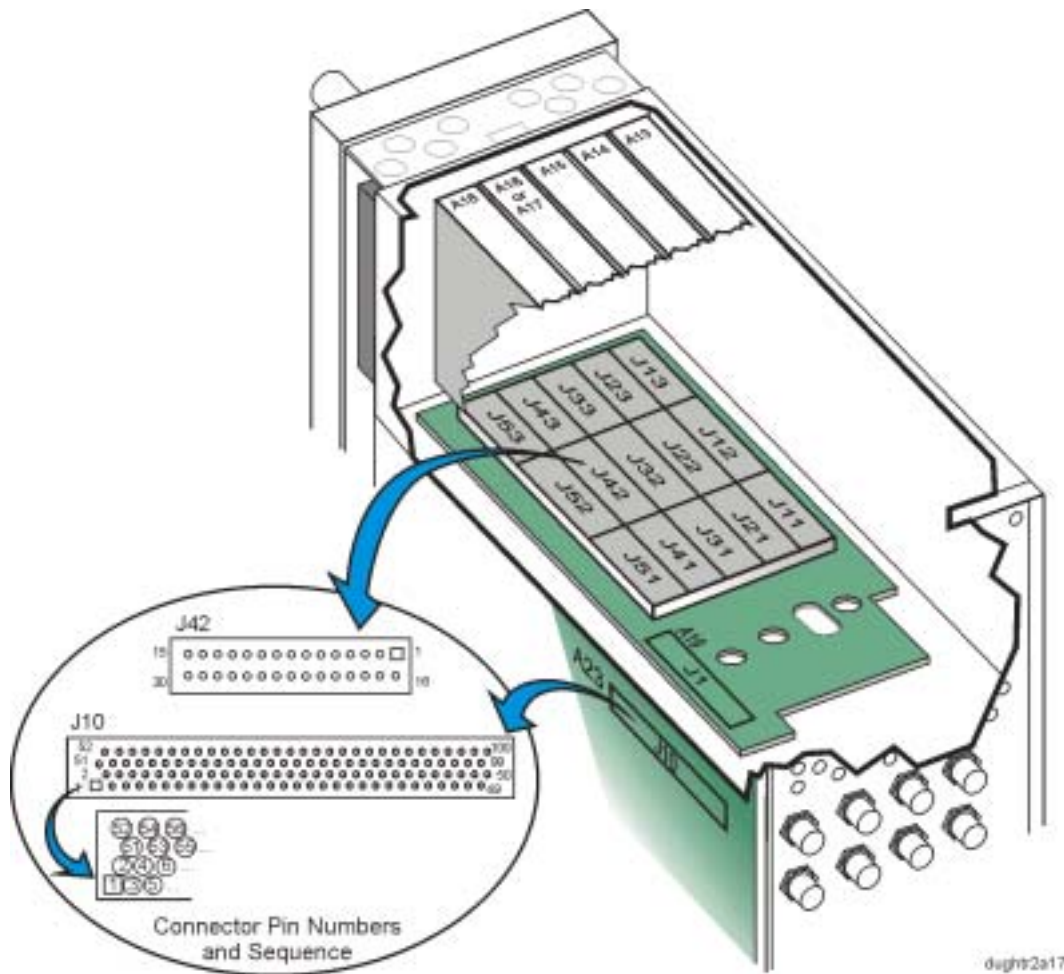


Table 1-19

Power Supply Voltage (Vdc)	Connector Pins on the		Minimum Value (Vdc)	Maximum Value (Vdc)	Origin
	A19 Daughterboard	A23 Motherboard			
+32	J42-7	J10-83	+31.04	+32.96	Main Supply
+15	J42-6, 21	J10-32, 82	+14.1	+15.9	Main Supply
-15	J42-2, 17	J10-36, 86	-15.9	-14.1	Main Supply
+9	J42-5, 20	J10-34, 84	+8.73	+9.27	Main Supply
-6	J42-3, 18	J10-37, 87	-6.36	-5.64	Main Supply
-5.2	J42-4, 19	J10-35, 85	-5.356	-5.044	Main Supply
+5.2	J42-14, 29	J10-30, 31, 80, 81	+4.68	+5.72	Main Supply



### 500 Power Supply

- If the  $\pm 15$  V and +5.2 V supplies are good on the connector pins indicated:
  - a. Turn power off, remove the A17 Synthesizer (Option 501, 502, 503, 504) and ohm the connection between the appropriate J42 and J10 pins.
    - If the connection is good, replace the A17 Synthesizer (Option 501, 502, 503, 504).  
(Refer to “[A17 Synthesizer \(Option 501, 502, 503, 504\)](#)” on page 2-45.)
    - If the connection is bad, replace the A19 Daughterboard.  
(Refer to “[A19 Daughterboard](#)” on page 2-49.)
- If the  $\pm 15$  V and +5.2 V supplies are bad on the connector pins indicated:
  - a. Check the origin of the supply.
    - If the supply voltage is good at the origin, replace the A23 Motherboard.  
(Refer to “[A23 Motherboard](#)” on page 2-57.)
    - If the supply voltage is bad at the origin, troubleshoot the problem using that assembly's troubleshooting procedure.

### 501 Loop Gain

- Replace the A17 Synthesizer (Option 501, 502, 503, 504).

### 502 Tuning Voltage

- If the +32 V and +9 V supplies are good on the connector pins indicated:
  - a. Turn power off, remove the A17 Synthesizer (Option 501, 502, 503, 504), and ohm the connection between the appropriate J42 and J10 pins.
    - If the connection is good:
      - a. Re-insert the A17 Synthesizer (Option 501, 502, 503, 504).
      - b. Turn power on and check the 10 MHz signal on the A23 Motherboard at J10 pin 99 using an oscilloscope.  
  
The signal should have a period of 100 ns and an amplitude of greater than 1.2 Vp-p.
        - If the signal is good, replace the A17 Synthesizer (Option 501, 502, 503, 504).  
(Refer to “[A17 Synthesizer \(Option 501, 502, 503, 504\)](#)” on page 2-45.)
        - If the signal is bad, troubleshoot the A18 Reference.  
(Refer to “[A18 Reference](#)” on page 2-47.)
    - If the connection is bad replace the A19 Daughterboard.  
(Refer to “[A19 Daughterboard](#)” on page 2-49.)
- If the +32 V and +9 V supplies are bad on the connector pins indicated:
  - a. Check the origin of the supply.
    - If the supply voltage is good at the origin, replace the A23 Motherboard.  
(Refer to “[A23 Motherboard](#)” on page 2-57.)
    - If the supply voltage is bad at the origin, troubleshoot the problem using that assembly's troubleshooting procedure.

**503 RF Detector**

- If the -6 V supply is good on the connector pins indicated:
  - a. Turn power off, remove the A17 Synthesizer (Option 501, 502, 503, 504) and ohm the connection between the appropriate J52 and J10 pins.
    - If the connection is good, replace the A17 Synthesizer (Option 501, 502, 503, 504).  
(Refer to [“A17 Synthesizer \(Option 501, 502, 503, 504\)”](#) on page 2-45.)
    - If the connection is bad, replace the A19 Daughterboard.  
(Refer to [“A19 Daughterboard”](#) on page 2-49.)
- If the -6 V supply is bad on the connector pins indicated:
  - a. Check the origin of the supply.
    - If the supply voltage is good at the origin, replace the A23 Motherboard.  
(Refer to [“A23 Motherboard”](#) on page 2-57.)
    - If the supply voltage is bad at the origin, troubleshoot the problem using that assembly's troubleshooting procedure.

**504 FM Inversion**

- Replace the A17 Synthesizer (Option 501, 502, 503, 504).

Self-Tests 6xx for the A21 YTO Driver (Option UNJ or Option 506)

Figure 1-16

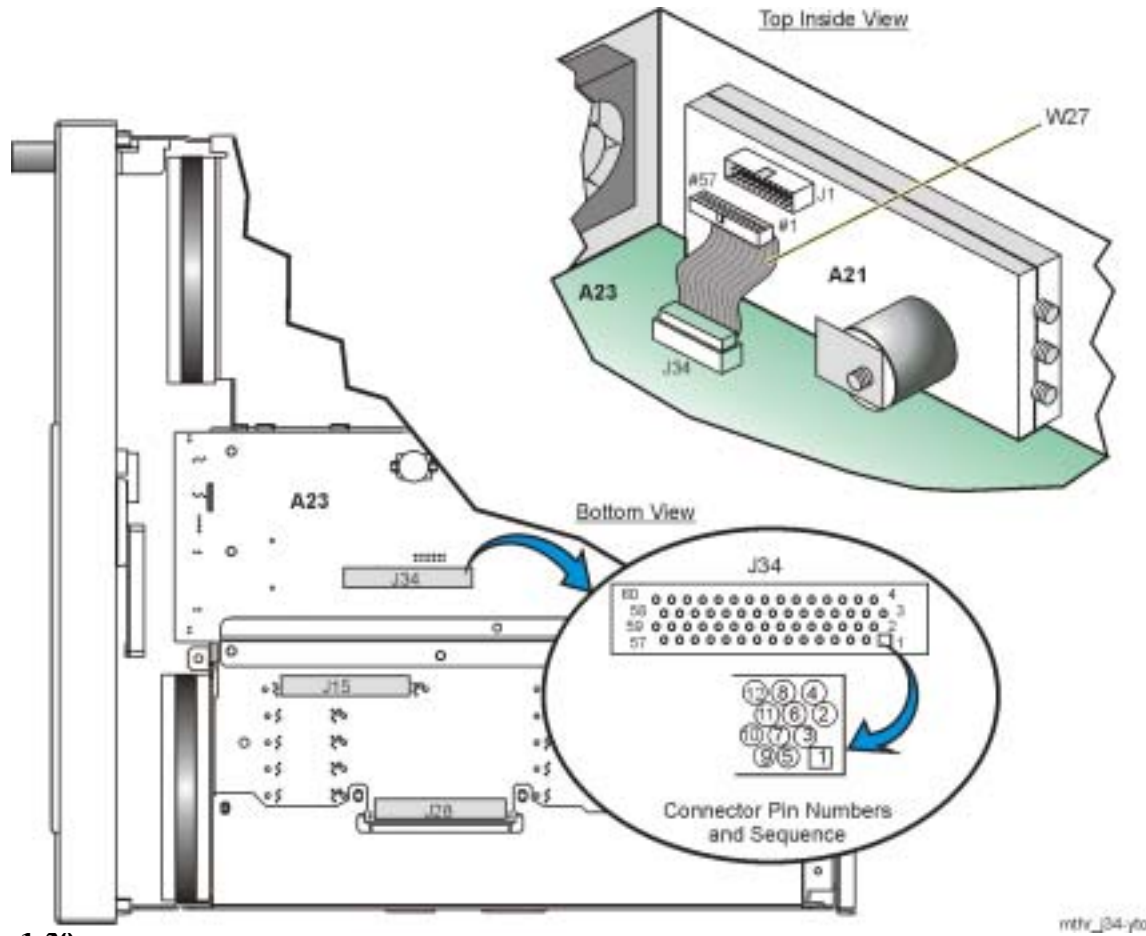


Table 1-20

Power Supply Voltage (Vdc)	Connector Pins on the		Minimum Value (Vdc)	Maximum Value (Vdc)	Origin
	A21 YTO Driver (Option UNJ or Option 506)	A23 Motherboard			
+32	J1-55, 57	J34-55, 57	+31.04	+32.96	Main Supply
+15	J1-54, 56	J34-54, 56	+14.1	+15.9	Main Supply
-15	J1-49	J34-49	-15.9	-14.1	Main Supply
-5.2	J1-50	J34-50	-5.356	-5.044	Main Supply
+5.2	J1-51	J34-51	+4.68	+5.72	Main Supply
+5.2 VD	J1-44	J34-44	+4.68	+5.72	Main Supply
+3.4 VDY	J1-45	J34-45	+3.298	+3.502	Main Supply

### 600 Post Regulators

- If the +5.2 VD, +3.4 VDY, and  $\pm 15$  V supplies are good on the J34 connector pins indicated:
  - a. Remove the W27 ribbon cable from J1 of the A21 YTO Driver (Option UNJ or Option 506).
  - b. Measure the +5.2 VD, +3.4 VDY, and  $\pm 15$  V on the appropriate pins of the W27 ribbon cable.
    - If the voltages are good, replace the A21 YTO Driver (Option UNJ or Option 506). (Refer to [“A21 YTO Driver \(Option UNJ or Option 506\) and A22 Coupler \(Option 506\)”](#) on page 2-55.)
    - If the voltages are bad:
      - a. Remove the W27 ribbon cable from the J34 connector on the A23 Motherboard.
      - b. On W27, ohm the appropriate pins from one end of the cable to the other.
        - If the pins on W27 measure good (indicated as zero ohms), replace the A23 Motherboard. (Refer to [“A23 Motherboard”](#) on page 2-57.)
        - If the pins on W27 measure bad (indicated as infinite ohms), replace the W27 cable.
- If the +5.2 VD, +3.4 VDY, and  $\pm 15$  V supplies are bad on the J34 connector pins indicated:
  - a. Check the origin of the supply.
    - If the supply voltage is good at the origin, replace the A23 Motherboard. (Refer to [“A23 Motherboard”](#) on page 2-57.)
    - If the supply voltage is bad at the origin, troubleshoot the problem using that assembly's troubleshooting procedure.

### 601 DACs Test

- If the +32 V, +5.2 V, and  $-5.2$  V supplies are good on the J34 connector pins indicated:
  - a. Remove the W27 ribbon cable from J1 of the A21 YTO Driver (Option UNJ or Option 506).
  - b. Measure the +32 V, +5.2 V, and  $-5.2$  V supplies on the appropriate pins of the W27 ribbon cable.
    - If the voltages are good, replace the A21 YTO Driver (Option UNJ or Option 506). (Refer to [“A21 YTO Driver \(Option UNJ or Option 506\) and A22 Coupler \(Option 506\)”](#) on page 2-55.)
    - If the voltages are bad:
      - a. Remove the W27 ribbon cable from the J34 connector on the A23 Motherboard.
      - b. On W27, ohm the appropriate pins from one end of the cable to the other.
        - If the pins on W27 measure good (indicated as zero ohms), replace the A23 Motherboard. (Refer to [“A23 Motherboard”](#) on page 2-57.)
        - If the pins on W27 measure bad (indicated as infinite ohms), replace the W27 cable.
- If the +32 V, +5.2 V, and  $-5.2$  V supplies are bad on the J34 connector pins indicated:
  - a. Check the origin of the supply.
    - If the supply voltage is good at the origin, replace the A23 Motherboard. (Refer to [“A23 Motherboard”](#) on page 2-57.)
    - If the supply voltage is bad, troubleshoot the problem using that assembly's procedure.

### 602 PLL Interface

- Check for A15 Sampler (Option UNJ or Option 506) YO phase lock interference.
  - a. Disconnect the cable from J4 of the A21 YTO Driver (Option UNJ or Option 506).
    - If test 602 still fails, replace the A21 YTO Driver (Option UNJ or Option 506).  
(Refer to “[A21 YTO Driver \(Option UNJ or Option 506\)](#) and [A22 Coupler \(Option 506\)](#)” on page 2-55.)
    - If test 602 passes, troubleshoot the A15 Sampler (Option UNJ or Option 506).  
(Refer to “[A15 Sampler \(Option UNJ or Option 506\)](#)” on page 2-41.)

### 603 FM Driver

- Measure the FM modulation signal from the A16 Frac-N (Option UNJ or Option 506).
  - a. Disconnect the cable at J3 of the A21 YTO Driver (Option UNJ or Option 506).
  - b. Select the FM/φM menu.
  - c. Select the following:
    - FM mode
    - FM Path 1
    - FM Dev 100 kHz
    - FM Source (Internal)
    - FM Rate 400 Hz
    - More (1 of 2) >> FM Waveform (Square)
    - FM On
  - d. Select Mode On/Off to On.
  - e. Use an oscilloscope and probe the center pin of W28 (the cable that connects to J3 of A21 YTO Driver (Option UNJ or Option 506)) to measure a 400 Hz square-wave signal with an amplitude > 2 Vp-p.
    - If the signal is good, replace the A21 YTO Driver (Option UNJ or Option 506).  
(Refer to “[A21 YTO Driver \(Option UNJ or Option 506\)](#) and [A22 Coupler \(Option 506\)](#)” on page 2-55.)
    - If the signal is bad, turn power off and remove the A15 Sampler (Option UNJ or Option 506) and A16 Frac-N (Option UNJ or Option 506).
      - a. Ohm the center pin connection between W28 and J14 of the A19 Daughterboard.  
  
This should be a short.
      - b. Ohm the connection between the center pin of W28 and ground.  
  
This should be an open.
        - If the connections are bad, check the W28 cable.
        - If the W28 cable is good, replace the A19 Daughterboard.  
(Refer to “[A19 Daughterboard](#)” on page 2-49.)
        - If the connections are good, troubleshoot the A16 Frac-N (Option UNJ or Option 506).

**Self-Tests 7xx for the A16 Frac-N (Option UNJ or Option 506)**

Figure 1-17

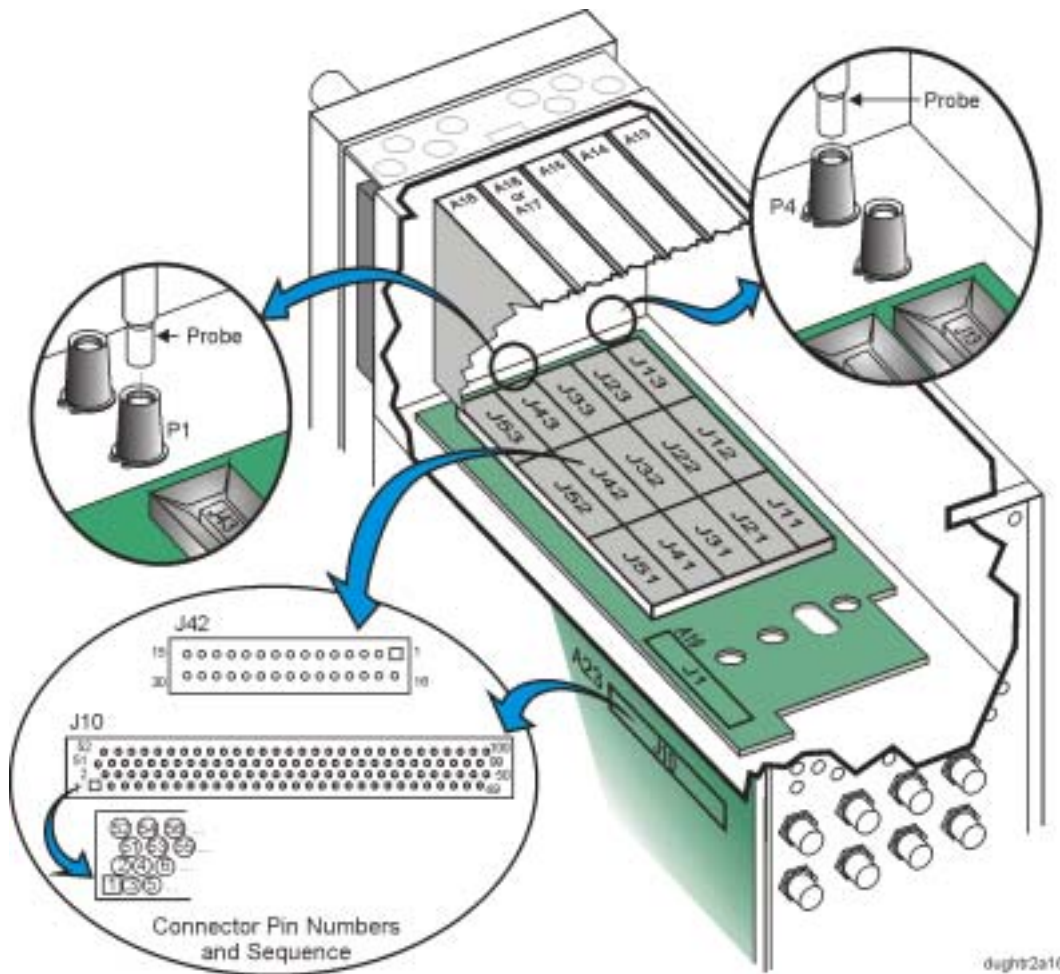


Table 1-21

Power Supply Voltage (Vdc)	Connector Pins on the		Minimum Value (Vdc)	Maximum Value (Vdc)	Origin
	A19 Daughterboard	A23 Motherboard			
+32	J42-7	J10-83	+31.04	+32.96	Main Supply
+15	J42-6, 21	J10-32, 82	+14.1	+15.9	Main Supply
-15	J42-2, 17	J10-36, 86	-15.9	-14.1	Main Supply
+9	J42-5, 20	J10-34, 84	+8.73	+9.27	Main Supply
-6	J42-3, 18	J10-37, 87	-5.64	-6.36	Main Supply
-5.2	J42-4, 19	J10-35, 85	-5.356	-5.044	Main Supply
+5.2	J42-14, 29	J10-30, 31, 80, 81	+4.68	+5.72	Main Supply

## 700 Power Supply

- If the +5.2 V, +15 V, and –15 V supplies are good on the connector pins indicated:
  - a. Turn power off to the signal generator.
  - b. Remove the A16 Frac-N (Option UNJ or Option 506) and ohm the connection between the appropriate J42 and J10 pins.
    - If the connection is good, replace the A16 Frac-N (Option UNJ or Option 506). (Refer to “[A16 Frac-N \(Option UNJ or Option 506\)](#)” on page 2-43.)
    - If the connection is bad, replace the A19 Daughterboard. (Refer to “[A19 Daughterboard](#)” on page 2-49.)
- If the +5.2 V, +15 V, and –15 V supplies are bad on the connector pins indicated:
  - a. Check the origin of the supply.
    - If the supply voltage is good at the origin, replace the A23 Motherboard. (Refer to “[A23 Motherboard](#)” on page 2-57.)
    - If the supply voltage is bad at the origin, troubleshoot the problem using that assembly's troubleshooting procedure.

## 701 Loop Gain

- Replace the A16 Frac-N (Option UNJ or Option 506).

## 702 Tuning Voltage

- If the +32 V, –5.2 V, and +9 V supplies are good on the connector pins indicated:
  - a. Turn power off to the signal generator.
  - b. Remove the A16 Frac-N (Option UNJ or Option 506) and ohm the connection between the appropriate J42 and J10 pins.
    - If the connection is good:
      - a. Re-insert the A16 Frac-N (Option UNJ or Option 506).
      - b. Turn power on to the signal generator.
      - c. Check the 10 MHz signal on the A23 Motherboard at J10 pin 99 using an oscilloscope.

The signal should have a period of 100 ns and an amplitude greater than 1.2 V<sub>p-p</sub>.

        - If the signal is good, replace the A16 Frac-N (Option UNJ or Option 506). (Refer to “[A16 Frac-N \(Option UNJ or Option 506\)](#)” on page 2-43.)
        - If the signal is bad, troubleshoot the A18 Reference.
      - If the connection is bad, replace the A19 Daughterboard. (Refer to “[A19 Daughterboard](#)” on page 2-49.)
- If the +32 V, –5.2 V, and +9 V supplies are bad on the connector pins indicated:
  - a. Check the origin of the supply.
    - If the supply voltage is good at the origin, replace the A23 Motherboard. (Refer to “[A23 Motherboard](#)” on page 2-57.)
    - If the supply voltage is bad at the origin, troubleshoot the problem using that assembly's troubleshooting procedure.

**703 Output Voltage**

- Replace the A16 Frac-N (Option UNJ or Option 506).

**704 Filter Test**

- If the -6 V supply is good on the connector pins indicated:
  - a. Turn power off to the signal generator.
  - b. Remove the A16 Frac-N (Option UNJ or Option 506) and ohm the connection between the appropriate J42 and J10 pins.
    - If the connection is good:
      - a. Re-insert the A16 Frac-N (Option UNJ or Option 506).
      - b. Remove the A13 Output (Option 501, 502, 503, 504 or Option UNJ).
      - c. Turn power on to the signal generator.
      - d. Connect a semi-rigid cable-extender probe to P4 and connect a signal analyzer to the probe; P4 delivers the RF output of the A16 Frac-N (Option UNJ or Option 506) through W41.

To locate W41, refer to “[Option UNJ - Semi-Rigid Cables includes the A21 YTO Driver \(Option UNJ or Option 506\) and the AT1 Electronic Attenuator \(Option 501, 502, 503, 504 or Option UNJ\) or the AT1 High-Power Mechanical Attenuator \(Option UNB\) with A28 Reverse Power Protection \(Option 501, 502, 503, 504 or Option UNJ\)](#)” on page 3-39.

- e. Tune the signal generator to the following frequencies and check for the presence of a signal:

**Table 1-22**

Options	Tune (MHz)	Check (MHz)	Level (dBm)
All	300	300 ± 10	> +5
All	500	500 ± 10	> +5
All	900	900 ± 10	> +5
502/503/504/506	1300	1300 ± 10	> +5
503/504/506	2250	2250 ± 10	> +5
503/504/506	3000	3000 ± 10	> +5

- If these signals are good, replace the A16 Frac-N (Option UNJ or Option 506).



- If these signals are bad, carefully remove the A16 Frac-N (Option UNJ or Option 506) with the power on.
  - a. Connect a semi-rigid cable-extender probe to P1 and connect a signal analyzer to the probe; P1 delivers the YO input of the A16 Frac-N (Option UNJ or Option 506) through W38.

To locate W38, refer to “[Option UNJ - Semi-Rigid Cables includes the A21 YTO Driver \(Option UNJ or Option 506\) and the AT1 Electronic Attenuator \(Option 501, 502, 503, 504 or Option UNJ\) or the AT1 High-Power Mechanical Attenuator \(Option UNB\) with A28 Reverse Power Protection \(Option 501, 502, 503, 504 or Option UNJ\)](#)” on page 3-39.

- b. Tune the signal generator to the following frequencies and check for the presence of a signal:

**Table 1-23**

Options	Tune (MHz)	Check (MHz)	Level (dBm)
All	300	4800 ± 300	> -10
All	500	8000 ± 300	> -10
All	900	7200 ± 300	> -10
502/503/504/506	1300	5200 ± 300	> -10
503/504/506	2250	4500 ± 300	> -10
503/504/506	3000	6000 ± 300	> -10

- If these signals are good, replace the A16 Frac-N (Option UNJ or Option 506). (Refer to “[A16 Frac-N \(Option UNJ or Option 506\)](#)” on page 2-43.)
- If these signals are bad, replace the A21 YTO Driver (Option UNJ or Option 506) (Refer to “[A21 YTO Driver \(Option UNJ or Option 506\) and A22 Coupler \(Option 506\)](#)” on page 2-55.)

- If the connection is bad, replace the A19 Daughterboard. (Refer to “[A19 Daughterboard](#)” on page 2-49.)

- If the -6 V supply is bad on the connector pins indicated:

- a. Check the origin of the supply.

- If the supply voltage is good at the origin, replace the A23 Motherboard. (Refer to “[A23 Motherboard](#)” on page 2-57.)

- If the supply voltage is bad at the origin, troubleshoot the problem using that assembly's troubleshooting procedure.

**705 F/2 Test**

- Replace the A16 Frac-N (Option UNJ or Option 506).

**706 FM Path Test**

- Measure the FM modulation signal from the A18 Reference.
  - a. Turn power off to the signal generator.
  - b. Remove the A16 Frac-N (Option UNJ or Option 506) and insert a ribbon cable extender<sup>1</sup> into J41 of the A19 Daughterboard.
  - c. Turn power on to the signal generator.
  - d. Select the FM/φM menu.
  - e. Select the following:
    - FM mode
    - FM Path 1
    - FM Dev 100 kHz
    - FM Source (Internal)
    - FM Rate 400 Hz
    - More (1 of 2) >> FM Waveform (Square)
    - FM On
  - f. Select Mode On/Off to On.
  - g. Use an oscilloscope and probe pin 11 of the ribbon cable extender to measure a 400 Hz square-wave signal with an amplitude > 4 Vp-p.
    - If the signal is good, replace the A16 Frac-N (Option UNJ or Option 506). (Refer to “[A16 Frac-N \(Option UNJ or Option 506\)](#)” on page 2-43.)
    - If the signal is bad, turn power off and remove the A18 Reference (refer to “[A18 Reference](#)” on page 2-47).
      - a. Ohm the connection between J41-11 and J53-11 of the A19 Daughterboard.  
  
This should be a short.
      - b. Ohm the connection between J41-11 and ground.  
  
This should be an open.
        - If the connections are bad, replace the A19 Daughterboard (refer to “[A19 Daughterboard](#)” on page 2-49).
        - If the connections are good, troubleshoot the A18 Reference.

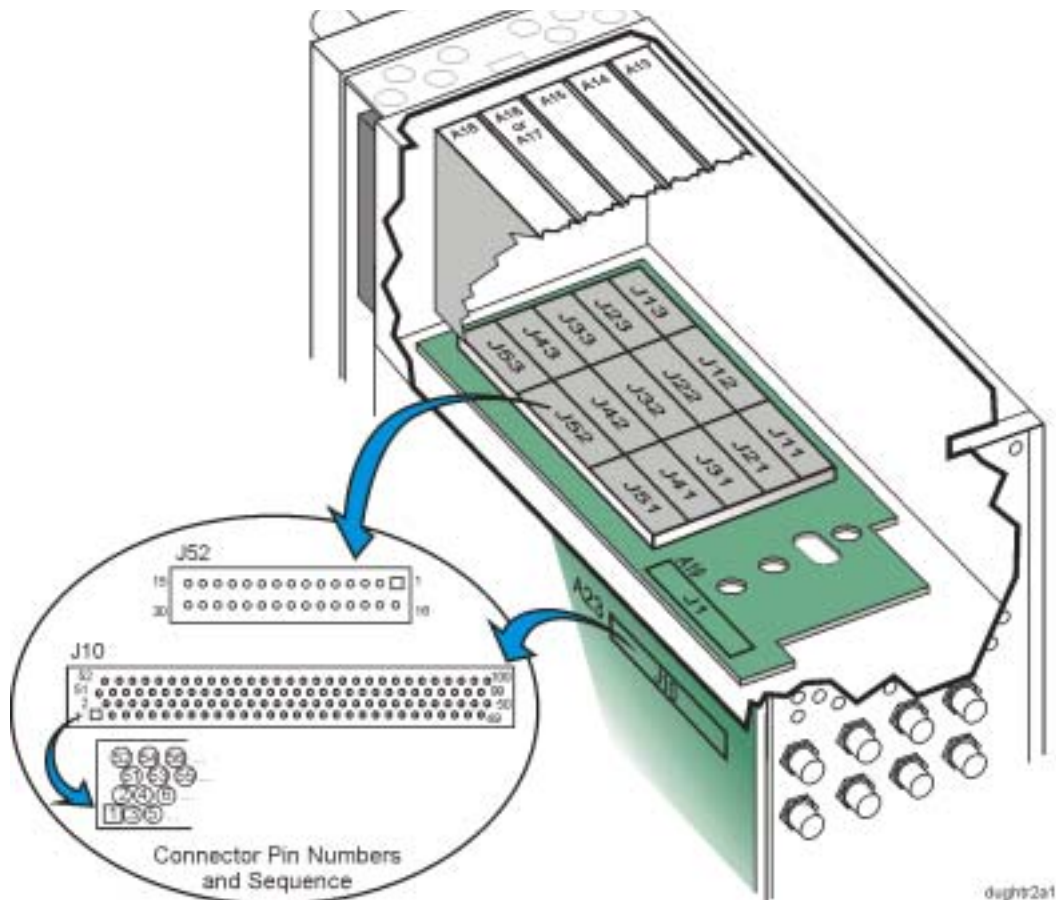
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1. The ribbon cable extender needs to be assembled by the user. It consists of two connector plugs (part number 1252-1010) and approximately eight inches of ribbon cable (part number 8120-2226). The ribbon cable contains 26 wires; six of these wires need to be stripped off before being connected to the plugs.

**Self-Tests 8xx for the A18 Reference**

**NOTE** These self-tests apply to the A18 Reference (Option 501, 502, 503, 504), A18 Reference (Option 501, 502, 503, 504 with Option 1E5), and A18 Reference (Option UNJ or Option 506).

**Figure 1-18**



**Table 1-24**

Power Supply Voltage (Vdc)	Connector Pins on the		Minimum Value (Vdc)	Maximum Value (Vdc)	Origin
	A19 Daughterboard	A23 Motherboard			
+32	J52-7	J10-83	+31.04	+32.96	Main Supply
+15	J52-6, 21	J10-32, 82	+14.1	+15.9	Main Supply
-15	J52-2, 17	J10-36, 86	-15.9	-14.1	Main Supply
-5.2	J52-4, 19	J10-35, 85	-5.356	-5.044	Main Supply
+5.2	J52-14, 29	J10-30, 31, 80, 81	+4.68	+5.72	Main Supply
+15 STBY	J52-26	J10-33	+14.55	+15.45	Main Supply

**800 1 GHz Detector**

- Replace the A18 Reference.

**801 Tuning Voltage**

- If the supply voltages are good on the J10 connector pins indicated:
  - a. Turn power off to the signal generator.
  - b. Remove the A18 Reference and ohm the connection between the appropriate J52 and J10 pins.
    - If the connection is good, replace the A18 Reference.  
(Refer to [“A18 Reference” on page 2-47.](#))
    - If the connection is bad, replace the A19 Daughterboard.  
(Refer to [“A19 Daughterboard” on page 2-49.](#))
- If the supply voltages are bad on the J10 connector pins indicated:
  - a. Check the origin of the supply.
    - If the supply voltage is good at the origin, replace the A23 Motherboard.  
(Refer to [“A23 Motherboard” on page 2-57.](#))
    - If the supply voltage is bad at the origin, troubleshoot the problem using that assembly's troubleshooting procedure.

**802 AM Mux and DACs**

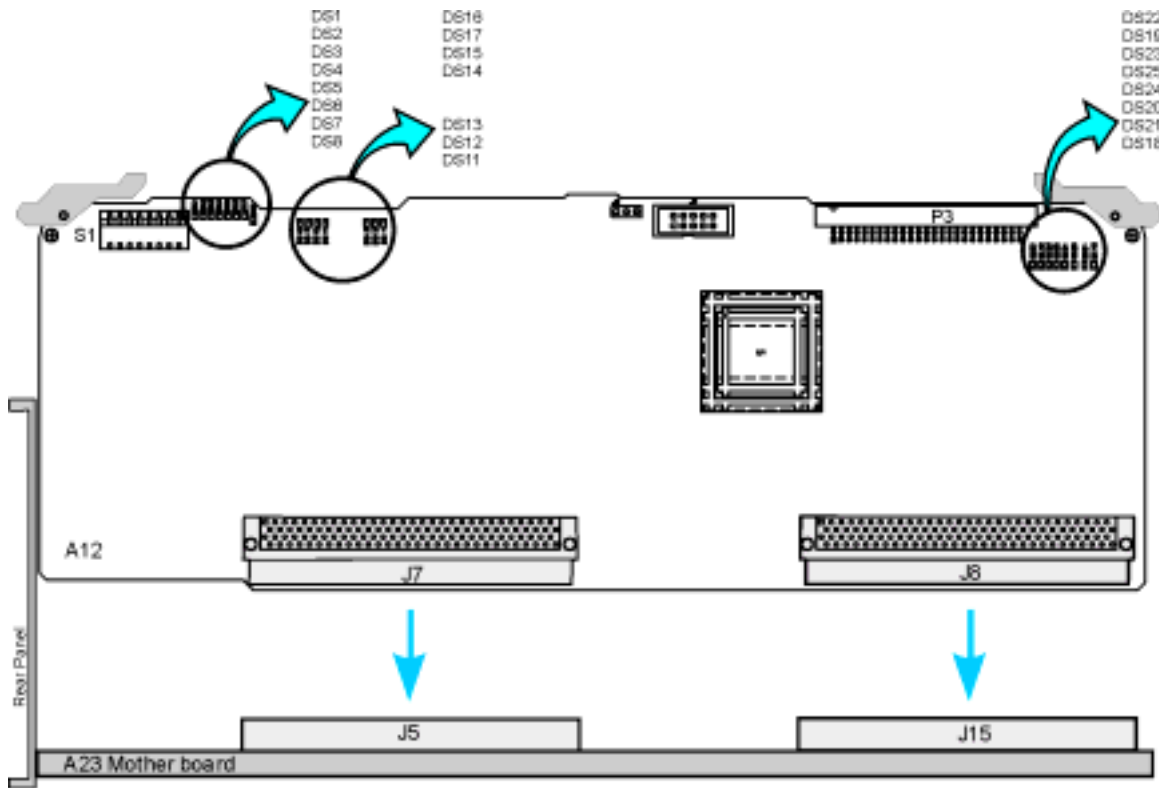
- Replace the A18 Reference.

**803 FM Mux and DACs**

- Replace the A18 Reference.

Self-Tests 11xx for the A12 CPU

Figure 1-19



a12\_cpu2mthr-bd

Table 1-25

Power Supply Voltage (Vdc)	Connector Pins on the		Minimum Value (Vdc)	Maximum Value (Vdc)	Origin
	A12 CPU	A23 Motherboard			
+32	J8-44	J15-44	+31.04	+32.96	Main Supply
+15	J8-103	J15-103	+14.55	+15.45	Main Supply
-15	J8-43	J15-43	-15.45	-14.55	Main Supply
+9	J8-58	J15-58	+8.73	+9.27	Main Supply
-6	J8-57	J15-57	-6.18	-5.82	Main Supply
-5.2	J8-101	J15-101	-5.356	-5.044	Main Supply
+5.2	J7-59, 60, 119, 120	J5-59, 60, 119, 120	+5.044	+5.356	Main Supply
+3.4	J7-55, 56, 57, 58, 115, 116, 117, 118	J5-55, 56, 57, 58, 115, 116, 117, 118	+3.298	+3.502	Main Supply

**1100 Power Supply**

- Run the Self-Test to verify that the A12 CPU is receiving the following power supplies: +10 V Ref, -6 V Ref, -5.2 V Ref, and the +9 V Ref.
  - If the power supply voltages are correct on the J15 and J5 connector pins indicated:
    - a. Ohm the connection between the connectors J8 and J15, or J7 and J5.
      - If the connection is good, replace the A12 CPU.  
(Refer to [“A12 CPU” on page 2-35.](#))
      - If the connection is bad, replace the A23 Motherboard.  
(Refer to [“A23 Motherboard” on page 2-57.](#))
  - If the power supply voltages are incorrect on the J15 and J5 connector pins indicated:
    - a. Check the origin of the power supply.
      - If the power supply voltages are good at the origin, replace the A23 Motherboard.  
(Refer to [“A23 Motherboard” on page 2-57.](#))
      - If the power supply voltages are bad, troubleshoot the problem using the assembly’s troubleshooting procedure.

Self-Tests 14xx for the A11 Internal Bit Error Rate Analyzer (Option UN7)

Figure 1-20

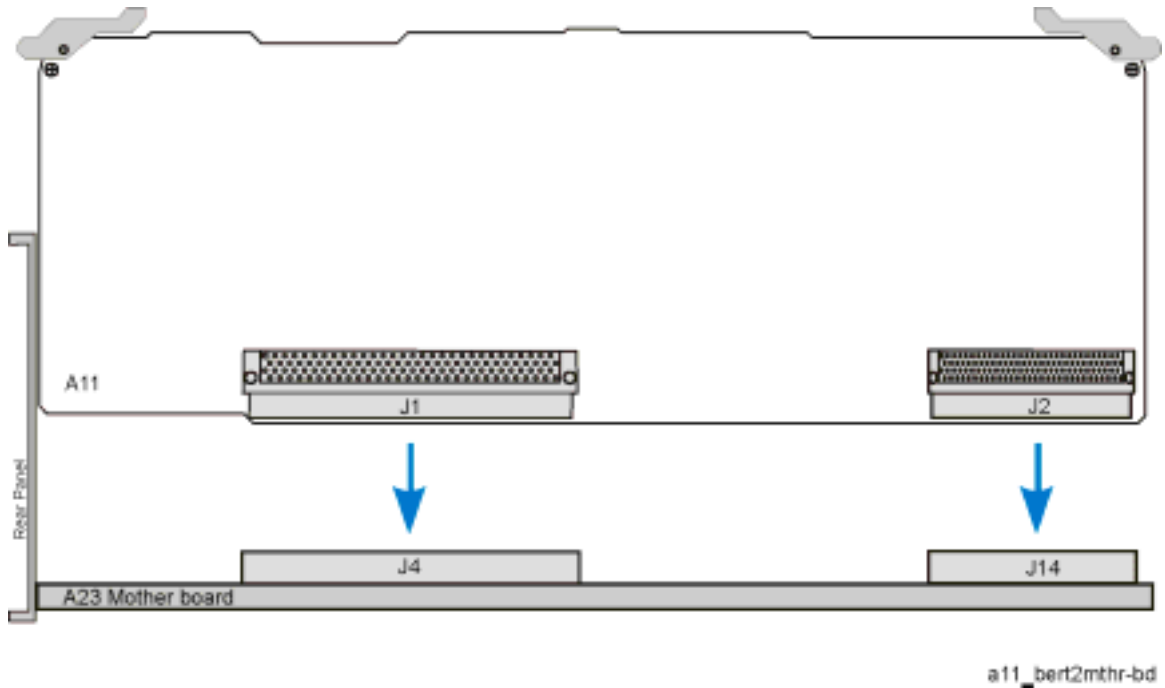


Table 1-26

Power Supply Voltage (Vdc)	Connector Pins on the A23 Motherboard	Minimum Value (Vdc)	Maximum Value (Vdc)	Origin
+15	J14-28	+14.55	+15.45	Main Supply
-15	J14-3	-15.45	-14.55	Main Supply
-5.2	J14-1, 26	-5.356	-5.044	Main Supply
+5.2 VD	J4-64, 65	+5.044	+5.356	Main Supply
+3.4 VD	J4-60, 61, 62, 63	+3.298	+3.502	Main Supply
+2.6 VD	J4-57, 58, 59	+2.425	+2.575	A23 Motherboard

**1400 Power Supply**

- If the supply voltages are good on the connector pins indicated:
  - a. Turn power off to the signal generator.
  - b. Remove the A11 Internal Bit Error Rate Analyzer (Option UN7) and ohm the connection between the appropriate pins of J14/J4, and sockets J14/J4.
    - If the connection is good, replace the A11 Internal Bit Error Rate Analyzer (Option UN7). (Refer to [“A11 Internal Bit Error Rate Analyzer \(Option UN7\)” on page 2-32.](#))
    - If the connection is bad, replace the A23 Motherboard. (Refer to [“A23 Motherboard” on page 2-57.](#))
- If the supply voltages are bad on the connector pins indicated:
  - a. Check the origin of the supply.
    - If the supply voltage is good at the origin, replace the A23 Motherboard. (Refer to [“A23 Motherboard” on page 2-57.](#))
    - If the supply voltage is bad at the origin, troubleshoot the problem using that assembly's troubleshooting procedure.



**Self-Tests 15xx  
 for the A9 Digital Demodulator (Option 501, 502, 503, 504 with Option 300)**

Figure 1-21

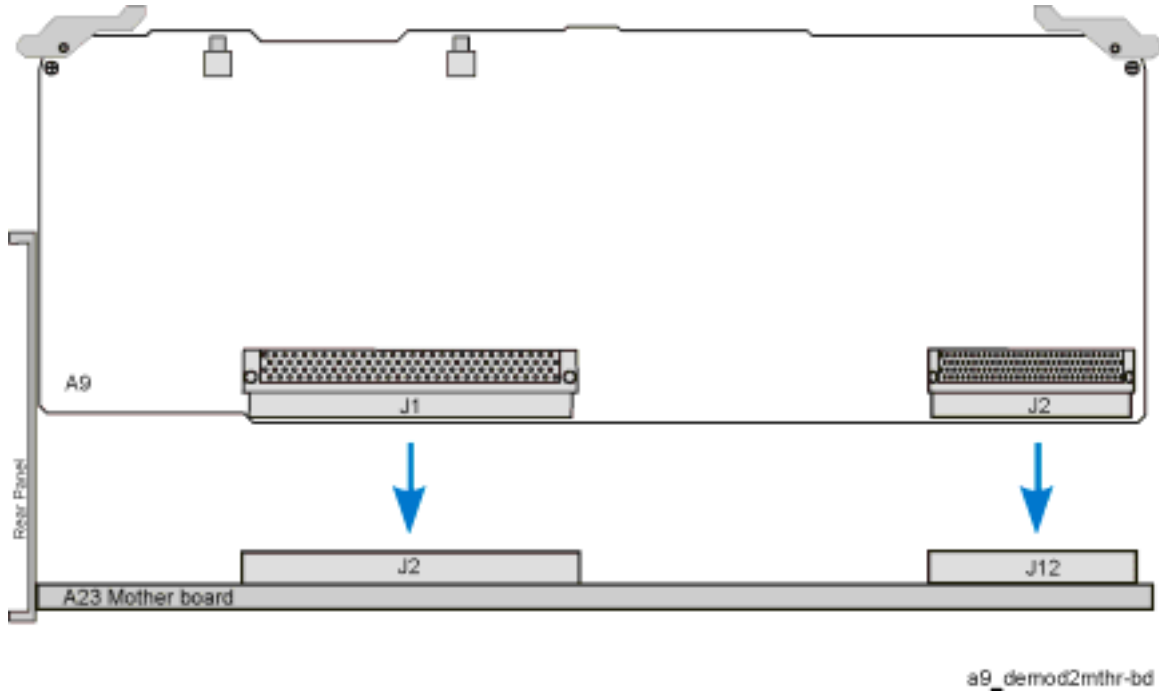


Table 1-27

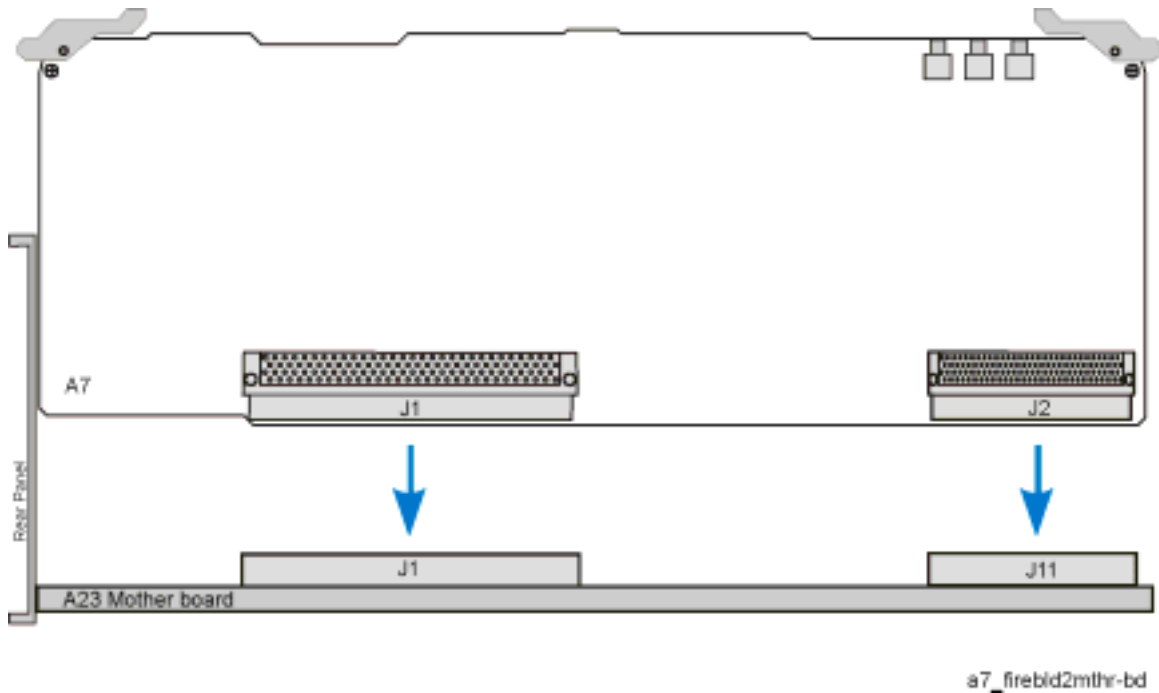
Power Supply Voltage (Vdc)	Connector Pins on the A23 Motherboard	Minimum Value (Vdc)	Maximum Value (Vdc)	Origin
+15	J12-28	+14.55	+15.45	Main Supply
-15	J12-3	-15.45	-14.55	Main Supply
-5.2	J12-1, 26	-5.356	-5.044	Main Supply
+5.2	J12-2, 27	+5.044	+5.356	Main Supply
+5.2 VD	J2-64, 65	+5.044	+5.356	Main Supply
+3.4 VD	J2-60, 61, 62, 63	+3.298	+3.502	Main Supply
+2.6 VD	J2-57, 58, 59	+2.425	+2.575	A23 Motherboard

**1500 Power Supply**

- If the supply voltages are good on the connector pins indicated:
  - a. Turn power off to the signal generator.
  - b. Remove the A9 Digital Demodulator (Option 501, 502, 503, 504 with Option 300) and ohm the connection between the appropriate pins of J12/J2, and sockets J12/J2.
    - If the connection is good, replace the A9 Digital Demodulator (Option 501, 502, 503, 504 with Option 300). (Refer to [“A9 Digital Demodulator \(Option 501, 502, 503, 504 with Option 300\)” on page 2-28.](#))
    - If the connection is bad, replace the A23 Motherboard. (Refer to [“A23 Motherboard” on page 2-57.](#))
- If the supply voltages are bad on the connector pins indicated:
  - a. Check the origin of the supply.
    - If the supply voltage is good at the origin, replace the A23 Motherboard. (Refer to [“A23 Motherboard” on page 2-57.](#))
    - If the supply voltage is bad at the origin, troubleshoot the problem using that assembly's troubleshooting procedure.

**Self-Tests 16xx for the A7 Baseband Generator (Option 001/601 or Option 002/602)**

Figure 1-22



**Table 1-28**

Power Supply Voltage (Vdc)	Connector Pins on the A23 Motherboard	Minimum Value (Vdc)	Maximum Value (Vdc)	Origin
+32	J11-4	+31.04	+32.96	Main Supply
+15	J11-28	+14.1	+15.9	Main Supply
-15	J11-3	-15.9	-14.1	Main Supply
-5.2	J11-1, 26	-5.356	-5.044	Main Supply
+5.2	J11-2, 27	+4.68	+5.72	Main Supply
+3.4 VD	J1-60, 61, 62, 63	+3.298	+3.502	Main Supply
+2.6 VD	J1-57, 58, 59	+2.425	+2.575	A23 Motherboard
+1.9 VD	J1-52	+1.843	+1.957	A23 Motherboard
+1.8 VD	J1-55, 56	+1.746	+1.854	A23 Motherboard

**1600 Host <-> BBB Comm**

- If the +3.4 VD, +2.6 VD, and +1.9 VD supply voltages are good on the connector pins indicated:
  - a. Turn power off to the signal generator.
  - b. Remove the A7 Baseband Generator (Option 001/601 or Option 002/602) and ohm the connection between the appropriate pins of J1, on the A23 Motherboard side, and socket J1, on the A7 Baseband Generator (Option 001/601 or Option 002/602) side.
    - If the connection is good, replace the A7 Baseband Generator (Option 001/601 or Option 002/602). (Refer to [“A7 Baseband Generator \(Option 001/601 or Option 002/602\)” on page 2-26.](#))
    - If the connection is bad, replace the A23 Motherboard. (Refer to [“A23 Motherboard” on page 2-57.](#))
- If the +3.4 VD supply voltage is bad on the connector pins indicated:
  - a. Check the origin of the supply.
    - If the supply voltage is good at the origin, replace the A23 Motherboard. (Refer to [“A23 Motherboard” on page 2-57.](#))
    - If the supply voltage is bad at the origin, troubleshoot the problem using that assembly's troubleshooting procedure.
- If the +2.6 VD and +1.9 VD supply voltages are bad on the connector pins indicated:
  - a. Replace the A23 Motherboard.

**1601 Apps1 <-> Apps2 Comm**

- If the +1.8 VD supply voltages is good on the connector pins indicated:
  - a. Turn power off to the signal generator.
  - b. Remove the A7 Baseband Generator (Option 001/601 or Option 002/602) and ohm the connection between the appropriate pins of J1, on the A23 Motherboard side, and socket J1, on the A7 Baseband Generator (Option 001/601 or Option 002/602) side.
    - If the connection is good, replace the A7 Baseband Generator (Option 001/601 or Option 002/602). (Refer to [“A7 Baseband Generator \(Option 001/601 or Option 002/602\)” on page 2-26.](#))
    - If the connection is bad, replace the A23 Motherboard. (Refer to [“A23 Motherboard” on page 2-57.](#))
- If the supply voltages are bad on the connector pins indicated:
  - a. Check the origin of the supply.
    - If the supply voltage is good at the origin, replace the A23 Motherboard. (Refer to [“A23 Motherboard” on page 2-57.](#))
    - If the supply voltage is bad at the origin, troubleshoot the problem using that assembly's troubleshooting procedure.

**1602 Apps1 <-> SH4 Comm**

- Replace the A7 Baseband Generator (Option 001/601 or Option 002/602).

**1603 Apps2 <-> SH4 Comm**

- Replace the A7 Baseband Generator (Option 001/601 or Option 002/602).

#### 1604 Fliegen <-> Apps2 Comm

- Replace the A7 Baseband Generator (Option 001/601 or Option 002/602).

#### 1605 SRAM Test

- Replace the A7 Baseband Generator (Option 001/601 or Option 002/602).

#### 1606 SDRAM Test

- Replace the A7 Baseband Generator (Option 001/601 or Option 002/602).

#### 1607 Paren <-> SH4 Comm

- Replace the A7 Baseband Generator (Option 001/601 or Option 002/602).

#### 1608 VCO Tune Test

- If the +32 V,  $\pm 15$  V, and  $\pm 5.2$  V supply voltages are good on the connector pins indicated:
  - a. Turn power off to the signal generator.
  - b. Remove the A7 Baseband Generator (Option 001/601 or Option 002/602) and ohm the connection between the appropriate pins of J11/J1, on the A23 Motherboard side, and socket J11/J1, on the A7 Baseband Generator (Option 001/601 or Option 002/602) side.

— If the connection is good:

- a. Re-insert the A7 Baseband Generator (Option 001/601 or Option 002/602).
- b. Turn power on to the signal generator.
- c. Check the 10 MHz signal on the A23 Motherboard at J1 pins 26 and 91 using an oscilloscope.

The signal should be 100 ns with an amplitude of approximately 1.0 V<sub>p-p</sub> or higher.

— If the 10 MHz signal is good, replace the A7 Baseband Generator (Option 001/601 or Option 002/602). (Refer to “[A7 Baseband Generator \(Option 001/601 or Option 002/602\)](#)” on page 2-26.)

— If the 10 MHz signal is bad, troubleshoot the A23 Motherboard and the A7 Baseband Generator (Option 001/601 or Option 002/602).

— If the connection is bad, replace the A23 Motherboard (refer to “[A23 Motherboard](#)” on page 2-57).

- If the +32 V,  $\pm 15$  V, and  $\pm 5.2$  V supply voltages are bad on the connector pins indicated:
  - a. Check the origin of the supply.
    - If the supply voltage is good at the origin, replace the A23 Motherboard. (Refer to “[A23 Motherboard](#)” on page 2-57.)
    - If the supply voltage is bad at the origin, troubleshoot the problem using that assembly's troubleshooting procedure.

#### 1609 IQ DAC Output Test

- Replace the A7 Baseband Generator (Option 001/601 or Option 002/602).

#### 1610 PLL Test

- Replace the A7 Baseband Generator (Option 001/601 or Option 002/602).

Self-Tests 20xx for the A14 Extended Frequency Output (Option 506)

Figure 1-23

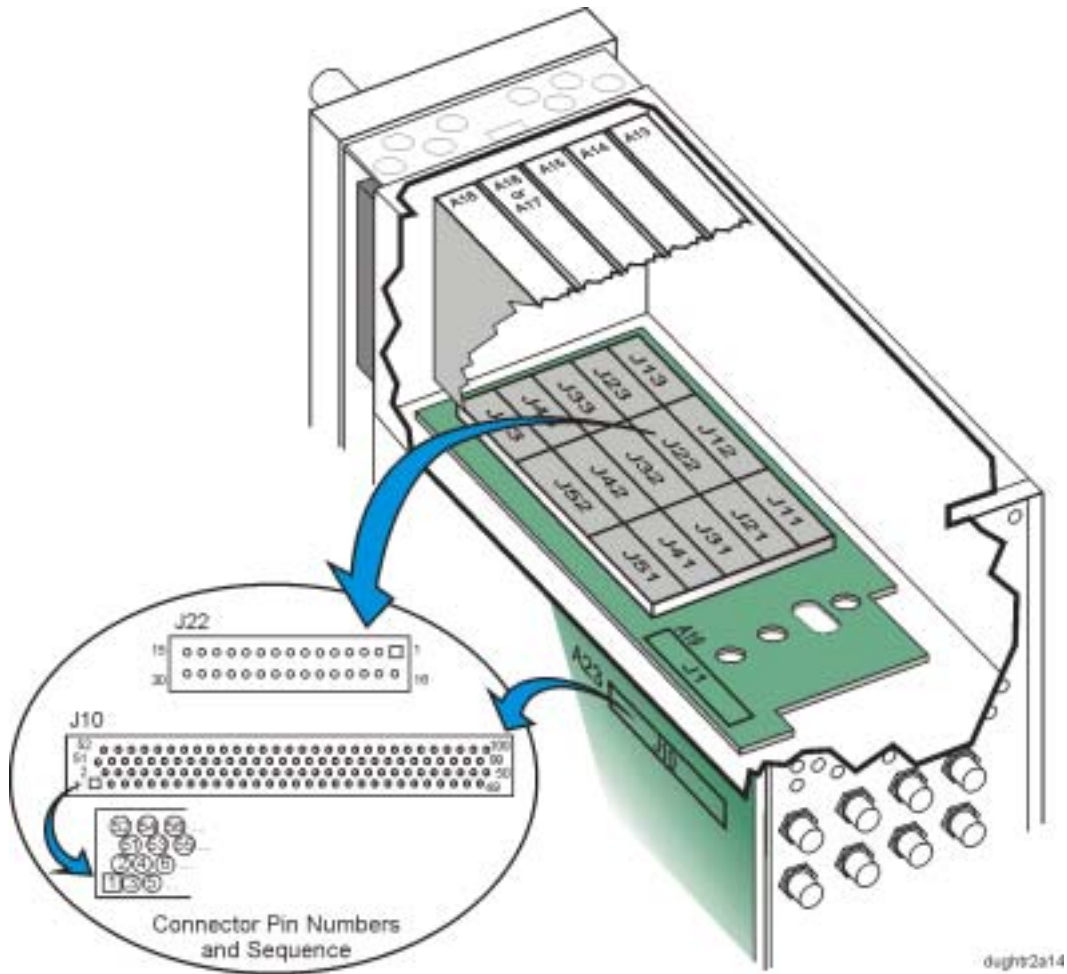


Table 1-29

Power Supply Voltage (Vdc)	Connector Pins on the		Minimum Value (Vdc)	Maximum Value (Vdc)	Origin
	A19 Daughterboard	A23 Motherboard			
+15	J22-6, 21	J10-32, 82	+14.1	+15.9	Main Supply
-15	J22-2, 17	J10-36, 86	-15.9	-14.1	Main Supply
+9	J22-5, 20	J10-34, 84	+8.73	+9.27	Main Supply
-6	J22-3, 18	J10-37, 87	-6.36	-5.64	Main Supply
-5.2	J22-4, 19	J10-35, 85	-5.356	-5.044	Main Supply
+5.2	J22-14, 29	J10-30, 31, 80, 81	+4.68	+5.72	Main Supply

### 2000 Ground and PTAT Test

- Using a DVM, measure the voltages on J10 (the PTAT voltage is derived from the  $\pm 15$  Vdc):
  - If the supply voltages are good on the J10 connector pins indicated:
    - a. Turn off the power to the signal generator.
    - b. Remove the A14 Extended Frequency Output (Option 506).
    - c. Ohm the connection between the appropriate pins of J22 and J10.
      - If the connection is good, replace the A14 Extended Frequency Output (Option 506). (Refer to “[A14 Extended Frequency Output \(Option 506\)](#)” on page 2-39.)
      - If the connection is bad, replace the A19 Daughterboard. (Refer to “[A19 Daughterboard](#)” on page 2-49.)
  - If the supply voltages are bad on any of the J10 connector pins indicated
    - a. Check the origin of the supply.
      - If the supply voltages are good at the origin, replace the A23 Motherboard. (Refer to “[A23 Motherboard](#)” on page 2-57.)
      - If the supply voltages are bad at the origin, troubleshoot the problem using that assembly's troubleshooting procedure.

### 2001 Prelevel Loop Test

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**CAUTION** Do not install the A14 Extended Frequency Output (Option 506) with the signal generator powered up or serious damage to the signal generator may be caused.

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- This test checks the Prelevel loop at three frequencies: 4.1 GHz, 5 GHz, and 6 GHz.
  - If the test fails:
    - a. Press the **View Test Info** softkey and use the RPG to highlight this test.
    - b. Press the **View Details** softkey to display the failed test frequency.
    - c. With the signal generator powered on, carefully remove the A14 Extended Frequency Output (Option 506).
    - d. Set the signal generator to the failed test frequency (either 4.1 GHz, 5 GHz, or 6 GHz).
    - e. Connect a spectrum analyzer to J4 of the A14 Extended Frequency Output (Option 506); this is the output signal of the A16 Frac-N (Option UNJ or Option 506).
    - f. The signal at J4 should be at the failed test frequency with an amplitude  $\geq -5$  dBm.
      - If the signal is not present or at the correct level, refer to troubleshooting for the A16 Frac-N (Option UNJ or Option 506).
      - If the signal is present, replace the A14 Extended Frequency Output (Option 506). (Refer to “[A14 Extended Frequency Output \(Option 506\)](#)” on page 2-39.)

### 2002 Quadrature DAC Test

- Replace the A14 Extended Frequency Output (Option 506).

**2003 VBLO Test**

- Replace the A14 Extended Frequency Output (Option 506).

**2004 Marble Test**

- Measure the Q signal from the A10 I/Q Multiplexer (Option 506).
  - a. Disconnect the cable at J9 of the A19 Daughterboard and connect it to a DVM.
  - b. Set the CW frequency to 6 GHz.
  - c. Turn on the I/Q modulation and adjust the Q offset with the following keys:
    - I/Q hardkey
    - I/Q On
    - I/Q Adjustments
    - I/Q Adjustments On
    - Q Offset
  - d. Rotate the RPG and observe the DVM.

The voltage should vary from approximately +0.30 Vdc for an offset of 50% to approximately -0.20 Vdc for an offset of 50%.

- If the signal is bad, check the signal at J6 of the A10 I/Q Multiplexer (Option 506).
  - If the signal is bad at J6, refer to troubleshooting the A10 I/Q Multiplexer (Option 506).
  - If the signal is good at J6, change the cable.

- Measure the I signal from the A10 I/Q Multiplexer (Option 506).
  - a. Disconnect the cable at J8 of the A19 Daughterboard and connect it to a DVM.
  - b. Set the CW frequency to 6 GHz.
  - c. Turn on the I/Q modulation and adjust the I offset with the following keys:
    - I/Q hardkey
    - I/Q On
    - I/Q Adjustments
    - I/Q Adjustments On
    - I Offset
  - d. Rotate the RPG and observe the DVM.

The voltage should vary from approximately +0.25 Vdc for an offset of 50% to approximately -0.25 Vdc for an offset of 50%.

- If the signal is bad, check the signal at J5 of the A10 I/Q Multiplexer (Option 506).
  - If the signal is bad at J5, refer to troubleshooting the A10 I/Q Multiplexer (Option 506).
  - If the signal is good at J5, change the cable.

- If both the I and Q signals are good, replace the A14 Extended Frequency Output (Option 506). (Refer to [“A14 Extended Frequency Output \(Option 506\)” on page 2-39.](#))



**2005 Gain Adjustment Test**

- Replace the A14 Extended Frequency Output (Option 506).

**2006 ALC Reference Test**

- Replace the A14 Extended Frequency Output (Option 506).

**2007 ALC Modulation Test**

- Replace the A14 Extended Frequency Output (Option 506).

**2008 Burst Modulation Test**

- Replace the A14 Extended Frequency Output (Option 506).

**2009 ALC Detector Test**

- Replace the A14 Extended Frequency Output (Option 506).

**2010 Bulk R DAC Test**

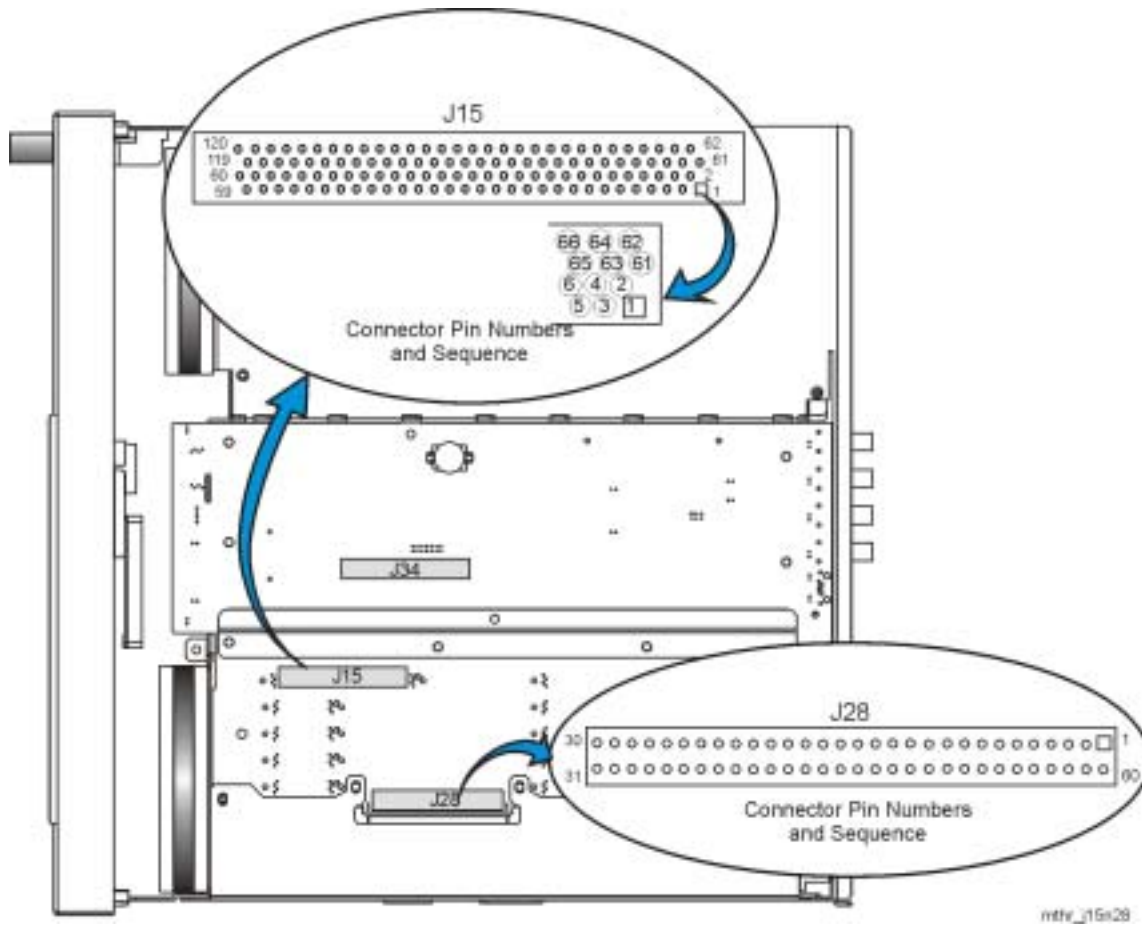
- Replace the A14 Extended Frequency Output (Option 506).

**2011 Log Offset Test**

- Replace the A14 Extended Frequency Output (Option 506).

**Self-Tests 21xx for the A23 Motherboard**

Figure 1-24



**Table 1-30**

Power Supply Voltage (Vdc)	Connector Pins on the A23 Motherboard	Minimum Value (Vdc)	Maximum Value (Vdc)	Origin
+32	J28-27	+31.04	+32.96	Main Supply
+15	J28-25, 26	+14.55	+15.45	Main Supply
-15	J28-23	-15.45	-14.55	Main Supply
+9	J28-19, 42	+8.73	+9.27	Main Supply
-6	J28-38	-6.18	-5.82	Main Supply
-5.2	J28-21, 40	-5.356	-5.044	Main Supply
+5.2 VD	J28-14, 15, 46, 47	+5.044	+5.356	Main Supply
+3.4 VD	J28-1, 2, 5, 6, 9, 10, 51, 52, 55, 56, 59, 60	+3.298	+3.502	Main Supply

### 2100 Power Supply

- If the supply voltages are good on the connector pins indicated, replace the A23 Motherboard.  
(Refer to [“A23 Motherboard” on page 2-57.](#))
- If the supply voltages are bad on the connector pins indicated:
  - a. Check the origin of the supply.
    - If the supply voltage is good at the origin, replace the A23 Motherboard.  
(Refer to [“A23 Motherboard” on page 2-57.](#))
    - If the supply voltage is bad at the origin, troubleshoot the problem using that assembly's troubleshooting procedure.

### 2101 Sweep Diag

- Replace the A23 Motherboard.

### 2102 LCD Bias

- Use a DVM to probe A23J15-35 for a signal of approximately 21 Vdc.
  - If the voltage is good, replace the A23 Motherboard.  
(Refer to [“A23 Motherboard” on page 2-57.](#))
  - If the voltage is bad, troubleshoot the A12 CPU.

**Self-Tests 23xx for the A13 Output**

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**NOTE** These self-test errors apply to the A13 Output (Option 501, 502, 503, 504 or Option UNJ) and A13 Output (Option UNB).

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Figure 1-25

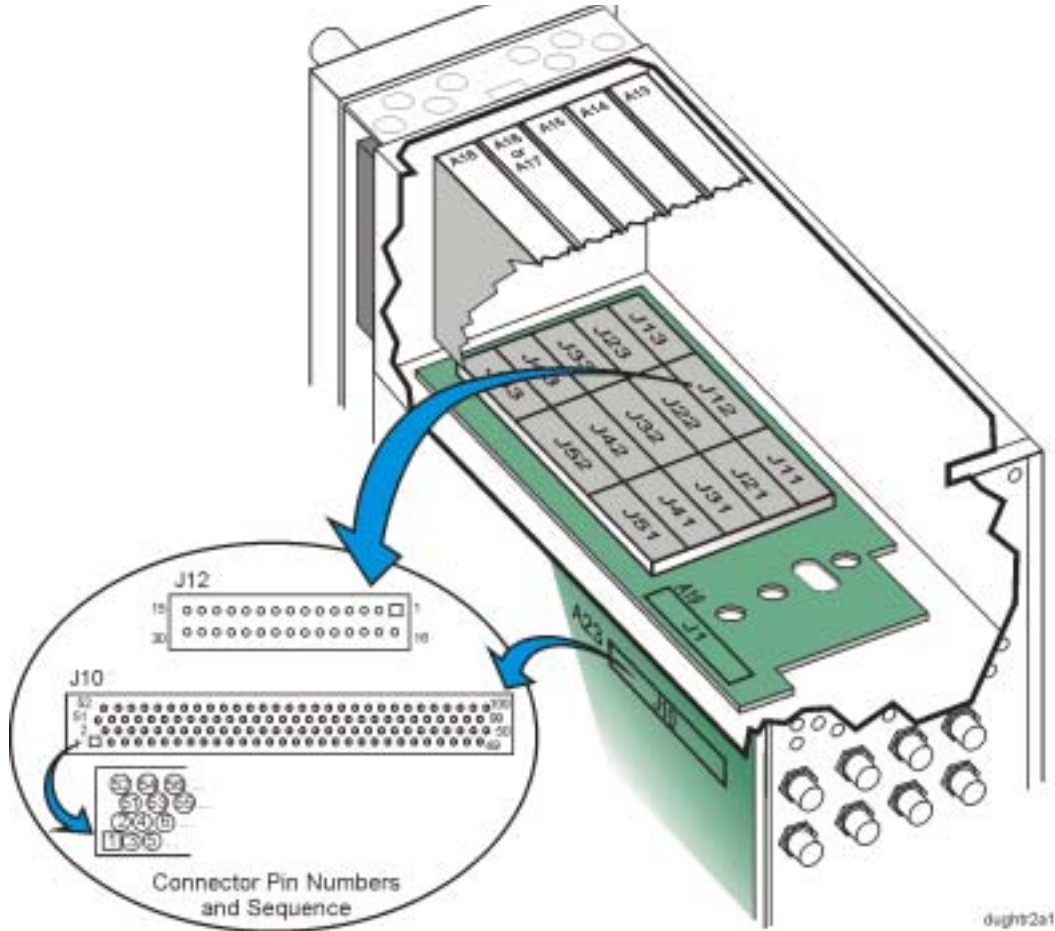


Table 1-31

Power Supply Voltage (Vdc)	Connector Pins on the		Minimum Value (Vdc)	Maximum Value (Vdc)	Origin
	A19 Daughterboard	A23 Motherboard			
+15	J12-6, 21	J10-32, 82	+14.1	+15.9	Main Supply
-15	J12-2, 17	J10-36, 86	-15.9	-14.1	Main Supply
+5.2	J12-14, 29	J10-30, 31, 80, 81	+4.68	+5.72	Main Supply
+9	J12-5, 20	J10-34, 84	+8.73	+9.27	Main Supply
-6	J12-3, 18	J10-37, 87	-6.36	-5.64	Main Supply
-5.2	J12-4, 19	J10-35, 85	-5.356	-5.044	Main Supply

### 2300 Ground and PTAT Test

- Using a DVM, measure the voltages on J10 (the PTAT voltage is derived from the  $\pm 15$  Vdc):
  - If the supply voltages are good on the J10 connector pins indicated:
    - a. Turn off the power and remove the A13 Output.
    - b. Ohm the connection between the appropriate pins of J12 and J10.
      - If the connection is good, replace the A13 Output (refer to “A13 Output” on page 2-37).
      - If the connection is bad, replace the A19 Daughterboard (refer to “A19 Daughterboard” on page 2-49).
  - If the supply voltages are bad on any of the J10 connector pins indicated:
    - a. Check the origin of the supply.
      - If the supply voltages are good at the origin, replace the A23 Motherboard.
      - (Refer to “A23 Motherboard” on page 2-57.)
      - If the supply voltages are bad at the origin, troubleshoot the problem using that assembly's troubleshooting procedure.

### 2301 Prelevel Loop Test

- Replace the A13 Output.

### 2302 Quadrature Loop Test

- Replace the A13 Output.

### 2303 Detector Heater Test

- Replace the A13 Output.

### 2304 IQ Path Test

- Measure the Q signal from the A10 I/Q Multiplexer.
  - a. Disconnect the cable from J3 of the A19 Daughterboard and connect it to a DVM.
  - b. Set the CW frequency to 2 GHz.
  - c. Turn on the I/Q modulation and adjust the Q offset.
  - d. Press: **I/Q** (hardkey) > **I/Q On** > **I/Q Adjustments** > **I/Q Adjustments On** > **Q Offset**
  - e. Rotate the RPG and observe the DVM. The voltage should vary from approximately +0.25 Vdc for an offset of 50% to approximately -0.25 Vdc for an offset of -50%.
    - If the signal is bad, check it at J10 of the A10 I/Q Multiplexer.
      - If the signal is bad at J10, refer to A10 I/Q Multiplexer troubleshooting.
      - If the signal is good at J10, change the cable.
- Measure the I signal from the A10 I/Q Multiplexer.
  - a. Disconnect the cable from J2 of the A19 Daughterboard and connect it to a DVM.
  - b. Set the CW frequency to 2 GHz.

**Troubleshooting Performance Test Problems**

- c. Turn on the I/Q modulation and adjust the I offset.
- d. Press: **I/Q** (hardkey) > **I/Q On** > **I/Q Adjustments** > **I/Q Adjustments On** > **I Offset**
- e. Rotate the RPG and observe the DVM. The voltage should vary from approximately +0.25 Vdc for an offset of 50% to approximately -0.25 Vdc for an offset of -50%.
  - If the signal is bad, check it at J9 of the A10 I/Q Multiplexer.
    - If the signal is bad at J9, refer to A10 I/Q Multiplexer troubleshooting ([page 1-27](#)).
    - If the signal is good at J9, change the cable.
- If the I and Q signals are good, replace the A13 Output (refer to [“A13 Output” on page 2-37](#)).

**2305 Prelevel Bypass Test**

- Replace the A13 Output.

**2306 Switched Filters Test**

- Replace the A13 Output.

**2307 Gain Adjust Test**

- Replace the A13 Output.

**2308 ALC Modulation Test**

- Replace the A13 Output.

**2309 Burst Modulation Test**

- Replace the A13 Output.

**2310 ALC Detector Test**

- Replace the A13 Output.

**2311 ALC Closed Loop Test**

- Replace the A13 Output.

**2312 Detector Logger Test**

- Replace the A13 Output.

**2313 Het Band Test**

- Measure the 1 GHz signal from the A18 Reference.
  - a. With the power off, remove the A13 Output and insert a ribbon cable extender into J11 of the A19 Daughterboard.
  - b. Power on the signal generator and tune to a CW frequency of 200 MHz.
  - c. Using a spectrum analyzer, probe pin 19 of the ribbon cable extender<sup>1</sup> for a 1 GHz signal with an amplitude > -20 dBm:
    - If the signal is present, replace the A13 Output (refer to [“A13 Output” on page 2-37](#)).

- If the signal is not present, power down the signal generator and remove the A18 Reference.  
(Refer to “[A18 Reference](#)” on page 2-47.)
- d. Using a DVM, ohm between J11-19 and J51-19:
  - If the path is open, replace the A19 Daughterboard (refer to “[A19 Daughterboard](#)” on page 2-49).
  - If the path is good (zero ohms), refer to the A18 Reference troubleshooting for the 1 GHz VCO.

- 
1. The ribbon cable extender needs to be assembled by the user. It consists of two connector plugs (part number 1252-1010) and approximately eight inches of ribbon cable (part number 8120-2226). The ribbon cable contains 26 wires; six of these wires need to be stripped off before being connected to the plugs.

## Troubleshooting Unlocks

The phase lock loop refers to that part of the frequency generating circuit used to maintain the phase relationship between the frequency generation loop and a reference frequency. By maintaining the phase relationship, the frequency accuracy of the signal generator is guaranteed.

To maintain accuracy, the phase lock loop compares the phase of a signal from the reference assembly to the phase of the frequency loop signal. If the two signals are slightly out of phase, the phase comparator circuit adjusts the frequency loop signal until the signals are in phase. If the phase comparator circuit cannot adjust the phase of the frequency loop signal to agree with the reference signal, the phase comparator rails and generates an unlocked message. The unlock message is immediately displayed on the front panel display.

Troubleshoot multiple Loop unlock messages in the following order:

1. [“514 Reference Oven Cold” on page 1-68](#)
2. [“515 10 MHz Signal Bad” on page 1-68](#)
3. [“512 Reference Unlocked” on page 1-69](#)
4. [“513 1 GHz Out of Lock” on page 1-69](#)
5. [“508 Frac-N Loop Unlock \(Option 501, 502, 503, 504\)” on page 1-69](#)  
or [“508 Frac-N Loop Unlock \(Option UNJ and Option 506\)” on page 1-69](#)
6. [“520 Sampler Unlocked \(Option UNJ and Option 506\)” on page 1-70](#)
7. [“521 YO Loop Unlocked \(Option UNJ and Option 506\)” on page 1-71](#)
8. [“516 Baseband Generator Unlock” on page 1-71](#)
9. [“623 Demod Unlock” on page 1-72](#)

### 514 Reference Oven Cold

A 514 Reference Oven Cold message indicates that the reference has not been connected to electrical power for 5 minutes. This message appears when the signal generator is unplugged or the A18 Reference is removed. The message is controlled by a timer and should turn off after 5 minutes. This is not a failure unless the message does not go off after 5 minutes.

- ❑ Check that the message goes away after 5 minutes.
  - If the message continues to be displayed after 5 minutes, replace the A18 Reference.  
(Refer to [“A18 Reference” on page 2-47.](#))

### 515 10 MHz Signal Bad

A 515 10 MHz Signal Bad message indicates a problem with the output of the 10 MHz reference oscillator on the A18 Reference.

- ❑ Check the display for this unlock message.
  - If the message is present, replace the A18 Reference.  
(Refer to [“A18 Reference” on page 2-47.](#))



## 512 Reference Unlocked

A 512 Reference Unlocked message indicates a problem with the 10 MHz reference phase-lock loop on the A18 Reference.

- ❑ Check for and disconnect any external reference signals that are connected to the instrument.
  - If the unlock message goes away when the external reference signal has been disconnected, check that the external reference signal meets the specifications for an external reference.
  - If the unlock message is present with the external reference disconnected, replace the A18 Reference. (Refer to “A18 Reference” on page 2-47.)

## 513 1 GHz Out of Lock

A 513 1GHz Out of Lock message indicates a problem with the 1 GHz phase-lock loop on the A18 Reference.

- ❑ Check for and disconnect any external reference signals that are connected to the instrument.
  - If the unlock message goes away when the external reference signal has been disconnected, check that the external reference signal meets the specifications for an external reference.
  - If the unlock message is present with the external reference disconnected, replace the A18 Reference. (Refer to “A18 Reference” on page 2-47.)

## 508 Frac-N Loop Unlock (Option 501, 502, 503, 504)

A 508 Frac-N Loop Unlock message indicates a problem with the Voltage Control Oscillator's (VCO) phase-lock loop on the A17 Synthesizer (Option 501, 502, 503, 504).

- ❑ Check the 10 MHz signal on the A23 Motherboard at J10 pin 99 using an oscilloscope.

The signal should have a period of 100 ns and an amplitude of greater than 1.2 Vp-p.

  - If the signal is good, replace the A17 Synthesizer (Option 501, 502, 503, 504). (Refer to “A17 Synthesizer (Option 501, 502, 503, 504)” on page 2-45.)
  - If the signal is bad, troubleshoot the A18 Reference. (Refer to “Self-Tests 8xx for the A18 Reference” on page 1-47.)

## 508 Frac-N Loop Unlock (Option UNJ and Option 506)

A 508 Frac-N Loop Unlock message indicates a problem with the Voltage Control Oscillator's (VCO) phase-lock loop on the A16 Frac-N (Option UNJ or Option 506).

- ❑ Check the 10 MHz signal on the A23 Motherboard at J10 pin 99 using an oscilloscope.

The signal should have a period of 100 ns and an amplitude of greater than 1.2 Vp-p.

  - If the signal is good, replace the A16 Frac-N (Option UNJ or Option 506). (Refer to “A16 Frac-N (Option UNJ or Option 506)” on page 2-43.)
  - If the signal is bad, troubleshoot the A18 Reference. (Refer to “Self-Tests 8xx for the A18 Reference” on page 1-47.)

## 520 Sampler Unlocked (Option UNJ and Option 506)

A 520 Sampler Unlocked message indicates a problem with the Voltage Control Oscillator's (VCO) phase-lock loop on the A15 Sampler (Option UNJ or Option 506).

❑ Run self-tests.

— If self-tests fail, troubleshoot the problem reported.

— If self-tests pass:

1. Turn power on to the signal generator.
2. Remove the A15 Sampler (Option UNJ or Option 506).
3. Connect a signal analyzer to the A15 Sampler (Option UNJ or Option 506) J1 input (W44 cable that comes from the A18 Reference).
4. Check for the presence of a 1 GHz signal with a power level of 0 dBm or greater.
  - If the signal is good on W44 cable connector for J1, replace the A15 Sampler (Option UNJ or Option 506). (Refer to [“A15 Sampler \(Option UNJ or Option 506\)” on page 2-41.](#))
  - If the signal is bad on the W44 cable connector for J1:
    - a. Turn power off to the signal generator.
    - b. Remove the A18 Reference. (Refer to [“A18 Reference” on page 2-47.](#))
    - c. Ohm the cable between the center pins (a short should be measured) and between the center pin and shielding (an open should be measured) at J3 of the A18 Reference and J1 of the A15 Sampler (Option UNJ or Option 506).
      - If the cable is good, troubleshoot the A18 Reference. (Refer to [“Self-Tests 8xx for the A18 Reference” on page 1-47.](#))
      - If an open is measured between the center pins or a short between the center pins and the shielding, replace the W44 cable.

## 521 YO Loop Unlocked (Option UNJ and Option 506)

A 521 YO Loop Unlocked message indicates a problem with the YO phase detector on the A15 Sampler (Option UNJ or Option 506).

1. Turn power on to the signal generator.
2. Remove the A15 Sampler (Option UNJ or Option 506).
3. Set the signal generator to 525 MHz.
4. Using a signal analyzer, check for the presence of signals as shown in [Table 1-32](#):
  - If all signals are good, replace the A15 Sampler (Option UNJ or Option 506). (Refer to [“A15 Sampler \(Option UNJ or Option 506\)”](#) on page 2-41.)
  - If any signal is bad, troubleshoot one of the following two assemblies:
    - either the A21 YTO Driver (Option UNJ or Option 506) (Refer to [“Self-Tests 6xx for the A21 YTO Driver \(Option UNJ or Option 506\)”](#) on page 1-39.)
    - or the A16 Frac-N (Option UNJ or Option 506) (Refer to [“Self-Tests 7xx for the A16 Frac-N \(Option UNJ or Option 506\)”](#) on page 1-42.)

**Table 1-32**

Signal Generator Output (MHz)	Output of A21 YTO Driver (Option UNJ or Option 506) Sampler End of W37	Output of A15 Sampler (Option UNJ or Option 506) Sampler End of W11
Tune	A15J3 (> -20 dBm, ±10 MHz)	A15J6 (> +5 dBm, ±10 MHz)
525	4200	796.875
750	6000	644.53
500	8000	593.75

## 516 Baseband Generator Unlock

A 516 Baseband Generator Unlock message indicates a problem with the Voltage Control Oscillator's (VCO) phase-lock loop on the A7 Baseband Generator (Option 001/601 or Option 002/602).

- ❑ Check the 10 MHz signal on the A23 Motherboard at J1 pins 26 and 91 using an oscilloscope.

The signal period should be 100 ns with an amplitude of approximately 1.0 Vp-p or greater.

- If the 10MHz signal is good, replace the A7 Baseband Generator (Option 001/601 or Option 002/602). (Refer to [“A7 Baseband Generator \(Option 001/601 or Option 002/602\)”](#) on page 2-26.)
- If the signal is not present, troubleshoot one of the following two assemblies:
  - either the A23 Motherboard (Refer to [“Self-Tests 21xx for the A23 Motherboard”](#) on page 1-62.)
  - or the A18 Reference (Refer to [“Self-Tests 8xx for the A18 Reference”](#) on page 1-47.)

## **623 Demod Unlock**

A 623 Demod Unlock message indicates a problem with the Voltage Control Oscillator's (VCO) phase-lock loop on the A9 Digital Demodulator (Option 501, 502, 503, 504 with Option 300).

- Check the 10 MHz signal on the A23 Motherboard at J2 pins 26 and 91 using an oscilloscope.

The signal period should be 100 ns with an amplitude of approximately 1.0 Vp-p or greater.

- If the signal is good, replace the A9 Digital Demodulator (Option 501, 502, 503, 504 with Option 300). (Refer to “[A9 Digital Demodulator \(Option 501, 502, 503, 504 with Option 300\)](#)” on page 2-28.)
- If the signal is bad, troubleshoot one of the following two assemblies:
  - either the A23 Motherboard (Refer to “[Self-Tests 21xx for the A23 Motherboard](#)” on page 1-62.)
  - or the A18 Reference (Refer to “[Self-Tests 8xx for the A18 Reference](#)” on page 1-47.)

## Troubleshooting Unlevels and the RF Path

Depending on the signal generator’s frequency range and option selection, there are either one or two output boards used in the RF path:

- Signal generators with Option 501, 502, 503, 504, and Option UNJ contain either the:
  - A13 Output (Option 501, 502, 503, 504 or Option UNJ)
  - or the A13 Output (Option UNB)
- Signal generators with Option 506 contain both the:
  - A13 Output (Option UNB)
  - and the A14 Extended Frequency Output (Option 506)

On the output boards, a leveled output power is obtained by comparing a detected voltage with a reference voltage. The reference voltage is generated using DACs on the output boards. The detected voltage is generated by coupling off a portion of the RF output signal and converting it to dc using detector diodes. When the reference and detected levels are the same, the integrated output level remains constant. When the reference and detected levels are not the same, the integrator output ramps either up or down to increase or decrease the detected level. If the integrator can not achieve a match between the detected voltage and the reference voltage, an unlevelled annunciator is displayed.

Each output board has an Automatic Leveling Control (ALC) circuit which consists of a detector and two modulator diodes. The A13 Output controls the power level for frequencies 4 GHz and below and the A14 Extended Frequency Output (Option 506) controls the power level for frequencies above 4 GHz and  $\leq 6$  GHz.

The RF path must provide a minimum power level to the ALC loop in order for the ALC loop to work properly and this minimum required power is slightly higher than the maximum-leveled power.

## Troubleshooting a Leveling Problem

**NOTE** Before proceeding to troubleshoot with this section, run a full self-test and correct any reported failures and unlock problems. (Refer to [“Self-Test Failures and Related Troubleshooting”](#) on page 1-17 and [“Troubleshooting Unlocks”](#) on page 1-68.)

- ❑ Check the RF path power level against the values in [Table 1-33](#).

**Table 1-33**

Frequencies	Standard Power	Option UNB
250 kHz to 1 GHz	+13 dBm	+17 dBm
> 1 GHz to 3 GHz	+10 dBm	+16 dBm
> 3 GHz to 4 GHz	+7 dBm	+13 dBm
> 4 GHz to 6 GHz (Option 506)	N/A	+10 dBm

- If the instrument cannot achieve the maximum power levels listed in [Table 1-33](#), perform the ALC Calibration and the Power Level Accuracy, High Power Calibration (refer to the *Calibration Guide*).
- If these adjustments fail or the instrument still cannot achieve the maximum power levels listed in [Table 1-33](#), proceed to [“Troubleshooting the RF Path”](#) on page 1-75.

❑ Check for an Unleveled Annunciator

— If the Unleveled Annunciator is on:

1. Check that the signal generator's amplitude is not set higher than the maximum level specified on the *Specifications* data sheet.
  - If the unleveled indication turns off after resetting the amplitude, the signal generator is operating correctly.
2. Check that the signal generator's RF output is terminated into 50 ohms.
  - If the unleveled indication turns off after terminating the RF output into 50 ohms, the signal generator is operating correctly.

— If the Unleveled Annunciator remains on:

1. Use a spectrum analyzer to check the RF signal level at the RF Output connector.

— *Signal Generator Setup*

- a. Set **ALC On/Off** to Off
- b. Set **Amplitude** to +25 dBm
- c. Set **Modulation** to Off
- d. Set **RF On/Off** to On
- e. Set **Sweep** to Frequency
- f. Set **Sweep Type** to Step
- g. Set **Start Frequency** to 250 kHz
- h. Set **Stop Frequency** to 1 GHz
- i. Set **Number of Points** to 500

— *Spectrum Analyzer Setup*

- a. Set **Start Frequency** to 225 kHz
- b. Set **Stop Frequency** to 1.25 GHz
- c. Set **Reference Level** to +30 dBm
- d. Set **Display** to Max Hold

2. Connect the RF output of the signal generator to the spectrum analyzer.
3. Measure and record the minimum power level.
4. Set the signal generator and spectrum analyzer start/stop frequencies to the next start/stop frequencies in [Table 1-34](#).

**Table 1-34**

Signal Generator		Spectrum Analyzer	
Start	Stop	Start	Stop
1 GHz	3 GHz	999 MHz	3.1 GHz
3 GHz	4 GHz	2.99 GHz	4.1 GHz
4.000000001 GHz	6 GHz	4 GHz	6.1 GHz

5. Measure and record the minimum power level at each start/stop frequency.
  - If any of the frequency ranges do not produce power levels equal to the maximum leveled power shown in [Table 1-33](#), set the signal generator to the frequency with the lowest power level and measure the power with a power meter.
  - If the power level is low, troubleshoot the RF path before proceeding.
  - If the RF signal levels are good, turn ALC On and set the signal generator to maximum-leveled power.

Note the frequencies where the unlevelled condition occur. Later, when troubleshooting in ALC Off mode, the unlevelled indication is turned off.

For each of the unlevelled conditions listed, always start with the signal generator set to the following settings.

- a. Set **ALC Mode** to ALC Off
- b. Set **Amplitude** to +25 dBm
- c. Set **Attenuator Hold Mode** to On
- d. Set **RF On/Off** to On
- e. Set **Modulation** to Off

### Troubleshooting the RF Path

Depending on the frequency range that an unlevel is occurring, refer to one of the following:

- Unlevelled or Low Power for Frequencies  $\geq 250$  kHz and  $< 250$  MHz on [page 1-75](#)
- Unlevelled or Low Power for Frequencies  $\geq 250$  MHz and  $\leq 4$  GHz on [page 1-76](#)
- Unlevelled or Low Power for Frequencies  $> 4$  GHz and  $\leq 6$  GHz (Option 506 Only) on [page 1-77](#)

### Unlevelled or Low Power for Frequencies $\geq 250$ kHz and $< 250$ MHz

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**NOTE** Before proceeding to troubleshoot with this section, run a full self-test and correct any reported failures and unlock problems. (Refer to “[Self-Test Failures and Related Troubleshooting](#)” on [page 1-17](#) and “[Troubleshooting Unlocks](#)” on [page 1-68](#).)

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1. Set the signal generator to the frequency where the signal generator is unlevelled.
2. With the signal generator powered on, carefully remove the A13 Output.
3. Use a semi-rigid cable extender and connect a spectrum analyzer to the A16 Frac-N (Option UNJ or Option 506) or A17 Synthesizer (Option 501, 502, 503, 504) RF output at J4 of the A13 Output and measure the power level.

The amplitude of the signal at J4 should be  $\geq 5$  dBm and the RF frequency at J4 is determined by the following equation:

$$J4 \text{ Frequency} = 1 \text{ GHz} - \text{Front Panel RF Frequency}$$

- If the J4 signal is good, replace the A13 Output.
- If the J4 signal is bad and the instrument is an:
  - Option 501, 502, 503, 504, replace the A17 Synthesizer (Option 501, 502, 503, 504)
  - Option UNJ or Option 506, check the signal leaving the YIG oscillator.

The YIG frequency is determined by the equation:

$$YIG \text{ Frequency} = (1 \text{ GHz} - \text{Front Panel RF Frequency}) * 8$$

- If the signal is good out of the YIG, replace the A16 Frac-N (Option UNJ or Option 506).
- If the signal is bad out of the YIG, replace the A21 YTO Driver (Option UNJ or Option 506).
- Option 506, check the signal at the input of the A22 Coupler (Option 506).
- If the signal is good, replace the A22 Coupler (Option 506).
- If the signal is bad, check the signal leaving the YIG oscillator.

The YIG frequency is determined by the equation:

$$YIG \text{ Frequency} = (1 \text{ GHz} - \text{Front Panel RF Frequency}) * 8$$

- If the signal is good, replace the semi-rigid cable (W54) between the YIG Oscillator and the input of the A22 Coupler (Option 506)
- If the signal is bad, replace the A21 YTO Driver (Option UNJ or Option 506).

**Unleveled or Low Power for Frequencies  $\geq$  250 MHz and  $\leq$  4 GHz**

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**NOTE** Before proceeding to troubleshoot with this section, run a full self-test and correct any reported failures and unlock problems. (Refer to [“Self-Test Failures and Related Troubleshooting”](#) on page 1-17 and [“Troubleshooting Unlocks”](#) on page 1-68.)

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1. Set the signal generator to the unleveled frequency.
2. With the signal generator powered on, carefully remove the A13 Output.
3. Use a semi-rigid cable extender and connect a spectrum analyzer to the A16 Frac-N (Option UNJ or Option 506) or A17 Synthesizer (Option 501, 502, 503, 504) RF output at J4 of the A13 Output.

The signal at J4 should be the same frequency that the signal generator is set to and the amplitude at J4 should be  $\geq$  0 dBm.

- If the J4 signal is good, replace the A13 Output.
- If the J4 signal is bad and the instrument is an:
  - Option 501, 502, 503, 504, change the A17 Synthesizer (Option 501, 502, 503, 504)
  - Option UNJ, check the signal leaving the YIG Oscillator.

The YIG frequency will be higher than the front panel frequency. It is divided down with the divisor being dependent on the front panel frequency.

- If the signal is good, change the A16 Frac-N (Option UNJ or Option 506).
- If the signal is bad out of the YIG, change the A21 YTO Driver (Option UNJ or Option 506)
- Option 506, check the signal at the input of the A22 Coupler (Option 506).
- If the signal is good, replace the A22 Coupler (Option 506).
- If the signal is bad, check the signal leaving the YIG Oscillator.



The YIG frequency will be higher than the front panel frequency. It is divided down with the divisor being dependent on the front panel frequency.

- If the signal is good, replace the semi-rigid cable (W54) between the YIG Oscillator and the input of the A22 Coupler (Option 506).

(Refer to “[Option 506 - Semi-Rigid Cables includes the A21 YTO Driver \(Option UNJ or Option 506\) and the AT1 High-Power Mechanical Attenuator \(Option UNB\) with an A29 DC Blocking Capacitor \(Option 506\)](#)” on page 3-53.)

- If the signal is bad, change the A21 YTO Driver (Option UNJ or Option 506).

### **Unleveled or Low Power for Frequencies > 4 GHz and ≤ 6 GHz (Option 506 Only)**

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**NOTE** Before proceeding to troubleshoot with this section, run a full self-test and correct any reported failures and unlock problems. (Refer to “[Self-Test Failures and Related Troubleshooting](#)” on page 1-17 and “[Troubleshooting Unlocks](#)” on page 1-68.)

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1. Set the signal generator to the unleveled frequency.
2. With the signal generator powered on, carefully remove the A14 Extended Frequency Output (Option 506).
3. Use a semi-rigid cable extender and connect a spectrum analyzer to the A22 Coupler (Option 506) on the coupled output at J4 of the A14 Extended Frequency Output (Option 506).

The signal at J4 should be the same frequency that the signal generator is set to and the amplitude at J4 should be  $\geq -8$  dBm.

- If the J4 signal is good, replace the A14 Extended Frequency Output (Option 506).
- If the J4 signal is bad, measure the signal at the cable (W54) connected to the input of the A22 Coupler (Option 506).

- If the signal is good, change the A22 Coupler (Option 506).

- If the signal is bad, check the signal leaving the YIG Oscillator.

The YIG frequency will be the same as the front panel frequency.

- If the signal is good, replace the semi-rigid cable (W54) between the YIG Oscillator and the input of the A22 Coupler (Option 506).

(Refer to “[Option 506 - Semi-Rigid Cables includes the A21 YTO Driver \(Option UNJ or Option 506\) and the AT1 High-Power Mechanical Attenuator \(Option UNB\) with an A29 DC Blocking Capacitor \(Option 506\)](#)” on page 3-53.)

- If the signal is bad, change the A21 YTO Driver (Option UNJ or Option 506).

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## Troubleshooting the AT1 Electronic Attenuator (Option 501, 502, 503, 504 or Option UNJ)

The AT1 Electronic Attenuator (Option 501, 502, 503, 504 or Option UNJ) has an attenuation range of 130 dB in 5 dB steps. This attenuator is used to coarse-tune the power level while the Automatic Leveling Control (ALC) loop is used to fine-tune the power level; the ALC fine-tunes the power level within each 5 dB step.

The attenuator is set to 0 dB attenuation for power levels from 0 dBm to +25 dBm. The first 5 dB step is switched in at approximately  $-0.5$  dBm, the second step is switched in at approximately  $-5.5$  dBm, the third step is switched in at approximately  $-10.5$  dBm; this pattern repeats until the last 5 dB step is switched in at approximately  $-125.5$  dBm. Attenuator control comes from the A23 Motherboard through A23J25.

**Table 1-35**

Power Supply Voltage (Vdc)	Connector Pins on the A23 Motherboard	Minimum Value (Vdc)	Maximum Value (Vdc)	Origin
+12	J25-14	+12.10	+12.90	A23 Motherboard
-12	J25-13	-12.90	-12.10	A23 Motherboard
+5	J25-15	+4.5	+5.356	Main Supply

- Check the power supply voltage on A23J25.
  - If the power supply voltage on A23J25 is good:
    1. Set the amplitude to +0 dBm.
    2. Set the amplitude increment to 5 dB.
    3. Set the **RF On/Off** to RF On.
    4. Set the amplitude level using the Down arrow key.
    5. Use [Table 1-38](#) to isolate the problem.
    - If the attenuator control line voltages are correct for the given amplitude setting, check the attenuator ribbon cable (W57):
      - If the attenuator ribbon cable is good, replace the AT1 Electronic Attenuator (Option 501, 502, 503, 504 or Option UNJ).
      - If the attenuator ribbon cable is bad, replace W57.
    - If the attenuator control line voltages are wrong for the given amplitude setting, check the that the control voltages change within a 5 dB range (Refer to [Table 1-38](#)):
      - If the attenuator control line voltages change within 5 dB, return the unit back to Agilent Technologies for calibration.
      - If the attenuator control line voltages do not change within 5 dB, replace the A23 Motherboard.  
(Refer to “[Self-Tests 21xx for the A23 Motherboard](#)” on page 1-62.)
  - If the power supply voltage on A23J25 is bad, troubleshoot the A6 Power Supply.  
(Refer to “[Power Supplies](#)” on page 1-20.)

**NOTE** Table 1-36 shows the amplitude levels and the corresponding attenuator control line voltages. The amplitude levels are chosen so that they are approximately in the center of each 5 dB switch point. Due to calibration variations, the absolute range of each 5 dB attenuation step may have to be re-established. This can be accomplished by decreasing the power setting while monitoring A23J25 pin 9. Note the power setting when the control line goes TTL-high (+5 V) and continue decreasing the power and noting the power setting when the control line goes TTL low (0 V). The difference in the two settings is the new range. Once a new range is established, use the mid-point of each new range as the amplitude settings in place of the settings in Table 1-36.

**Table 1-36 Attenuator Control Lines and Corresponding Amplitude Levels**

A23J25 Pin Numbers and Voltages							
Amplitude Level (dBm)	Pin 4 5 dB Step	Pin 9 5 dB Step	Pin 6 10 dB Step	Pin 7 10 dB Step	Pin 3 20 dB Step	Pin 5 40 dB Step	Pin 10 40 dB Step
+0	0	0	0	0	0	0	0
-5	+5	0	0	0	0	0	0
-10	+5	+5	0	0	0	0	0
-15	+5	0	+5	0	0	0	0
-20	+5	+5	+5	0	0	0	0
-25	+5	0	+5	+5	0	0	0
-30	+5	+5	+5	+5	0	0	0
-35	+5	0	+5	0	+5	0	0
-40	+5	+5	+5	0	+5	0	0
-45	+5	0	+5	+5	+5	0	0
-50	+5	+5	+5	+5	+5	0	0
-55	+5	0	+5	0	0	+5	0
-60	+5	+5	+5	0	0	+5	0
-65	+5	0	+5	+5	0	+5	0
-70	+5	+5	+5	+5	0	+5	0
-75	+5	0	+5	0	+5	+5	0
-80	+5	+5	+5	0	+5	+5	0
-85	+5	0	+5	+5	+5	+5	0
-90	+5	+5	+5	+5	+5	+5	0
-95	+5	0	+5	0	0	+5	+5
-100	+5	+5	+5	0	0	+5	+5

**Table 1-36 Attenuator Control Lines and Corresponding Amplitude Levels (Continued)**

-105	+5	0	+5	+5	0	+5	+5
-110	+5	+5	+5	+5	0	+5	+5
-115	+5	0	+5	0	+5	+5	+5
-120	+5	+5	+5	0	+5	+5	+5
-125	+5	0	+5	+5	+5	+5	+5
-130	+5	+5	+5	+5	+5	+5	+5

## Troubleshooting the **AT1 High-Power Mechanical Attenuator (Option UNB)**

The AT1 High-Power Mechanical Attenuator (Option UNB) has an attenuation range of 130 dB in 5 dB steps. This attenuator is used to coarse-tune the power level while the Automatic Leveling Control (ALC) loop is used to fine-tune the power level; the ALC fine-tunes the power level within each 5 dB step.

The attenuator is set to 0 dB attenuation for power levels from 0 dBm to +25 dBm. The first 5 dB step is switched in at approximately –0.5 dBm, the second step is switched in at approximately –5.5 dBm, the third step is switched in at approximately –10.5 dBm; this pattern repeats until the last 5 dB step is switched in at approximately –125.5 dBm. Attenuator control comes from the A23 Motherboard through A23J25.

**Table 1-37**

Power Supply Voltage (Vdc)	Connector Pins on A23 Motherboard	Minimum Value (Vdc)	Maximum Value (Vdc)	Origin
+15	J25-1	+14.1	+15.9	Main Supply

- ❑ Check the power supply voltage on A23J25 pin 1.
  - If the power supply voltage on A23J25 pin 1 is good:
    1. Set the amplitude to +2 dBm.
    2. Set the amplitude increment to 5 dB.
    3. Set the **RF On/Off** to RF On.
    4. Set the amplitude level using the Down arrow key.
    5. Use [Table 1-38](#) to isolate the problem.
      - If the attenuator control line voltages are correct for the given amplitude setting, check the attenuator ribbon cable (W57):
        - If the attenuator ribbon cable is good, replace the AT1 High-Power Mechanical Attenuator (Option UNB).
        - If the attenuator ribbon cable is bad, replace W57.
      - If the attenuator control line voltages are wrong for the given amplitude setting, check the that the control voltages change within a 5 dB range (Refer to [Table 1-38](#)):
        - If the attenuator control line voltages change within 5 dB, return the unit back to Agilent Technologies for calibration.
        - If the attenuator control line voltages do not change within 5 dB, replace the A23 Motherboard. (Refer to “[Self-Tests 21xx for the A23 Motherboard](#)” on page 1-62.)
  - If the power supply voltage on A23J25 pin 1 is bad, troubleshoot the A6 Power Supply. (Refer to “[Power Supplies](#)” on page 1-20.)

**NOTE** [Table 1-38](#) shows the amplitude levels and the corresponding attenuator control line voltages. The amplitude levels are chosen so that they are approximately in the center of each 5 dB switch point. Due to calibration variations, the absolute range of each 5 dB attenuation step may have to be re-established. This can be accomplished by decreasing the power setting while monitoring A23J25 pin 10. Note the power setting when the control line goes TTL-high (+5 V) and continue decreasing the power and noting the power setting when

the control line goes TTL low (0 V). The difference in the two settings is the new range. Once a new range is established, use the mid-point of each new range as the amplitude settings in place of the settings in [Table 1-38](#).

**Table 1-38 Attenuator Control Lines and Corresponding Amplitude Levels**

A23J25 Pin Numbers and Voltages					
Amplitude Level (dBm)	Pin 10 5 dB Step	Pin 9 10 dB Step	Pin 6 20 dB Step	Pin 7 40 dB Step	Pin 5 60 dB Step
+2	0	0	0	0	0
-3	+5	0	0	0	0
-8	0	+5	0	0	0
-13	+5	+5	0	0	0
-18	0	0	+5	0	0
-23	+5	0	+5	0	0
-28	0	+5	+5	0	0
-33	+5	+5	+5	0	0
-38	0	0	0	+5	0
-43	+5	0	0	+5	0
-48	0	+5	0	+5	0
-53	+5	+5	0	+5	0
-58	0	0	0	0	+5
-63	+5	0	0	0	+5
-68	0	+5	0	0	+5
-73	+5	+5	0	0	+5
-78	0	0	+5	0	+5
-83	+5	0	+5	0	+5
-88	0	+5	+5	0	+5
-93	+5	+5	+5	0	+5
-98	0	0	0	+5	+5
-103	+5	0	0	+5	+5
-108	0	+5	0	+5	+5
-113	+5	+5	0	+5	+5
-118	0	0	+5	+5	+5
-123	+5	0	+5	+5	+5
-128	0	+5	+5	+5	+5

## Troubleshooting Adjustment Problems

When an adjustment does not work or fails to resolve a problem and all self-tests have passed, use the following table to locate the most likely failure.

**Table 1-39 Troubleshooting Adjustment Problems**

<b>The following adjustments are listed in the order that they should be performed for proper calibration.</b>	
<b>Adjustment that is failing...</b>	<b>Assemblies most likely causing failure...</b>
Analog Bus ADC Calibration	<ul style="list-style-type: none"> <li>• A12 CPU</li> <li>• Refer to “<a href="#">Troubleshooting Analog Bus ADC Calibration Failures</a>” on page 1-85</li> </ul>
Pretune Calibration (Option UNJ or Option 506 Only)	<ul style="list-style-type: none"> <li>• A21 YTO Driver (Option UNJ or Option 506)</li> <li>• A16 Frac-N (Option UNJ or Option 506)</li> </ul>
Internal Source Calibration	<ul style="list-style-type: none"> <li>• A23 Motherboard</li> <li>• A18 Reference</li> </ul>
VCO Bias Potentiometer Calibration	<ul style="list-style-type: none"> <li>• A17 Synthesizer (Option 501, 502, 503, 504)</li> <li>• A16 Frac-N (Option UNJ or Option 506)</li> </ul>
Lock Angle Potentiometer Calibration (Not Used with Option UNJ or Option 506)	<ul style="list-style-type: none"> <li>• A17 Synthesizer (Option 501, 502, 503, 504)</li> </ul>
KV versus Frequency Calibration	<ul style="list-style-type: none"> <li>• A17 Synthesizer (Option 501, 502, 503, 504)</li> <li>• A16 Frac-N (Option UNJ or Option 506)</li> </ul>
Timebase DAC Calibration	<ul style="list-style-type: none"> <li>• A18 Reference</li> </ul>
FM Scale DAC Offset Calibration	<ul style="list-style-type: none"> <li>• A18 Reference</li> </ul>
FM Path Offset Calibration	<ul style="list-style-type: none"> <li>• A18 Reference</li> </ul>
FM In-Band DAC Offset Calibration	<ul style="list-style-type: none"> <li>• A16 Frac-N (Option UNJ or Option 506)</li> <li>• A17 Synthesizer (Option 501, 502, 503, 504)</li> </ul>
FM Inverting Amplifier Offset Calibration	<ul style="list-style-type: none"> <li>• A16 Frac-N (Option UNJ or Option 506)</li> <li>• A17 Synthesizer (Option 501, 502, 503, 504)</li> </ul>
FM 1/2 Path Ratio Gain Calibration	<ul style="list-style-type: none"> <li>• A18 Reference</li> </ul>
Modulation Source Relative Gain Calibration	<ul style="list-style-type: none"> <li>• A18 Reference</li> <li>• A23 Motherboard</li> </ul>
FM Out-of-Band Calibration (Not Used with Option UNJ or Option 506)	<ul style="list-style-type: none"> <li>• A16 Frac-N (Option UNJ or Option 506)</li> <li>• A21 YTO Driver (Option UNJ or Option 506)</li> </ul>
FM/PM YO Frequency Compensation Calibration (Option UNJ or Option 506 Only)	<ul style="list-style-type: none"> <li>• A21 YTO Driver (Option UNJ or Option 506)</li> </ul>
DCFM Calibration	<ul style="list-style-type: none"> <li>• A16 Frac-N (Option UNJ or Option 506)</li> <li>• A17 Synthesizer (Option 501, 502, 503, 504)</li> </ul>
Low Frequency (LF) Output Calibration	<ul style="list-style-type: none"> <li>• A18 Reference</li> <li>• A23 Motherboard</li> </ul>
External Input Peak Detector Calibration	<ul style="list-style-type: none"> <li>• A18 Reference</li> </ul>
Burst Modulator Calibration	<ul style="list-style-type: none"> <li>• A13 Output</li> <li>• A14 Extended Frequency Output (Option 506)</li> </ul>

**Table 1-39 Troubleshooting Adjustment Problems (Continued)**

<b>The following adjustments are listed in the order that they should be performed for proper calibration.</b>	
<b>Adjustment that is failing...</b>	<b>Assemblies most likely causing failure...</b>
Burst Audio Path Gain Calibration	<ul style="list-style-type: none"> <li>• A13 Output</li> <li>• A14 Extended Frequency Output (Option 506)</li> <li>• A18 Reference</li> </ul>
AM Audio Path Offset Calibration	<ul style="list-style-type: none"> <li>• A18 Reference</li> </ul>
Prelevel Calibration	<ul style="list-style-type: none"> <li>• A13 Output</li> <li>• A14 Extended Frequency Output (Option 506)</li> </ul>
VBLO Mixer Bias Calibration	<ul style="list-style-type: none"> <li>• A13 Output</li> <li>• A14 Extended Frequency Output (Option 506)</li> </ul>
Digital Gain Adjust Calibration	<ul style="list-style-type: none"> <li>• A13 Output</li> <li>• A14 Extended Frequency Output (Option 506)</li> </ul>
Bypass Gain Adjust Calibration	<ul style="list-style-type: none"> <li>• A13 Output</li> <li>• A14 Extended Frequency Output (Option 506)</li> </ul>
ALC Calibration	<ul style="list-style-type: none"> <li>• A13 Output</li> <li>• A14 Extended Frequency Output (Option 506)</li> </ul>
Power Level Accuracy, High Power Calibration	<ul style="list-style-type: none"> <li>• AT1 High-Power Mechanical Attenuator (Option UNB)</li> <li>• A28 Reverse Power Protection (Option 501, 502, 503, 504 or Option UNJ)</li> <li>• A13 Output (Option UNB)</li> <li>• A14 Extended Frequency Output (Option 506)</li> <li>• A29 DC Blocking Capacitor (Option 506)</li> </ul>
ALC Modulation Driver Bias Calibration	<ul style="list-style-type: none"> <li>• A13 Output</li> <li>• A14 Extended Frequency Output (Option 506)</li> </ul>
Power Level Accuracy, Low Power Calibration	<ul style="list-style-type: none"> <li>• AT1 High-Power Mechanical Attenuator (Option UNB)</li> <li>• A28 Reverse Power Protection (Option 501, 502, 503, 504 or Option UNJ)</li> <li>• Loose or leaky RF cables.</li> </ul>
Power Search Calibration	<ul style="list-style-type: none"> <li>• A13 Output</li> <li>• A14 Extended Frequency Output (Option 506)</li> </ul>
AM Gain Calibration	<ul style="list-style-type: none"> <li>• A13 Output</li> <li>• A14 Extended Frequency Output (Option 506)</li> </ul>
I/Q Gain/Offset/Quadrature Calibration	<ul style="list-style-type: none"> <li>• A10 I/Q Multiplexer</li> <li>• A13 Output (Option 501, 502, 503, 504 or Option UNJ)</li> <li>• A14 Extended Frequency Output (Option 506)</li> <li>• A7 Baseband Generator (Option 001/601 or Option 002/602)</li> </ul>
I/Q Impairment Calibration	<ul style="list-style-type: none"> <li>• A10 I/Q Multiplexer</li> <li>• A13 Output</li> <li>• A14 Extended Frequency Output (Option 506)</li> </ul>



## Troubleshooting Analog Bus ADC Calibration Failures

The Analog Bus ADC Calibration adjusts the ADC reference voltage to match the A12 CPU +10 Vdc reference.

There are three main reasons that this calibration could fail:

- problems with the +10 Vdc reference
- A12 CPU problems
- an analog multiplexer on another assembly is starting to fail

**Depending on the option of your signal generator, perform one of the following:**

- [Procedure for Signal Generators with Option 501, 502, 503, 504 on page 1-85](#)
- [Procedure for Signal Generators with Option UNJ on page 1-86](#)
- [Procedure for Signal Generators with Option 506 on page 1-87](#)

### Procedure for Signal Generators with Option 501, 502, 503, 504

- Run Self-Test 1100.
  - If Self-Test 1100 passes:
    - a. Turn the power off to the signal generator and remove the A17 Synthesizer (Option 501, 502, 503, 504) and A18 Reference.
    - b. Using service software, run the Analog Bus ADC Calibration.
      - If the calibration passes:
        - a. Turn the power off to the signal generator.
        - b. Re-install the A17 Synthesizer (Option 501, 502, 503, 504).
        - c. Re-run the Analog Bus ADC Calibration.
          - If the calibration passes, replace the A18 Reference.
          - If the calibration fails, replace the A17 Synthesizer (Option 501, 502, 503, 504).
      - If the calibration fails:
        - a. Turn the power off to the signal generator.
        - b. Re-install both the A17 Synthesizer (Option 501, 502, 503, 504) and A18 Reference.
        - c. Remove the A13 Output.
        - d. Re-run the Analog Bus ADC Calibration.
          - If the calibration passes, replace the A13 Output.
          - If the calibration fails, replace the A12 CPU.
  - If Self-Test 1100 fails, refer to [“Self-Tests 11xx for the A12 CPU” on page 1-49](#).

## Procedure for Signal Generators with Option UNJ

- Run Self-Test 1100.
  - If Self-Test 1100 passes:
    - a. Turn the power off to the signal generator and remove the A15 Sampler (Option UNJ or Option 506), A16 Frac-N (Option UNJ or Option 506), and A18 Reference.
    - b. Using service software, run the Analog Bus ADC Calibration.
      - If the calibration passes:
        - a. Turn the power off to the signal generator.
        - b. Re-install the A15 Sampler (Option UNJ or Option 506).
        - c. Re-run the Analog Bus ADC Calibration.
          - If the calibration passes:
            - a. Re-install the A16 Frac-N (Option UNJ or Option 506).
            - b. Re-run the calibration.
              - If the calibration passes, replace the A18 Reference.
              - If the calibration fails, replace the A16 Frac-N (Option UNJ or Option 506).
          - If the calibration fails, replace the A15 Sampler (Option UNJ or Option 506).
    - If the calibration fails:
      - a. Turn the power off to the signal generator.
      - b. Re-install the A15 Sampler (Option UNJ or Option 506), A16 Frac-N (Option UNJ or Option 506), and A18 Reference.
      - c. Remove the A13 Output.
      - d. Re-run the Analog Bus ADC Calibration.
        - If the calibration passes, replace the A13 Output.
        - If the calibration fails, replace the A12 CPU.
  - If Self-Test 1100 fails, refer to [“Self-Tests 11xx for the A12 CPU” on page 1-49](#).

## Procedure for Signal Generators with Option 506

- Run Self-Test 1100.
  - If Self-Test 1100 passes:
    - a. Turn the power off to the signal generator and remove the A15 Sampler (Option UNJ or Option 506), A16 Frac-N (Option UNJ or Option 506), and A18 Reference.
    - b. Using service software, run the Analog Bus ADC Calibration.
      - If the calibration passes:
        - a. Turn the power off to the signal generator.
        - b. Re-install the A15 Sampler (Option UNJ or Option 506).
        - c. Re-run the Analog Bus ADC Calibration.
          - If the calibration passes:
            - a. Re-install the A16 Frac-N (Option UNJ or Option 506).
            - b. Re-run the calibration.
              - If the calibration passes, replace the A18 Reference.  
(Refer to [“A18 Reference” on page 2-47.](#))
              - If the calibration fails, replace the A16 Frac-N (Option UNJ or Option 506).  
(Refer to [“A16 Frac-N \(Option UNJ or Option 506\)” on page 2-43.](#))
        - If the calibration fails, replace the A15 Sampler (Option UNJ or Option 506).  
(Refer to [“A15 Sampler \(Option UNJ or Option 506\)” on page 2-41.](#))
  - If the calibration fails:
    - a. Turn the power off to the signal generator.
    - b. Re-install the A15 Sampler (Option UNJ or Option 506), A16 Frac-N (Option UNJ or Option 506), and A18 Reference.
    - c. Remove the A13 Output and A14 Extended Frequency Output (Option 506).
    - d. Re-run the Analog Bus ADC Calibration.
      - If the calibration passes:
        - a. Re-install the A13 Output.
        - b. Re-run the calibration.
          - If the calibration passes, replace the A14 Extended Frequency Output (Option 506).  
(Refer to [“A14 Extended Frequency Output \(Option 506\)” on page 2-39.](#))
          - If the calibration fails, replace the A13 Output.  
(Refer to [“A13 Output” on page 2-37.](#))
      - If the calibration fails, replace the A12 CPU.  
(Refer to [“A12 CPU” on page 2-35.](#))
- If Self-Test 1100 fails, refer to [“Self-Tests 11xx for the A12 CPU” on page 1-49.](#)

## Troubleshooting Performance Test Problems

Performance test that is failing...	Action to perform...
<p>Internal AM Accuracy</p> <p>This is part of the Internal AM Accuracy and Distortion performance test.</p> <p>Note</p> <p>When a performance test fails and all self-tests have passed, use this table to locate the most likely cause. These performance tests are listed in the order that they should be performed to minimize changes in test equipment configurations.</p>	<ul style="list-style-type: none"> <li>• Perform: <ul style="list-style-type: none"> <li>— Internal Source Calibration</li> <li>— All AM Adjustments</li> </ul> </li> <li>• If still failing and: <ul style="list-style-type: none"> <li>— problem is at carrier frequencies <math>\leq 4</math> GHz, troubleshoot the following assemblies: <ul style="list-style-type: none"> <li>— A18 Reference</li> <li>— A13 Output</li> <li>— A23 Motherboard</li> </ul> </li> <li>— problem is only at carrier frequencies <math>&gt; 4</math> GHz, troubleshoot the following assemblies: <ul style="list-style-type: none"> <li>— A14 Extended Frequency Output (Option 506)</li> </ul> </li> </ul> </li> </ul>
<p>Internal FM Accuracy</p> <p>This is part of the Internal FM Accuracy and Distortion performance test.</p>	<ul style="list-style-type: none"> <li>• Perform: <ul style="list-style-type: none"> <li>— Internal Source Calibration</li> <li>— KV versus Frequency Calibration</li> <li>— All FM adjustments</li> </ul> </li> <li>• If still failing, troubleshoot the following assemblies: <ul style="list-style-type: none"> <li>— A17 Synthesizer (Option 501, 502, 503, 504) or A16 Frac-N (Option UNJ or Option 506)</li> <li>— A18 Reference</li> <li>— A23 Motherboard</li> </ul> </li> </ul>
<p>Internal PM Accuracy</p> <p>This is part of the Phase Modulation Accuracy and Distortion performance test.</p>	<ul style="list-style-type: none"> <li>• Perform: <ul style="list-style-type: none"> <li>— Internal FM Accuracy and Distortion</li> </ul> </li> <li>• If still failing, troubleshoot the following assemblies: <ul style="list-style-type: none"> <li>— A17 Synthesizer (Option 501, 502, 503, 504) or A16 Frac-N (Option UNJ or Option 506)</li> <li>— A18 Reference</li> </ul> </li> </ul>

Performance test that is failing...	Action to perform...
<p>Internal AM Distortion</p> <p>This is part of the Internal AM Accuracy and Distortion performance test.</p>	<ul style="list-style-type: none"> <li>• Perform:               <ul style="list-style-type: none"> <li>— Internal AM Accuracy (This is part of the Internal AM Accuracy and Distortion performance test.)</li> <li>— All AM Adjustments</li> </ul> </li> <li>• If still failing and:               <ul style="list-style-type: none"> <li>— problem is at carrier frequencies <math>\leq 4</math> GHz, troubleshoot the following assemblies:                   <ul style="list-style-type: none"> <li>— A18 Reference</li> <li>— A13 Output</li> <li>— A23 Motherboard</li> </ul> </li> <li>— problem is only at carrier frequencies <math>&gt; 4</math> GHz, troubleshoot the following assemblies:                   <ul style="list-style-type: none"> <li>— A14 Extended Frequency Output (Option 506)</li> </ul> </li> </ul> </li> </ul>
<p>Internal FM Distortion</p> <p>This is part of the Internal FM Accuracy and Distortion performance test.</p>	<ul style="list-style-type: none"> <li>• Perform:               <ul style="list-style-type: none"> <li>— Internal FM Accuracy (This is part of the Internal FM Accuracy and Distortion performance test.)</li> </ul> </li> <li>• If still failing, troubleshoot the following assemblies:               <ul style="list-style-type: none"> <li>— A17 Synthesizer (Option 501, 502, 503, 504) or A16 Frac-N (Option UNJ or Option 506)</li> <li>— A18 Reference</li> <li>— A23 Motherboard</li> </ul> </li> </ul>
<p>Internal PM Distortion</p> <p>This is part of the Phase Modulation Accuracy and Distortion performance test.</p>	<ul style="list-style-type: none"> <li>• Perform:               <ul style="list-style-type: none"> <li>— Internal PM Accuracy (This is part of the Phase Modulation Accuracy and Distortion performance test.)</li> </ul> </li> <li>• If still failing, troubleshoot the following assemblies:               <ul style="list-style-type: none"> <li>— A17 Synthesizer (Option 501, 502, 503, 504) or A16 Frac-N (Option UNJ or Option 506)</li> <li>— A18 Reference</li> </ul> </li> </ul>

Performance test that is failing...	Action to perform...
AM Frequency Response	<ul style="list-style-type: none"> <li>• Perform:                             <ul style="list-style-type: none"> <li>— Internal AM Accuracy (This is part of the Internal AM Accuracy and Distortion performance test.)</li> </ul> </li> <li>• If still failing and:                             <ul style="list-style-type: none"> <li>— problem is at carrier frequencies <math>\leq</math> 4 GHz, troubleshoot the following assemblies:                                     <ul style="list-style-type: none"> <li>— A13 Output</li> </ul> </li> <li>— problem is only at carrier frequencies <math>&gt;</math> 4 GHz, troubleshoot the following assemblies:                                     <ul style="list-style-type: none"> <li>— A14 Extended Frequency Output (Option 506)</li> </ul> </li> </ul> </li> </ul>
FM Frequency Response	<ul style="list-style-type: none"> <li>• Perform:                             <ul style="list-style-type: none"> <li>— Internal FM Accuracy (This is part of the Internal FM Accuracy and Distortion performance test.)</li> </ul> </li> <li>• If still failing, troubleshoot the following assemblies:                             <ul style="list-style-type: none"> <li>— A17 Synthesizer (Option 501, 502, 503, 504) or A16 Frac-N (Option UNJ or Option 506)</li> </ul> </li> </ul>
Phase Modulation Frequency Response	<ul style="list-style-type: none"> <li>• Perform:                             <ul style="list-style-type: none"> <li>— Internal PM Accuracy (This is part of the Phase Modulation Accuracy and Distortion performance test.)</li> </ul> </li> <li>• If still failing, troubleshoot the following assemblies:                             <ul style="list-style-type: none"> <li>— A17 Synthesizer (Option 501, 502, 503, 504) or A16 Frac-N (Option UNJ or Option 506)</li> </ul> </li> </ul>
DCFM Frequency Offset Relative to CW	<ul style="list-style-type: none"> <li>• Perform:                             <ul style="list-style-type: none"> <li>— DCFM Calibration</li> </ul> </li> <li>• If still failing, troubleshoot the following assemblies:                             <ul style="list-style-type: none"> <li>— A18 Reference</li> <li>— A23 Motherboard</li> </ul> </li> </ul>

Performance test that is failing...	Action to perform...
Residual FM (Not Used with Option UNJ or Option 506)	<ul style="list-style-type: none"> <li>• Perform:               <ul style="list-style-type: none"> <li>— No adjustments.</li> </ul> </li> <li>• If failing, troubleshoot the following assemblies:               <ul style="list-style-type: none"> <li>— A18 Reference</li> <li>— A17 Synthesizer (Option 501, 502, 503, 504)</li> </ul> </li> </ul>
Phase Noise and Residual FM (Manual Test - Option UNJ or Option 506 Only)	<ul style="list-style-type: none"> <li>• Perform:               <ul style="list-style-type: none"> <li>— No adjustments.</li> </ul> </li> <li>• If failing, refer to:               <ul style="list-style-type: none"> <li>— <a href="#">“Troubleshooting Phase Noise (Option UNJ and Option 506 Only)” on page 1-102</a></li> </ul> </li> </ul>
Harmonics  Part of the Harmonic, Subharmonic, and Nonharmonic Spurious Signals performance test.	<ul style="list-style-type: none"> <li>• Perform:               <ul style="list-style-type: none"> <li>— No adjustments.</li> </ul> </li> <li>• If failing, refer to:               <ul style="list-style-type: none"> <li>— <a href="#">“Troubleshooting Harmonic Spurious” on page 1-97</a></li> </ul> </li> </ul>
Subharmonics  Part of the Harmonic, Subharmonic, and Nonharmonic Spurious Signals performance test.	<ul style="list-style-type: none"> <li>• Perform:               <ul style="list-style-type: none"> <li>— No adjustments.</li> </ul> </li> <li>• If failing, troubleshoot the following assemblies:               <ul style="list-style-type: none"> <li>— A17 Synthesizer (Option 501, 502, 503, 504)</li> </ul> </li> </ul>
Nonharmonics  Part of the Harmonic, Subharmonic, and Nonharmonic Spurious Signals performance test.	<ul style="list-style-type: none"> <li>• Perform:               <ul style="list-style-type: none"> <li>— No adjustments.</li> </ul> </li> <li>• If failing, refer to:               <ul style="list-style-type: none"> <li>— <a href="#">“Troubleshooting Non-Harmonic Spurious” on page 1-100</a></li> </ul> </li> </ul>

Troubleshooting  
**Troubleshooting Performance Test Problems**

Performance test that is failing...	Action to perform...
Power Level Accuracy	<p>If the problem shows up at high power levels <math>\geq -55</math> dBm:</p> <ul style="list-style-type: none"> <li>• Perform: <ul style="list-style-type: none"> <li>— Digital Gain Adjust Calibration</li> <li>— Bypass Gain Adjust Calibration</li> <li>— ALC Modulation Driver Bias Calibration</li> <li>— ALC Calibration</li> <li>— Power Level Accuracy, High Power Calibration</li> </ul> </li> <li>• If still failing, at high power levels <math>\geq -55</math> dBm, refer to: <ul style="list-style-type: none"> <li>— <a href="#">“Troubleshooting Unlevels and the RF Path” on page 1-73</a></li> </ul> </li> </ul> <p>If the problem shows up only at low power levels <math>&lt; -55</math> dBm:</p> <ul style="list-style-type: none"> <li>• Perform: <ul style="list-style-type: none"> <li>— Power Level Accuracy, Low Power Calibration</li> <li>— Check the AT1 step attenuator and all semi-rigid cabling in the RF path.</li> </ul> </li> </ul>
Timebase Aging Rate (Manual Test - Option UNJ, Option 506, or Option 1E5 Only)	<ul style="list-style-type: none"> <li>• Perform: <ul style="list-style-type: none"> <li>— Timebase DAC Calibration</li> </ul> </li> <li>• If failing, troubleshoot the following assemblies: <ul style="list-style-type: none"> <li>— A18 Reference</li> </ul> </li> </ul>
Digital Modulation Level Accuracy	<ul style="list-style-type: none"> <li>• Perform: <ul style="list-style-type: none"> <li>— All I/Q calibrations</li> </ul> </li> <li>• If failing, troubleshoot the following assemblies: <ul style="list-style-type: none"> <li>— A13 Output (fails only <math>\leq 4</math> GHz)</li> <li>— A14 Extended Frequency Output (Option 506) (fails only <math>&gt; 4</math> GHz)</li> </ul> </li> </ul>



Performance test that is failing...	Action to perform...
Internal Digital Modulation Quality (Option 402 Only)	<ul style="list-style-type: none"> <li>• Perform:               <ul style="list-style-type: none"> <li>— All I/Q calibrations</li> <li>— I/Q Modulation Quality (Option 402 Only)</li> </ul> </li> <li>• If still failing, troubleshoot the failure.</li> <li>• If it passes, troubleshoot the following assemblies:               <ul style="list-style-type: none"> <li>— A10 I/Q Multiplexer</li> <li>— A13 Output (fails only <math>\leq 4</math> GHz)</li> <li>— A14 Extended Frequency Output (Option 506) (fails only <math>&gt; 4</math> GHz)</li> </ul> </li> </ul>
Custom I/Q RF Modulation Quality (Option 402 Only)	<ul style="list-style-type: none"> <li>• Perform:               <ul style="list-style-type: none"> <li>— All I/Q calibrations</li> <li>— I/Q Modulation Quality (Option 402 Only)</li> </ul> </li> <li>• If still failing, troubleshoot the failure.</li> <li>• If it passes, troubleshoot the following assemblies:               <ul style="list-style-type: none"> <li>— A10 I/Q Multiplexer</li> <li>— A13 Output (fails only <math>\leq 4</math> GHz)</li> <li>— A14 Extended Frequency Output (Option 506) (fails only <math>&gt; 4</math> GHz)</li> </ul> </li> </ul>
I/Q Modulation Quality (Option 402 Only)	<ul style="list-style-type: none"> <li>• Perform:               <ul style="list-style-type: none"> <li>— All I/Q calibrations</li> </ul> </li> <li>• If still failing, troubleshoot the following assemblies:               <ul style="list-style-type: none"> <li>— A7 Baseband Generator (Option 001/601 or Option 002/602)</li> <li>— A10 I/Q Multiplexer</li> </ul> </li> </ul>

Performance test that is failing...	Action to perform...
<p>Pulse Modulation On/Off Ratio</p>	<ul style="list-style-type: none"> <li>• Perform:                             <ul style="list-style-type: none"> <li>— No adjustments</li> </ul> </li> <li>• If failing, troubleshoot the following assemblies:                             <ul style="list-style-type: none"> <li>— A18 Reference (fails at all frequencies)</li> <li>— A13 Output (fails only <math>\leq 4</math> GHz)</li> <li>— A14 Extended Frequency Output (Option 506) (fails only <math>&gt; 4</math> GHz)</li> </ul> </li> </ul>
<p>Burst Modulation On/Off Ratio</p>	<ul style="list-style-type: none"> <li>• Perform:                             <ul style="list-style-type: none"> <li>— All Burst adjustments</li> </ul> </li> <li>• If failing, troubleshoot the following assemblies:                             <ul style="list-style-type: none"> <li>— A18 Reference (fails at all frequencies)</li> <li>— A13 Output (fails only <math>\leq 4</math> GHz)</li> <li>— A14 Extended Frequency Output (Option 506) (fails only <math>&gt; 4</math> GHz)</li> </ul> </li> </ul>
<p>CDMA Adjacent Channel Power (Not Used with Option UNB or Option 506)</p> <p>Used for Option 401 Only with CDMA 2000.</p>	<ul style="list-style-type: none"> <li>• Perform:                             <ul style="list-style-type: none"> <li>— All I/Q adjustments</li> </ul> </li> <li>• If failing, troubleshoot the following assemblies:                             <ul style="list-style-type: none"> <li>— A7 Baseband Generator (Option 001/601 or Option 002/602) (fails at all frequencies)</li> <li>— A10 I/Q Multiplexer</li> <li>— A13 Output (fails only <math>\leq 4</math> GHz)</li> <li>— A14 Extended Frequency Output (Option 506) (fails only <math>&gt; 4</math> GHz)</li> </ul> </li> </ul>

Performance test that is failing...	Action to perform...
<p>WCDMA Adjacent Channel Power            (Not Used with Option UNB or Option 506)</p> <p>Used for Option 400 Only.</p>	<ul style="list-style-type: none"> <li>• Perform:               <ul style="list-style-type: none"> <li>— All I/Q adjustments</li> </ul> </li> <li>• If failing, troubleshoot the following assemblies:               <ul style="list-style-type: none"> <li>— A7 Baseband Generator (Option 001/601 or Option 002/602) (fails at all frequencies)</li> <li>— A10 I/Q Multiplexer</li> <li>— A13 Output (fails only <math>\leq 4</math> GHz)</li> <li>— A14 Extended Frequency Output (Option 506) (fails only <math>&gt; 4</math> GHz)</li> </ul> </li> </ul>
<p>Alternate Timeslot Power Settling Time            (Not Used with Option UNB or Option 506)</p>	<ul style="list-style-type: none"> <li>• Perform:               <ul style="list-style-type: none"> <li>— No adjustments</li> </ul> </li> <li>• If failing, troubleshoot the following assemblies:               <ul style="list-style-type: none"> <li>— AT1 Electronic Attenuator (Option 501, 502, 503, 504 or Option UNJ)                    or AT1 High-Power Mechanical Attenuator (Option UNB)</li> <li>— A13 Output</li> </ul> </li> </ul>
<p>Dual Arbitrary Waveform Generator Check</p> <p>Used for Option 001/601 or Option 002/602 Only.</p>	<ul style="list-style-type: none"> <li>• Perform:               <ul style="list-style-type: none"> <li>— All I/Q adjustments</li> </ul> </li> <li>• If failing, troubleshoot the following assemblies:               <ul style="list-style-type: none"> <li>— A7 Baseband Generator (Option 001/601 or Option 002/602) (fails at all frequencies)</li> <li>— A10 I/Q Multiplexer</li> </ul> </li> </ul>

**Troubleshooting Performance Test Problems**

Performance test that is failing...	Action to perform...
<p>GSM Loopback BER Check (Option 300 Only)</p>	<ul style="list-style-type: none"> <li>• Perform:                             <ul style="list-style-type: none"> <li>— All I/Q adjustments</li> <li>— Internal Digital Modulation Quality (Option 402 Only)</li> </ul> </li> <li>• If the Internal Digital Modulation Quality (Option 402 Only) performance test passes, troubleshoot the following assemblies:                             <ul style="list-style-type: none"> <li>— A9 Digital Demodulator (Option 501, 502, 503, 504 with Option 300)</li> <li>— A11 Internal Bit Error Rate Analyzer (Option UN7)</li> <li>— A20 Downconverter (Option 501, 502, 503, 504 with Option 300)</li> </ul> </li> </ul>

## Troubleshooting Harmonic Spurious

Harmonics are multiples of the fundamental output frequency produced by a signal generator. Specifically, the second harmonic is two times the output frequency and its amplitude is measured relative to the fundamental (carrier) output frequency's amplitude; the power level of a harmonic is expressed as  $-xx$  dBc.

- If the frequency of a harmonic falls beyond the frequency range of a signal generator, that particular harmonic is not specified or measured.
- If the frequency of a harmonic falls within the frequency range of a signal generator, the harmonic's power level is obtained with the following procedure:
  1. The signal generator and spectrum analyzer are initially set to the same harmonic frequency.
  2. The signal generator is also set to a specific power level and its output peak power level is measured on the spectrum analyzer; this peak power level is the reference level used for the harmonic measurement.
  3. The signal generator is then set to the fundamental frequency and the power level of the fundamental frequency is measured by using **Peak Search** and **Marker Delta**.

This measurement obtains the power level of the harmonic and is expressed as a dBc value; it is the difference between the fundamental peak power level and the harmonic frequency power level.

## Troubleshooting Harmonic Spurious by Fundamental Frequency

Because the RF Output frequency is derived from a mixer on the A13 Output and the A14 Extended Frequency Output (Option 506), harmonic spurious problems are determined by the frequency range of their fundamental frequency.

### Fundamental Frequencies $\leq 250$ MHz

Problems with the RF Output frequency  $\leq 250$  MHz are most likely the A13 Output.

### Fundamental Frequencies $> 250$ MHz and $\leq 3$ GHz

Problems with frequencies  $> 250$  MHz and  $\leq 3$  GHz are best determined by measuring the harmonic levels at both the RF Output and the Coherent Carrier outputs with the following procedures:

- [“To Verify Harmonic Performance at the RF Output” on page 1-98](#)
- [“To Verify Harmonic Performance at the Coherent Carrier Output” on page 1-98](#)

### Fundamental Frequencies $> 3$ GHz

The harmonic frequency is outside the upper frequency limit of the Agilent E4438C ESG vector signal generator and is therefore not specified.

## To Verify Harmonic Performance at the RF Output

1. Signal Generator Settings
  - a. Set the frequency of the *signal generator* to the harmonic frequency being measured. For example, when planning to measure the second harmonic of a 1 GHz signal, set the frequency of the *signal generator* to 2 GHz.
  - b. Set the power level of the *signal generator* to +4 dBm (+7.5 dBm for Option UNB).
2. Spectrum Analyzer Settings
  - a. Connect a *spectrum analyzer* to the RF Output connector of the signal generator.
  - b. Set the frequency of the spectrum analyzer to the harmonic frequency being measured. To continue with our example, set the frequency of the spectrum analyzer to 2 GHz.
  - c. On the *spectrum analyzer*, press the **Peak Search** and **Marker Delta** keys. The Marker should indicate approximately 0 dBm.
3. Signal Generator Settings
  - a. Set the *signal generator* to the fundamental frequency of the harmonic being measured. In this example, because the measured harmonic was at 2 GHz, the *signal generator* should be set to 1 GHz.
4. Spectrum Analyzer Settings
  - a. On the *spectrum analyzer*, read the power level (and record it for future reference in case further troubleshooting is necessary). The Marker should indicate a power level in dBc; this is the power level of the harmonic being measured.
  - b. Compare the power level of the Marker against the specified harmonic performance level for your signal generator.

## To Verify Harmonic Performance at the Coherent Carrier Output

1. Signal Generator Settings
  - a. Set the frequency of the *signal generator* to the fundamental frequency being measured. For example, when planning to measure the second harmonic of a 1 GHz signal (which would be 2 GHz), set the frequency of the *signal generator* to 1 GHz.
  - b. Set the power level of the *signal generator* to +4 dBm (+7.5 dBm for Option UNB).
2. Spectrum Analyzer Settings
  - a. Connect a *spectrum analyzer* to the Coherent Carrier Output connector of the signal generator.
  - b. Set the frequency of the spectrum analyzer to the fundamental frequency being measured. To continue with our example, set the frequency of the spectrum analyzer to 1 GHz.
  - c. Press the **Peak Search** and **Marker Delta** keys. The Marker should indicate approximately 0 dBm.
  - d. Set the frequency of the spectrum analyzer to the harmonic frequency being measured. To continue with our example, set the frequency of the spectrum analyzer to 2 GHz.
  - e. Press the **Peak Search** key and read the power level (and record it for future reference in case further troubleshooting is necessary). The Marker should indicate the harmonic power level in dBc.
  - f. Compare the power level of the Marker against the specified harmonic performance level for your signal generator. Filtering on the A13 Output should improve the harmonic performance at the RF Output versus the Coherent Carrier output. However, the magnitude of this difference changes with frequency and actual harmonic content. Therefore, it is hard to define an absolute acceptable harmonic level at the Coherent Carrier output.

- If the harmonic level at the Coherent Carrier output is significantly higher than the harmonic level of the RF Output, the most likely causes are one of the following:
  - Signal Generators with Option 501, 502, 503, 504,  
replace the A17 Synthesizer (Option 501, 502, 503, 504) followed by the A13 Output.  
(Refer to [“A17 Synthesizer \(Option 501, 502, 503, 504\)”](#) on page 2-45  
and [“A13 Output”](#) on page 2-37.)
  - Signal Generators with Option UNJ or Option 506,  
replace the A16 Frac-N (Option UNJ or Option 506) followed by the A13 Output.  
(Refer to [“A16 Frac-N \(Option UNJ or Option 506\)”](#) on page 2-43 and [“A13 Output”](#) on page 2-37.)

## Troubleshooting Non-Harmonic Spurious

There are many different groupings a spur can fall into. The grouping determines what assembly is most likely generating the spur. Each data point in the service software test report indicates what type of spur is tested. The procedure for troubleshooting spurs is to identify the grouping based on failing a performance test, checking for loose or broken cables or castings, and replacing the assembly.

**Table 1-40**

Type of Spur	Options	Description
Mixing Spurs	All	<p>These spurs are generated by the mixing products of the RF and IF signals on the A13 Output. The instrument is tuned to a frequency in the Heterodyne-Band (<math>\leq 250</math> MHz) to measure these spurs.</p> <p>The most likely cause is the A13 Output.</p>
Power Supply Spurs	All	<p>These spurs are generated by the power supply switching at a 75 kHz rate.</p> <p>The most likely cause is the A6 Power Supply.</p>
Offset Reference Spurs	All	<p>These spurs are generated by the 10 MHz frequency reference on the A18 Reference.</p> <p>The most likely cause is the A18 Reference.</p>
RF and LO Feedthrough Spurs	All	<p>When the signal generator is tuned to a heterodyne band frequency (<math>\leq 250</math> MHz), the RF and LO signals feed through from the mixer on the A13 Output and generate spurs on the RF output.</p> <p>The most likely cause is the A13 Output</p>
Frac-N Spurs	All	<p>These spurs are generated by the Frac-N phase-lock loop. They show up at a small offset from the carrier, so they are checked at a 3 kHz offset.</p> <p>The most likely cause is the A16 Frac-N (Option UNJ or Option 506) or the A17 Synthesizer (Option 501, 502, 503, 504).</p>



**Table 1-40**

Sampler Spurs	Option UNJ and Option 506	<p>These spurs are generated when the sampler IF frequency, divided by two, equals a harmonic of the sampling frequency.</p> <p>The most likely cause is the A15 Sampler (Option UNJ or Option 506).</p>
F/2 Spurs	All	<p>These spurs are exactly one half of the RF output frequency.</p> <p>If these spurs occur, perform the VCO Bias Potentiometer Calibration. If the spurs still occur, the most likely cause is the A17 Synthesizer (Option 501, 502, 503, 504) or the A16 Frac-N (Option UNJ or Option 506).</p>
Comb Feedthrough	Option UNJ and Option 506	<p>These spurs are harmonics of the sampling frequency and are at an absolute frequency, not at an offset from the carrier.</p> <p>The most likely cause is the A15 Sampler (Option UNJ or Option 506).</p>
WCDMA	Option 400	<p>These spurs are offset spurs that are generated when the WCDMA modulation format is running.</p> <p>The most likely cause is the A7 Baseband Generator (Option 001/601 or Option 002/602).</p>
Baseband Generator Sideband Spurs	Option 001/601 or Option 002/602	<p>These spurs are mixing spurs that are generated by the baseband generator at offsets up to 2.5 MHz. A modulation format must be running to generate these spurs.</p> <p>The most likely cause is the A7 Baseband Generator (Option 001/601 or Option 002/602).</p>
Baseband Generator Bit Clock Spurs	Option 001/601 or Option 002/602	<p>These spurs occur at the symbol rate of a digital modulation format. A modulation format must be running to generate these spurs.</p> <p>The most likely cause is the A7 Baseband Generator (Option 001/601 or Option 002/602).</p>

### **Troubleshooting Phase Noise (Option UNJ and Option 506 Only)**

Poor grounding or shielding problems in either the test environment or the measurement system can cause the phase noise measurement to fail. Physical vibration is another common cause of phase noise. Before performing a phase noise measurement, assure that all instrument covers are installed, the work surface is free of physical vibrations, and the phase noise system is working properly.

Phase noise failures at specific offsets are fairly predictable. After assuring that the phase noise measurement accurately reflects a failure, use [Table 1-41](#) to troubleshoot phase noise problems. The troubleshooting procedure consists of assembly substitution.

**Table 1-41**

<b>Frequency Offset...</b>	<b>Most Likely Assembly Causing Phase Noise Failure...</b>
0 to 10 kHz	A18 Reference (Option UNJ or Option 506)
>10 kHz to 100 kHz	A15 Sampler (Option UNJ or Option 506)
>100 kHz to 1 MHz	A21 YTO Driver (Option UNJ or Option 506)
Frequencies $\leq$ 4 GHz	A13 Output
Frequencies > 4 GHz	A14 Extended Frequency Output (Option 506)

## Troubleshooting Pulse Modulation and LF Out

The pulse waveform originates on the A23 Motherboard, is routed through the A18 Reference, and then is routed out to the A13 Output and A14 Extended Frequency Output (Option 506). When pulse modulation is switched on, the pulse waveform is used to turn on and off the burst modulator on the A13 Output and A14 Extended Frequency Output (Option 506).

To configure the signal generator for pulse mode and check the pulse waveform on an oscilloscope from the front panel LF Out:

1. Press the **Pulse** hardkey and the **Pulse On** softkey.
2. Press the **LF Out** hardkey and the **LF Out On** softkey.
3. Press the **LF Out Amplitude** softkey and use the numeric keypad to enter **5 Vp**.

There should be a square wave on the oscilloscope with a period of 80 us and an amplitude of approximately 10 Vpp. The period and pulse width of the pulse waveform may be varied using softkeys under the **Pulse** menu.

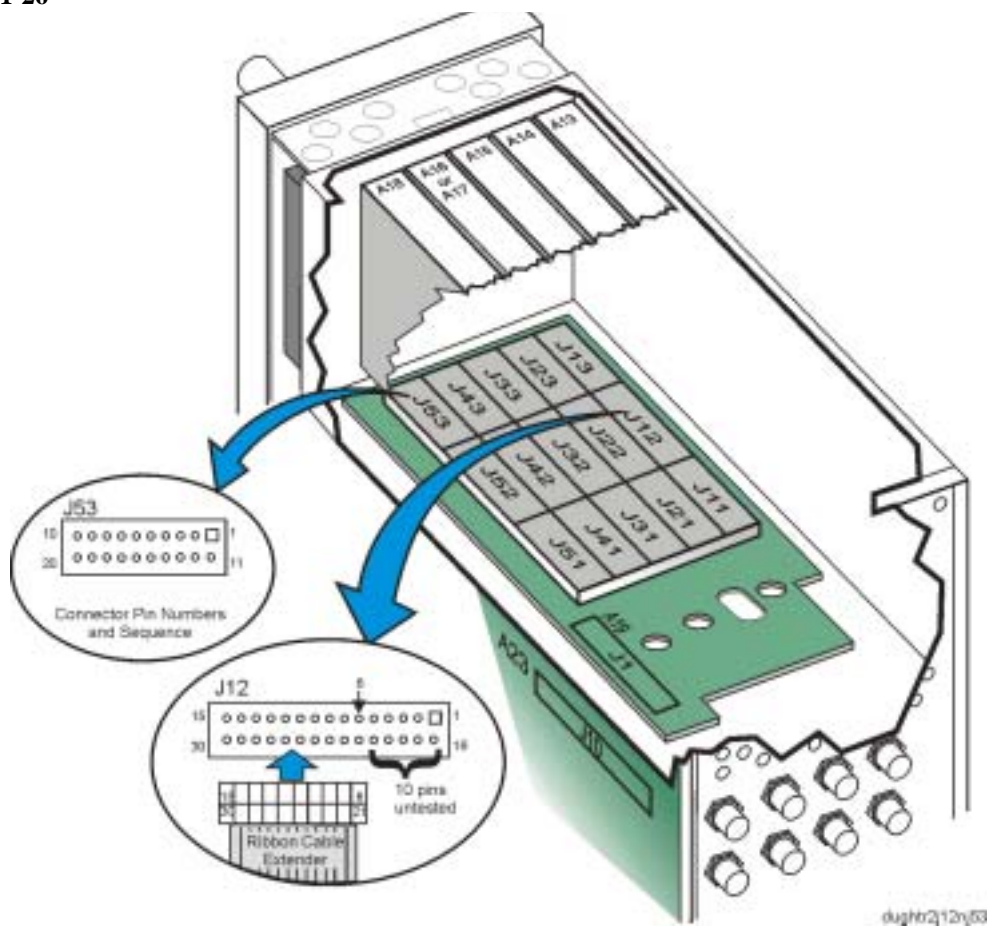
- If the pulse waveform is good at the LF Out port, check it at the A19 Daughterboard with one of the following procedures:
  - [“To Verify the Pulse Waveform at the A19 Daughterboard \(All Options\)”](#) on page 1-104
  - [“To Verify the Pulse Waveform at the A19 Daughterboard \(Option 506 Only\)”](#) on page 1-106
- If there is no pulse waveform at the LF Out port, proceed to [“No Pulse Signal from LF Out”](#) on page 1-108.

### To Verify the Pulse Waveform at the A19 Daughterboard (All Options)

1. Turn power off to the signal generator.
2. Remove the A13 Output.
3. Install a ribbon cable extender<sup>1</sup> into J12 on the A19 Daughterboard.

J12 is a 30-pin connector and the ribbon cable extender has only 20 pins. Align the ribbon cable extender with the pins on the left side of J12. When the ribbon cable extender is properly installed, you will see two rows (5 pins each) on the right-side of the ribbon cable extender.

Figure 1-26



1. The ribbon cable extender needs to be assembled by the user. It consists of two connector plugs (part number 1252-1010) and approximately eight inches of ribbon cable (part number 8120-2226). The ribbon cable contains 26 wires; six of these wires need to be stripped off before being connected to the plugs.

4. Turn power on to the signal generator and configure it for pulse mode.
  - a. Press the **Pulse** hardkey and the **Pulse On** softkey.
  - b. Press the **LF Out** hardkey and the **LF Out On** softkey.
  - c. Press the **LF Out Amplitude** softkey and use the numeric keypad to enter **5 Vp**.
  
5. Use an oscilloscope and probe J12-23.

There should be a square wave with a period of 80 us and an amplitude of approximately 5 Vpp. The period and pulse width of this signal may be varied using softkeys under the **Pulse** menu.

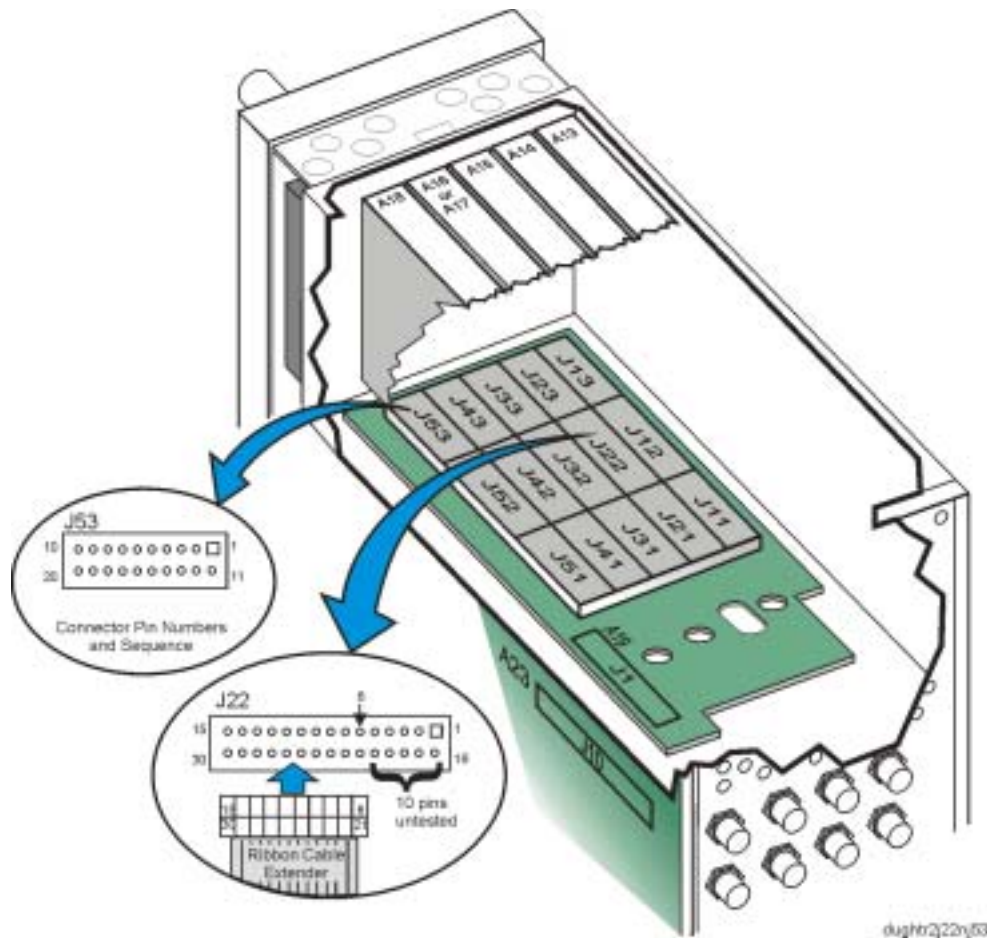
- If the signal is present, replace the A13 Output.  
(Refer to [“A13 Output” on page 2-37.](#))
- If the signal is not present:
  - a. Turn power off to the signal generator.
  - b. Remove the A18 Reference. (Refer to [“A18 Reference” on page 2-47.](#))
  - c. Insert a second ribbon cable extender into J53 on the A19 Daughterboard.
  - d. Ohm between J12-23 and J53-7 on the A19 Daughterboard.
    - If the path is good, replace the A18 Reference.  
(Refer to [“A18 Reference” on page 2-47.](#))
    - If the path is bad (open), replace the A19 Daughterboard.  
(Refer to [“A19 Daughterboard” on page 2-49.](#))

### To Verify the Pulse Waveform at the A19 Daughterboard (Option 506 Only)

1. Turn power off to the signal generator.
2. Remove the A14 Extended Frequency Output (Option 506).
3. Install the ribbon cable extender<sup>1</sup> into J22 on the A19 Daughterboard.

J22 is a 30-pin connector and the ribbon cable extender has only 20 pins. Align the ribbon cable extender with the pins on the left side of J22. When the ribbon cable extender is properly installed, you will see two rows (5 pins each) on the right-side of the ribbon cable extender.

Figure 1-27



1. The ribbon cable extender needs to be assembled by the user. It consists of two connector plugs (part number 1252-1010) and approximately eight inches of ribbon cable (part number 8120-2226). The ribbon cable contains 26 wires; six of these wires need to be stripped off before being connected to the plugs.

4. Turn power on to the signal generator and configure it for pulse mode.
  - a. Press the **Pulse** hardkey and the **Pulse On** softkey.
  - b. Press the **LF Out** hardkey and the **LF Out On** softkey.
  - c. Press the **LF Out Amplitude** softkey and use the numeric keypad to enter **5 Vp**.
  
5. Using an oscilloscope, probe J22-23.

There should be a square wave with a period of 80 us and an amplitude of approximately 5 Vpp. The period and pulse width of this signal may be varied using softkeys under the **Pulse** menu.

- If the signal is present, replace the A14 Extended Frequency Output (Option 506).  
(Refer to [“A14 Extended Frequency Output \(Option 506\)”](#) on page 2-39.)
- If the signal is not present:
  - a. Turn power off to the signal generator.
  - b. Remove the A18 Reference.
  - c. Insert a second ribbon cable extender into J53 on the A19 Daughterboard.
  - d. Ohm between J22-23 and J53-7 on the A19 Daughterboard.
    - If the path is good, replace the A18 Reference.  
(Refer to [“A18 Reference”](#) on page 2-47.)
    - If the path is bad (open), replace the A19 Daughterboard

## No Pulse Signal from LF Out

To function, the internal pulse generator on the A23 Motherboard requires a 10 MHz reference signal from the A18 Reference.

1. Assure that all self-tests have passed.
2. If an external 10 MHz reference is connected to the rear panel of the signal generator, remove the connection.
3. Use an oscilloscope and probe J10-99 of the A23 Motherboard.

There should be a 10 MHz sine wave with an amplitude  $> 1.2$  Vpp.

- If the signal is good at J10-99, use the oscilloscope to probe J10-50.

There should be a square wave with a period of 80 us and an amplitude of approximately 5 Vpp. The period and pulse width of this signal may be varied using softkeys under the **Pulse** menu.

— If the signal is good at J10-50:

- a. Turn power off to the signal generator.
- b. Remove the A18 Reference.
- c. Insert a ribbon cable extender<sup>1</sup> into J53.
- d. Ohm between J10-50 and J53-6.

— If the path is good between J10-50 and J53-6,  
ohm between the center conductor of the LF Out and J53-13.

— If the path is good between the center conductor of the LF Out and J53-13,  
replace the A18 Reference. (Refer to [“A18 Reference” on page 2-47.](#))

— If the path is bad (open) between the center conductor of the LF Out and J53-13,  
replace the A19 Daughterboard. (Refer to [“A19 Daughterboard” on page 2-49.](#))

— If the path is bad (open) between J10-50 and J53-6, replace the A19 Daughterboard.

— If the signal is not present at J10-50, replace the A23 Motherboard.  
(Refer to [“A23 Motherboard” on page 2-57.](#))

- If the signal is not present at J10-99:
  - a. Turn power off to the signal generator.
  - b. Remove the A18 Reference.
  - c. Insert the ribbon cable extender into J51 on the A19 Daughterboard.
  - d. Ohm between J10-99 and J51-7 on the A19 Daughterboard.
    - If the path is good, replace the A18 Reference.  
(Refer to [“A18 Reference” on page 2-47.](#))
    - If the path is bad (open), replace the A19 Daughterboard.  
(Refer to [“A19 Daughterboard” on page 2-49.](#))

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1. The ribbon cable extender needs to be assembled by the user. It consists of two connector plugs (part number 1252-1010) and approximately eight inches of ribbon cable (part number 8120-2226). The ribbon cable contains 26 wires; six of these wires need to be stripped off before being connected to the plugs.



## Contacting Agilent Technologies

If you should have a problem with your signal generator, contact your nearest sales and service offices. Before calling, please be prepared to provide the following information:

- a complete description of the failure
- is there a reported failure (Y or N) and what is the failure being reported
- was the failure dead on arrival (DOA) or did the instrument work before use
- the model number, all options, and serial number of the instrument
- the firmware revision date
- has self-test been run (Y or N)

## Review the Warranty

If there is still a problem, read the warranty printed in Chapter 5. If your signal generator is covered by a separate maintenance agreement, be familiar with its terms.

Agilent Technologies offers several maintenance plans to service your signal generator after warranty expiration. Call your Agilent Technologies sales and service office for full details.

## Contacting Agilent Sales and Service Offices

Before contacting Agilent, read the warranty printed in Chapter 5. If your signal generator is covered under a maintenance agreement, be familiar with its terms.

Assistance with test and measurements needs, and information on finding a local Agilent office is available on the Web at: <http://www.agilent.com/find/assist>

If you do not have access to the internet, contact the Agilent Technologies U.S. contact Center for information about our local office:

phone: 1 800 452 4844

fax: 1 888 900 8921

In any correspondence or telephone conversation, refer to the signal generator by its model number and full serial number. With this information, the Agilent representative can determine whether your unit is still within its warranty period.

## Important Information Needed by an Agilent Service Representative

Be as specific as possible about the nature of the problem and include information such as:

- any error messages that appeared on the signal generator
- a complete performance test record from the calibration guide for your signal generator
- any other specific data on the performance of the signal generator

## Returning a Signal Generator for Service

Use the information in this section if you need to return the signal generator to Agilent Technologies.

### Packaging the Signal Generator

1. Use the original packaging materials or a strong shipping container that is made of double-walled, corrugated cardboard with 159 kg (350 lb.) bursting strength. The carton must be both large enough and strong enough to accommodate the signal generator and allow at least 3 to 4 inches on all sides of the signal generator for packing material.

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**CAUTION** Signal generator damage can result from using packaging materials other than those specified. Never use styrene pellets, in any shape, as packaging materials. They do not adequately cushion the instrument or prevent it from shifting in the carton. Styrene pellets cause equipment damage by generating static electricity and by lodging in the signal generator fan.

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2. Surround the instrument with at least 3 to 4 inches of packing material, or enough to prevent the instrument from moving in the carton. If packing foam is not available, the best alternative is SD-240 Air Cap™ from Sealed Air Corporation (Hayward, CA 94545). Air Cap looks like a pink plastic sheet covered with 1-1/4 inch air-filled bubbles. Use the Air Cap to reduce static electricity. Wrap the instrument several times in the material to both protect the instrument and prevent it from moving in the carton.
3. Seal the shipping container securely with strong, nylon adhesive tape.
4. Mark the shipping container “FRAGILE, HANDLE WITH CARE” to ensure careful handling.
5. Retain copies of all shipping papers.

## Block Descriptions

This section contains block descriptions for all of the functional blocks in a signal generator:

- “Overall Block Description for Option 501, 502, 503, 504” on page 1-112
- “Frequency Synthesis Block Description for Option 501, 502, 503, 504” on page 1-117
- “RF Path Block Description for Option 501, 502, 503, 504” on page 1-121
- “Overall Block Description for Option UNJ and Option 506” on page 1-127
- “Frequency Synthesis Block Description for Option UNJ and Option 506” on page 1-133
- “RF Path Block Description for Option UNJ or Option 506” on page 1-141
- “Analog Modulation Block Description for All Options” on page 1-149
- “Digital Modulation Block Description for All Options” on page 1-155

## Overall Block Description for Option 501, 502, 503, 504

### Overview

The objective of this overall block description is to provide a functional overview of the Agilent E4438C ESG vector signal generator with Option 501, 502, 503, 504. Some of the functional blocks discussed are common to many types of instruments while others are more specific to signal generators.

- Common Functions:
  - Power Supply
  - CPU
  - Input/Output Interface
- Specific Signal Generator Functions:
  - Frequency Generation
  - Output Power Level Control
  - Modulation

### A6 Power Supply

The main power supply converts line voltage (120 Vac or 240 Vac) to regulated dc voltages. Some of the required dc voltages are not directly provided by the main power supply and are generated by converting the main power supply voltages. Most of the conversions are performed on the A23 Motherboard, but a few are performed on individual assemblies.

### A24 Line Module

The line voltage is connected to the power supply through the A24 Line Module. Line voltage selection isn't required because the A6 Power Supply automatically detects and adjusts to different line voltages. The power supply has an internal line fuse, but if the fuse opens, the power supply must be replaced; it is not field repairable.

### A12 CPU

The A12 CPU controls all activities in the signal generator. It translates information entered from the front panel keys, LAN, GPIB, or Auxiliary Interface (RS-232) into machine level instructions and communicates these instructions on the internal buses. The A12 CPU also monitors critical circuits for unlevelled and unlocked conditions and reports these problems on the display.

### Input/Output Interface

The Input/Output Interface is located on the front panel and includes the A1 Keyboard, the A2 Display, and the On/Off power switch. The user interface consists of the front panel keypad and the display softkeys. The current signal generator state is displayed on the A2 Display. Above the power switch, are amber and green Light Emitting Diodes (LED). When the amber LED is lit, it indicates that line voltage is present and that the signal generator is in standby mode. When the green LED is lit, it indicates that the signal generator is in the power-on mode.

### Front Panel Hardkeys, Softkeys, and Keypad

Some front panel keys are referred to as hardkeys; these keys are dedicated to specific functions. Dedicated hardkeys are used to select the most commonly used features and control the display's contrast and intensity. Front panel keys along the display are referred to as softkeys and are used to select functions; these functions are displayed to the left of each softkey on the

display. As softkeys are pressed, their displayed functionality is executed; some of these softkeys have lower-level menus which are displayed when pressed. The front panel has a keypad that uses a row and column configuration. Pressing a key on this keypad makes a connection between a row and column. The row and column information is routed to the A12 CPU where it is interpreted and the appropriate action is taken.

### **A2 Display**

A Liquid Crystal Display (LCD) provides information about the signal generator's settings and condition. The LCD requires a power supply, lighting, and data. The lighting, supplied from a light bulb on the A2DS1 Display Backlight is powered by the A4 Inverter. The A4 Inverter converts a dc voltage an ac voltage. Data is generated on the A12 CPU and routed to the LCD through the A3 Front Panel Auxilary Board.

### **A25 Rear Panel Board**

The A24 Line Module, LAN, GPIB, and RS-232 connections are located on the A25 Rear Panel Board. On signal generators with Option 1EM, the A25 Rear Panel Board is replaced with the A26 Rear Panel LVDS Board (Option 1EM) and A27 Rear Panel SMB Board (Option 1EM).

### **RF Output Connector**

The RF output connector is a female Type-N. Option 1EM routes the RF output to the signal generator's rear panel.

### **RF Path - Frequencies 250 kHz to 4 GHz**

The output of a Voltage Controlled Oscillator (VCO) is routed to the A13 Output which contains ALC circuitry to maintain power leveling. AM and Pulse modulation are also implemented on the A13 Output. The signal is further conditioned and routed through the optional AT1 Attenuator and to the front panel RF output connector.

For frequencies below 250 MHz, the A13 Output down-converts the RF signal. To accomplish the down conversion, the A18 Reference provides a 1 GHz LO to a mixer on the A13 Output. The resultant IF out of the mixer is then routed to the AT1 Attenuator or to the front panel RF output connector.

### **Frequency Control**

Frequency accuracy and stability are established with the A18 Reference, A17 Synthesizer (Option 501, 502, 503, 504), and the A12 CPU.

### **Frequency Generation**

The A17 Synthesizer (Option 501, 502, 503, 504) contains a VCO that generates frequencies from 500 MHz to 1 GHz. To maintain frequency accuracy, the VCO is phase locked to a 10 MHz reference from the A18 Reference. To change the output frequency, the VCO tuning voltage is changed. Depending on the output frequency, the VCO frequency is either divided by 2, multiplied by 2 or 4, or left unchanged.

### **Output Power Leveling/Automatic Leveling Control**

Output power control circuitry consists of a detector, integrators, two ALC modulators, and other associated circuitry. This circuitry is commonly referred to as the Automatic Leveling Control (ALC) loop. A small portion of the RF signal is detected and converted to a dc voltage. In closed loop operation, a comparison is made between a reference voltage and the detected voltage. If the detected and reference voltage levels agree, the modulator drive current remains constant. If the detected and reference voltage levels do not agree, the modulator drive current changes causing the RF output power to increase or decrease until the reference and detected voltages agree. In open loop operation or ALC OFF mode, only the reference voltage is used to control the modulation drive current. The reference voltage is determined by the desired power setting and includes the stored calibration data used to compensate for any losses that occur after the detector. The reference voltage is generated on the A13 Output by a DAC and is controlled by the A12 CPU.

### **Analog Modulation**

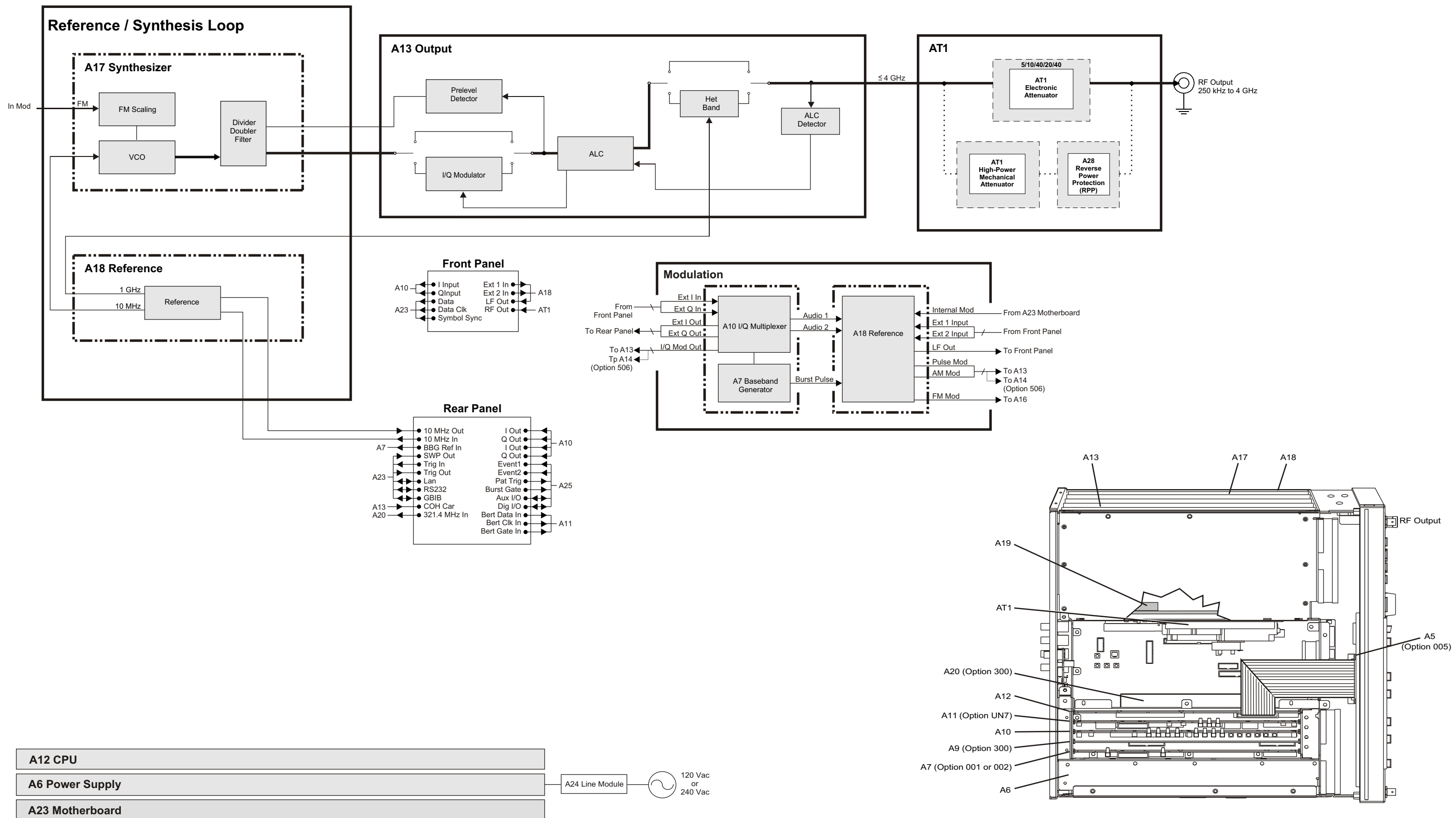
An *Internal Modulation Source Function Generator*, located on the A23 Motherboard, is used to provide AM, FM, PM, and Pulse modulation capabilities. To improve phase modulation control, this function generator is tied to the 10 MHz reference signal from the A18 Reference. The function generator's signal is routed to the A18 Reference, which contains a multiplexer. The multiplexer is controlled through the front panel and may be used to select the modulation source. External modulation may be applied through the EXT 1 and EXT 2 front panel inputs. There is also a feature that allows internally generated signals to be routed to the LF Output on the front panel. AM and Pulse modulation signals are routed to the A13 Output while FM and PM modulations are routed to the A17 Synthesizer (Option 501, 502, 503, 504).

### **Digital Modulation**

The A10 I/Q Multiplexer routes the front panel I/Q baseband signals to the A13 Output so that they can be used to modulate the RF signal. The A10 I/Q Multiplexer also routes the front panel I/Q signals to the rear panel. Along with the original I/Q signal, an inverted I/Q signal is available at the rear panel.

If the signal generator contains an A7 Baseband Generator (Option 001/601 or Option 002/602), it may provide the baseband I/Q signals; it also has the capability of burst modulation. The A10 I/Q Multiplexer may select which baseband signal to use and route the signal in the same manner as the external front panel I/Q signals.

The I/Q signals are simultaneously routed by the A10 I/Q Multiplexer to the A18 Reference so they may be used to modulate AM, FM, and PM.



Overall Block Diagram for Option 501, 502, 503, 504





## Frequency Synthesis Block Description for Option 501, 502, 503, 504

### Overview

Frequency synthesis for Option 501, 502, 503, 504 signal generators is made from portions of the following:

- A18 Reference (Option 501, 502, 503, 504)  
or A18 Reference (Option 501, 502, 503, 504 with Option 1E5)
- A17 Synthesizer (Option 501, 502, 503, 504)
- A13 Output (Option 501, 502, 503, 504 or Option UNJ)  
or A13 Output (Option UNB)
- A23 Motherboard

The *Frequency Synthesis Block* consists of the A18 Reference and the A17 Synthesizer. Frequency synthesis begins on the A18 Reference with a 10 MHz reference. The A18 Reference has a *Temperature Compensated Xtal Oscillator* (TCXO), while the high stability timebase, A18 Reference (Option 501, 502, 503, 504 with Option 1E5), has an *Oven Controlled Xtal Oscillator* (OCXO). Both references may be phase locked to an external reference or operate stand-alone. The 10 MHz reference phase locks a 1 GHz discrete oscillator on the A18 Reference. The frequency accuracy of the 10 MHz reference is translated to the final output frequency accuracy. The 10 MHz signal is distributed throughout the signal generator. In the following description, the main focus will concentrate on the 10 MHz going to the A17 Synthesizer, but the 10 MHz is also routed to the *Internal Modulation Source Function Generator* located on the A23 Motherboard and routed to the rear panel. The 1 GHz is routed to the heterodyne circuit located on the A13 Output.

The 10 MHz reference is part of a Phase Lock Loop (PLL) circuit located on the A17 Synthesizer. The PLL tunes a Voltage Controlled Oscillator (VCO), which is divided or multiplied to achieve the final RF output frequency.

### A18 Reference

Essential to frequency accuracy and low phase noise are clean and stable reference signals. The reference signals on the A18 Reference are a 10 MHz TCXO or OCXO and a 1 GHz discrete oscillator. To minimize frequency drift, the 10 MHz TCXO is tuned by a DAC and the OCXO has an oven to maintain a constant crystal temperature.

If a signal generator with an A18 Reference (Option 501, 502, 503, 504 with Option 1E5) has been without electrical power, a "Reference Oven Cold" warning is displayed when the signal generator is powered on. The warning message is controlled by a timer and should go out after five minutes of being connected to ac power.

The 10 MHz reference may be phase locked to an external 2, 5, or 10 MHz reference. The 10 MHz reference is available at the signal generator's rear panel and may be used to phase lock other instruments in a system. The 10 MHz reference is routed to the A17 Synthesizer and is part of a PLL which is used to tune a VCO.

The 1 GHz discrete oscillator is split into two paths. One path is divided down to 10 MHz and is then phase compared to the 10 MHz signal from the A18 Reference; the difference of this comparison is used to drive an integrator which in turn is used to tune the 1 GHz oscillator and ensure both reference signals are phase locked together. The other path of the 1 GHz signal is routed to the A13 Output.

### A17 Synthesizer (Option 501, 502, 503, 504)

The A17 Synthesizer generates the final RF output frequency. Two proprietary application specific integrated circuits (ASIC), named Paren and Tesera, are used to tune a VCO on the A17 Synthesizer. The VCO is capable of being tuned from 500 MHz to 1000 MHz.

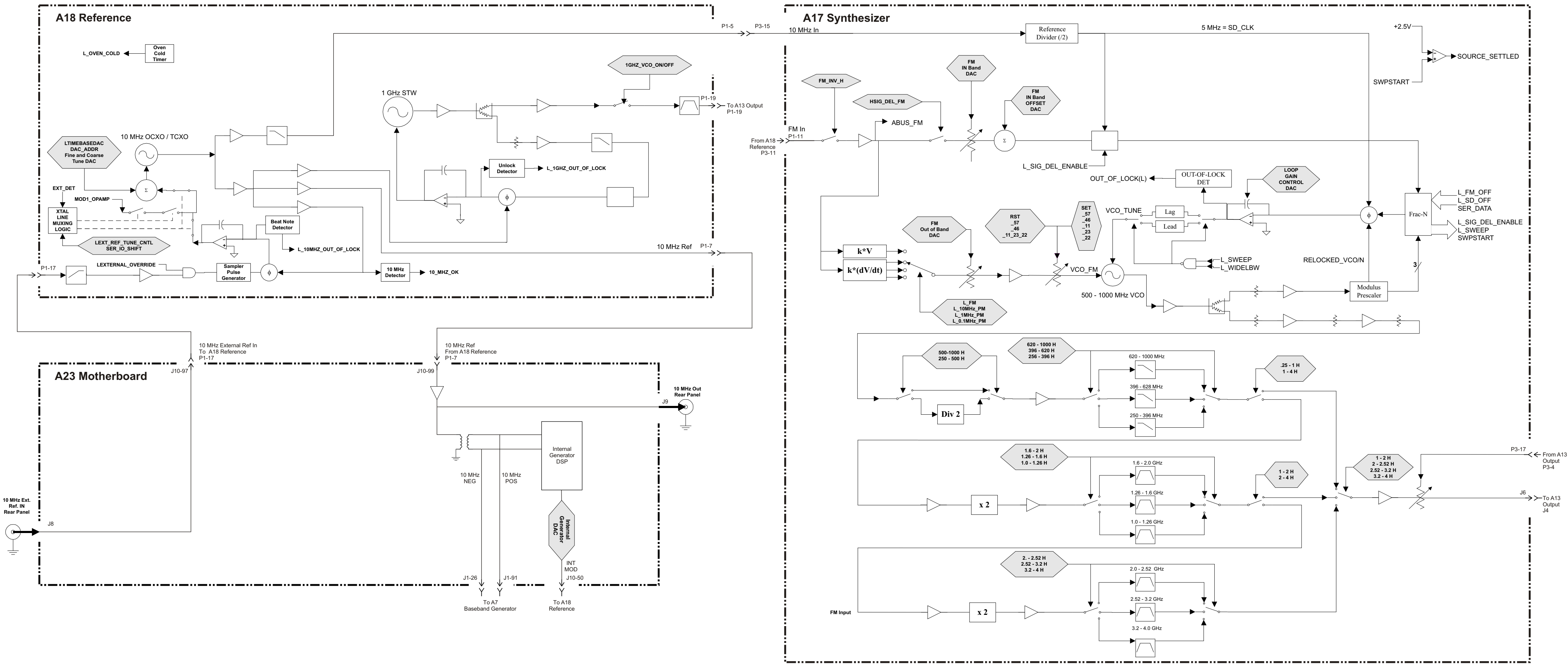
The tuning of the VCO begins with a 10 MHz signal from the A18 Reference. The 10 MHz reference is divided down to 5 MHz to reduce fractional spurs in the phase detector. The 5 MHz reference is then phase compared to a divided down VCO

**Troubleshooting Performance Test Problems**

output. The primary division of the VCO is performed within the Tesera pre scaler ASIC; the pre scaler divisor is provided by the Paren ASIC. The output of the phase detector is integrated and then used to tune the VCO.

Depending on the desired output frequency, the VCO is either divided by two, multiplied by two or four, or left unchanged. The final signal is filtered and routed to the A13 Output.

The baseband FM and PM from the A18 Reference are added to the VCO on the A17 Synthesizer. Depending on the modulation rate, there are two different paths for the modulation to be added to the VCO. If the rate is outside the VCO PLL bandwidth, the baseband signal is scaled and applied to the VCO. If the baseband rate is within the VCO PLL bandwidth, the baseband signal is scaled and sent to the *Sigma-Delta Modulator* for digitizing. The digitized signal is used by Paren and Tesera to change the VCO tuning and implement FM.



Frequency Synthesis Block Diagram for Option 501, 502, 503, and 504



## RF Path Block Description for Option 501, 502, 503, 504

### Overview

The RF Path for signal generators with Option 501, 502, 503, 504 is made from portions of the following:

- A18 Reference (Option 501, 502, 503, 504)  
or A18 Reference (Option 501, 502, 503, 504 with Option 1E5)
- A17 Synthesizer (Option 501, 502, 503, 504)
- A13 Output (Option 501, 502, 503, 504 or Option UNJ)  
or A13 Output (Option UNB)
- AT1 Electronic Attenuator (Option 501, 502, 503, 504 or Option UNJ)  
or AT1 High Power Mechanical Attenuator (Standard or Option UNJ with Option UNB)  
with A28 Reverse Power Protection (Option 501, 502, 503, 504 or Option UNJ)

Frequency generation is performed on the A17 Synthesizer (Option 501, 502, 503, 504) with a Voltage Controlled Oscillator (VCO). The signal is routed to the A13 Output which contains the Automatic Level Control (ALC) loop for maintaining leveled power.

When producing RF frequencies below 250 MHz, a heterodyne mixer located on the A13 Output is switched into the RF path. The RF signal is mixed with a 1 GHz LO, provided by the A18 Reference, to produce the final 100 kHz to < 250 MHz output; specifications apply to frequencies  $\geq$  250 kHz.

AM, Pulse, Burst, and I/Q baseband signals modulate the RF on the A13 Output. AM modulation originates on the A18 Reference, I/Q and Burst modulation originates on the A7 Baseband Generator (Option 001/601 or Option 002/602), and Pulse modulation originates on the A23 Motherboard. AM modulation is routed directly to the A13 Output, I/Q modulation is routed through the A10 I/Q Multiplexer to the A13 Output, Pulse modulation and Burst modulation are routed through the A18 Reference to the A13 Output.

The RF signal leaving the A13 Output is then routed through an electronic attenuator. The attenuator includes a Reverse Power Protection (RPP) circuit to prevent damage to the synthesizer. Options UNB and Option 506 replace the electronic attenuator with a mechanical attenuator. Option 506 doesn't include an RPP circuit, but instead includes the A29 DC Blocking Capacitor (Option 506) which helps to protect the attenuator from dc power that may inadvertently be applied to the RF output connector.

Each signal generator's maximum frequency and power are dependent on the options that it contains.

### A17 Synthesizer (Option 501, 502, 503, 504)

The A17 Synthesizer (Option 501, 502, 503, 504) is part of the *Frequency Synthesis Block*. It provides the A13 Output with its input signal. The RF is generated on the A17 Synthesizer (Option 501, 502, 503, 504) by a VCO and this VCO is either divided or multiplied to achieve a final frequency which is amplified and filtered prior to it being routed to the A13 Output. A pre-level bias voltage generated by the A13 Output is fed back where it is used to bias a pin diode and control the amount of RF amplification.

### A13 Output (Option 501, 502, 503, 504 or Option UNJ) or A13 Output (Option UNB)

The functions of the A13 Output include output power leveling, A17 Synthesizer (Option 501, 502, 503, 504) pre-leveling, pulse/burst, I/Q and amplitude modulation (AM).

The filtered RF signal from the A17 Synthesizer (Option 501, 502, 503, 504) is amplified and filtered by the A13 Output. After the first stage of amplification on the A13 Output, the RF signal is split with one path becoming the

coherent carrier reference. The coherent carrier reference is free of any AM or Pulse/Burst modulation and is available at the COH Carrier output on the instrument's rear panel. The other path continues through the A13 Output. If I/Q modulation is enabled, the RF signal is routed through the I/Q modulator, otherwise it is bypassed. Depending on the output frequency, different bandpass filters are switched in to remove unwanted components from the RF signal. After filtering, the RF signal is split with a portion of the signal being detected and becoming the pre-level bias voltage used by the A17 Synthesizer (Option 501, 502, 503, 504).

Output power leveling is accomplished with an Automatic Leveling Control (ALC) loop. The ALC has two modes of operation: ALC open or ALC closed. For normal operation, the ALC loop is closed and the output power is leveled. For some modulation applications, the ALC is opened and an internal Power Search routine may be used to achieve amplitude accuracy.

When the ALC loop is closed, a portion of the RF output is detected and compared to the sum of the ALC reference voltage and the baseband AM. The difference is then applied to an integrator. The integrator output will ramp either positive or negative, based on the power of the input signal. If there is no difference between the detected voltage and the sum of both the ALC reference voltage and baseband AM, the integrator output will remain unchanged. The output voltage ramp is converted to a current and used to bias pin diodes in the ALC modulator. These pin diodes vary the amount of attenuation in the RF path and thus maintain leveled output power.

The A13 Output has two independent integrators to facilitate fast power switching. The feedback detector voltage is compared to one ALC reference voltage for one power level, while at the same time, a second independent ALC reference voltage is set up for the second power level. When the user changes from one power level to the other, the hardware switches between the two integrator circuits and their respective references. This function and its related parameters are available through the front panel **Alternate Amplitude** softkey and this softkey is only present when an electronic attenuator is installed.

Depending upon the application, the user may change the ALC bandwidth from normal to narrow. The defaults are normal if I/Q modulation is off and narrow if external I/Q modulation is on. The softkey is not available if internal I/Q modulation is selected.

When AM is enabled, the baseband signal originates on the A18 Reference. The signal enters the A13 Output as a Log AM signal. An Antilog circuit converts the Log AM back to Linear AM. As the baseband AM signal changes amplitude, it causes the ALC modulator bias to change proportionately. This is due to the AM signal being summed with the ALC Reference voltage prior to the integrator. Care must be taken to ensure that the ALC doesn't become unleveled due to a combination of high carrier power and large modulation depth. The instrument's total output power equals the sum of the carrier power and the AM sidebands' power. If this sum is greater than the maximum output power rating, the ALC loop may become unleveled.

The ALC loop also contains a pulse/burst modulator. The pulse signal originates on the A18 Reference and the burst signal originates on the A7 Baseband Generator (Option 001/601 or Option 002/602). The pin diodes within the modulator are normally biased to provide minimum attenuation. When pulse/burst modulation is used, the pin diodes are biased off and on thus providing maximum or minimum attenuation. This causes the RF signal to pulse on and off. For narrow pulse widths, the ALC is opened. To maintain power level accuracy, a Power Search routine may be used through the front panel. The Power Search routine shuts down the modulation, momentarily closes the ALC loop, and levels. The ALC is then opened up and the modulation is turned back on.

The I/Q modulator is also located on the A13 Output and may be switched in or out of the RF path. Baseband modulation, for the I/Q modulator, is provided by the A10 I/Q Multiplexer. User-selectable lowpass filtering may be applied to the baseband I and Q signals. The user may also perform a front panel I/Q calibration. The calibration doesn't require any external stimulus and will minimize quadrature and gain errors. A portion of the I and Q signals are fed back to the A10 I/Q Multiplexer which minimize offset. When I/Q modulation is enabled, the pre-level bias for the A17 Synthesizer (Option 501, 502, 503, 504) is provided by the I/Q modulator.

**AT1 Electronic Attenuator (Option 501, 502, 503, 504 or Option UNJ)  
or AT1 High-Power Mechanical Attenuator (Option UNB)  
with A28 Reverse Power Protection (Option 501, 502, 503, 504 or Option UNJ)**

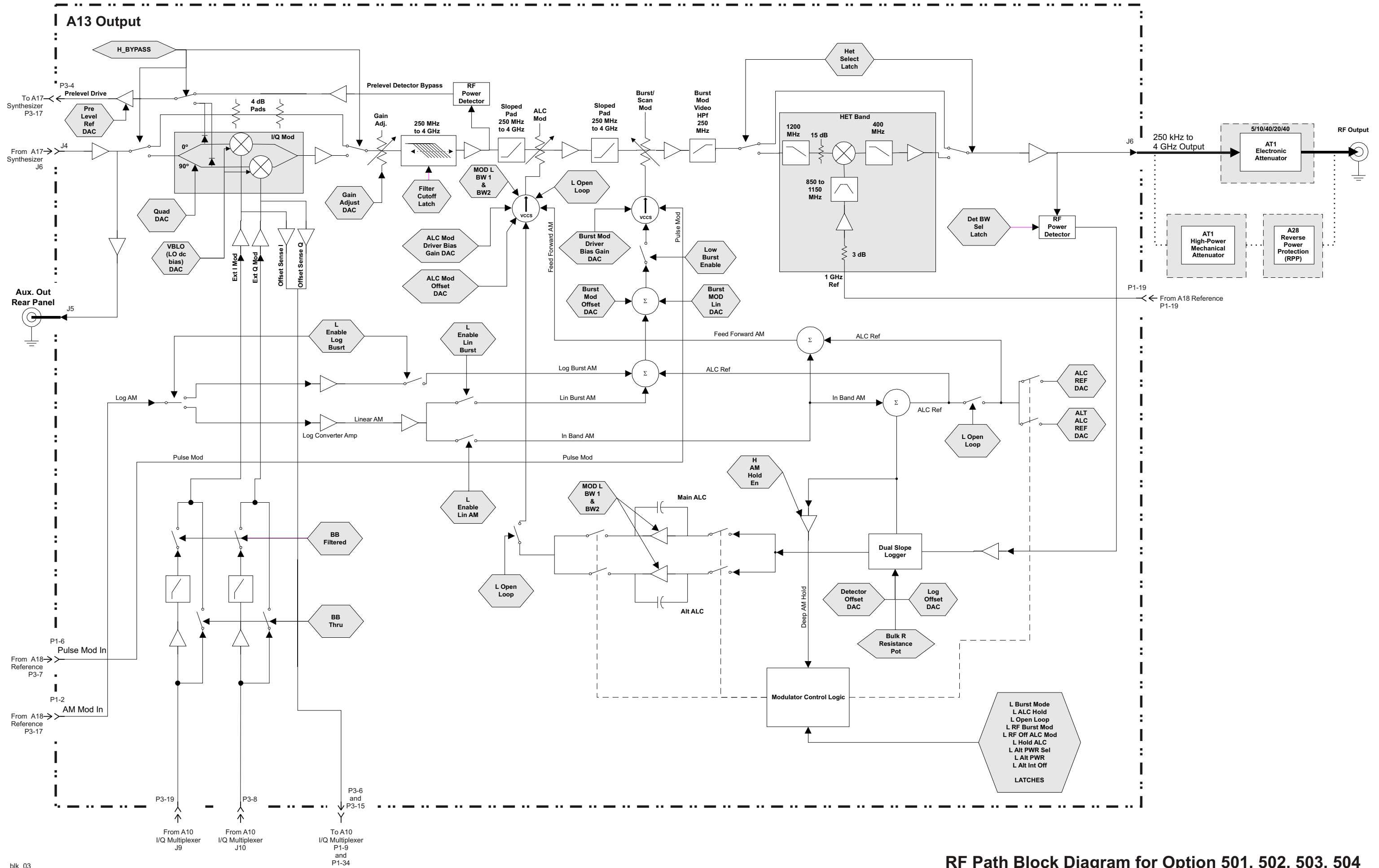
There are two different attenuator modules available. A standard instrument has a high-speed solid state 130 dB electronic attenuator. Option UNB and Option 506 have a 135 dB mechanical attenuator. The mechanical attenuator is standard hardware for Option 506. Both attenuators step in 5 dB increments. When the attenuator changes values, the new attenuation section is

switched into the RF path prior to switching the unused attenuator section out. This prevents damage to sensitive devices under test due to high power being applied. Attenuation sections are switched into operation in a specific order to achieve the best input and output matching for each level of attenuation. The attenuator modules are field replaceable, but not field repairable.

The standard electronic attenuator has the RPP circuitry incorporated internally with the attenuator module circuitry. In contrast, the mechanical attenuator has the A28 Reverse Power Protection (Option 501, 502, 503, 504 or Option UNJ) circuit as an external module that is attached to the attenuator. The RPP circuitry prevents instrument damage when power is mistakenly applied to the instrument's RF output. Operationally, the RPP circuitry opens an RF relay when 1 Watt of energy is sensed on the RF output. Repeated high levels of reverse power degrade the performance of the RPP or completely bypass the open RF relay. The RPP may be reset through front panel softkeys.

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## Overall Block Description for Option UNJ and Option 506

### Overview

The objective of this overall block description is to provide a functional overview of the Agilent E4438C ESG vector signal generator with Option UNJ and Option 506. Some of the functional blocks discussed are common to many types of instruments while others are more specific to signal generators.

- Common Functions:
  - Power Supply
  - CPU
  - Input/Output Interface
- Specific Signal Generator Functions:
  - Frequency Generation
  - Output Power Level Control
  - Modulation

### A6 Power Supply

The main power supply converts line voltage (120 Vac or 240 Vac) to regulated dc voltages. Some of the required dc voltages are not directly provided by the main power supply and are generated by converting the main power supply voltages. Most of the conversions are performed on the A23 Motherboard, but a few are performed on individual assemblies.

### A24 Line Module

The line voltage is connected to the power supply through the A24 Line Module. Line voltage selection isn't required because the A6 Power Supply automatically detects and adjusts to different line voltages. The power supply has an internal line fuse, but if the fuse opens, the power supply must be replaced; it is not field repairable.

### A12 CPU

The A12 CPU controls all activities in the signal generator. It translates information entered from the front panel keys, LAN, GPIB, or Auxiliary Interface (RS-232) into machine level instructions and communicates these instructions on the internal buses. The A12 CPU also monitors critical circuits for unlevelled and unlocked conditions and reports these problems on the display.

### Input/Output Interface

The Input/Output Interface is located on the front panel and includes the A1 Keyboard, the A2 Display, and the On/Off power switch. The user interface consists of the front panel keypad and the display softkeys. The current signal generator state is displayed on the A2 Display. Above the power switch, are amber and green Light Emitting Diodes (LED). When the amber LED is lit, it indicates that line voltage is present and that the signal generator is in standby mode. When the green LED is lit, it indicates that the signal generator is in the power-on mode.

### Front Panel Hardkeys, Softkeys, and Keypad

Some front panel keys are referred to as hardkeys; these keys are dedicated to specific functions. Dedicated hardkeys are used to select the most commonly used features and control the display's contrast and intensity. Front panel keys along the display are referred to as softkeys and are used to select functions; these functions are displayed to the left of each softkey on the

display. As softkeys are pressed, their displayed functionality is executed; some of these softkeys have lower-level menus which are displayed when pressed. The front panel has a keypad that uses a row and column configuration. Pressing a key on this keypad makes a connection between a row and column. The row and column information is routed to the A12 CPU where it is interpreted and the appropriate action is taken.

### **A2 Display**

A Liquid Crystal Display (LCD) provides information about the signal generator's settings and condition. The LCD requires a power supply, lighting, and data. The lighting, supplied from a light bulb on the A2DS1 Display Backlight is powered by the A4 Inverter. The A4 Inverter converts a dc voltage an ac voltage. Data is generated on the A12 CPU and routed to the LCD through the A3 Front Panel Auxiliary Board.

### **A25 Rear Panel Board**

The A24 Line Module, LAN, GPIB, and RS-232 connections are located on the A25 Rear Panel Board. On signal generators with Option 1EM, the A25 Rear Panel Board is replaced with the A26 Rear Panel LVDS Board (Option 1EM) and A27 Rear Panel SMB Board (Option 1EM).

### **RF Output Connector**

The RF output connector is a female Type-N. Option 1EM routes the RF output to the signal generator's rear panel.

### **RF Path - Frequencies 250 kHz to 4 GHz (Option UNJ)**

The output of a YIG oscillator is routed, through the A16 Frac-N, to the A13 Output which contains ALC circuitry to maintain power leveling. AM and Pulse modulation are also implemented on the A13 Output. The signal is further conditioned and routed through the optional AT1 Attenuator and to the front panel RF output connector.

For frequencies below 250 MHz, the A13 Output down converts the RF signal. To accomplish the down conversion, the A18 Reference provides a 1 GHz LO to a mixer on the A13 Output. The resultant IF out of the mixer is then routed to the AT1 Attenuator or to the front panel RF output connector.

### **RF Path - Frequencies > 4 GHz to ≤ 6 GHz (Option 506 Only)**

The output of a YIG oscillator is routed to the A13 Output which contains ALC circuitry to maintain power leveling. AM and Pulse modulation are also implemented on the A13 Output. The signal is further conditioned and routed through the A14 Extended Frequency Output (Option 506), to the optional AT1 Attenuator, and then to the front panel RF output connector.

For frequencies > 4 GHz and ≤ 6 GHz, the A22 Coupler (Option 506) couples off the YIG oscillator signal and routes it to the A14 Extended Frequency Output (Option 506) which contains similar circuitry to that of the A13 Output.

So, depending on the desired frequency, the RF path switches between the A13 Output and the A14 Extended Frequency Output (Option 506) before it is routed to the optional AT1 Attenuator, and then to the front panel RF output connector. The RF path is switched on the A14 Extended Frequency Output (Option 506).

### **Frequency Control**

Frequency accuracy and stability are established with the A18 Reference (Option UNJ or Option 506), A15 Sampler (Option UNJ or Option 506), A16 Frac-N (Option UNJ or Option 506), and the A12 CPU.

### **Frequency Generation**

The YIG oscillator, on the A21 YTO Driver (Option UNJ or Option 506), generates frequencies from 4 GHz to 8 GHz. For frequencies from 250 MHz to 4 GHz, the YIG oscillator is tuned in the 4 to 8 GHz range and the output is divided by 2, 4, 8, or 16. If the desired frequency is below 250 MHz, an additional mixer is used. For frequencies > 4 GHz and ≤ 6 GHz (Option 506) the YIG frequency is not divided.

To change frequencies, the A12 CPU controls the A21 YTO Driver and coarse tunes the YIG with a voltage provided by the

pre-tune DAC. The A15 Sampler compares the signal from the YIG to a reference signal and generates an error voltage proportional to the frequency error. This voltage is summed with the pre-tune DAC voltage and corrects (fine tunes) the YIG's output frequency.

The reference signal is provided by the VCO on the A16 Frac-N that is phase-locked to a 10 MHz reference on the A18 Reference. The A15 Sampler down converts the YIG frequencies to make the comparison with the reference. The comparison is done by a phase detector, which generates an output voltage proportional to the difference in frequency/phase. This voltage tunes the YIG until there is no difference in frequency/phase.

### **Output Power Leveling/Automatic Leveling Control**

Output power control circuitry consists of a detector, integrators, two ALC modulators, and other associated circuitry. This circuitry is commonly referred to as the Automatic Leveling Control (ALC) loop. A small portion of the RF signal is detected and converted to a dc voltage. In closed loop operation, a comparison is made between a reference voltage and the detected voltage. If the detected and reference voltage levels agree, the modulator drive current remains constant. If the detected and reference voltage levels do not agree, the modulator drive current changes causing the RF output power to increase or decrease until the reference and detected voltages agree. In open loop operation or ALC OFF mode, only the reference voltage is used to control the modulation drive current. The reference voltage is determined by the desired power setting and includes the stored calibration data used to compensate for any losses that occur after the detector. The reference voltage is generated on the A13 Output and A14 Extended Frequency Output (Option 506) by a DAC and is controlled by the A12 CPU.

### **Analog Modulation**

An *Internal Modulation Source Function Generator*, located on the A23 Motherboard, is used to provide AM, FM, PM, and Pulse modulation capabilities. To improve phase modulation control, this function generator is tied to the 10 MHz reference signal from the A18 Reference. The function generator's signal is routed to the A18 Reference, which contains a multiplexer. The multiplexer is controlled through the front panel and may be used to select the modulation source. External modulation may be applied through the EXT 1 and EXT 2 front panel inputs. There is also a feature that allows internally generated signals to be routed to the LF Output on the front panel. AM and Pulse modulation signals are routed to the A13 Output and A14 Extended Frequency Output (Option 506) while FM and PM modulations are routed to the A16 Frac-N (Option UNJ or Option 506).

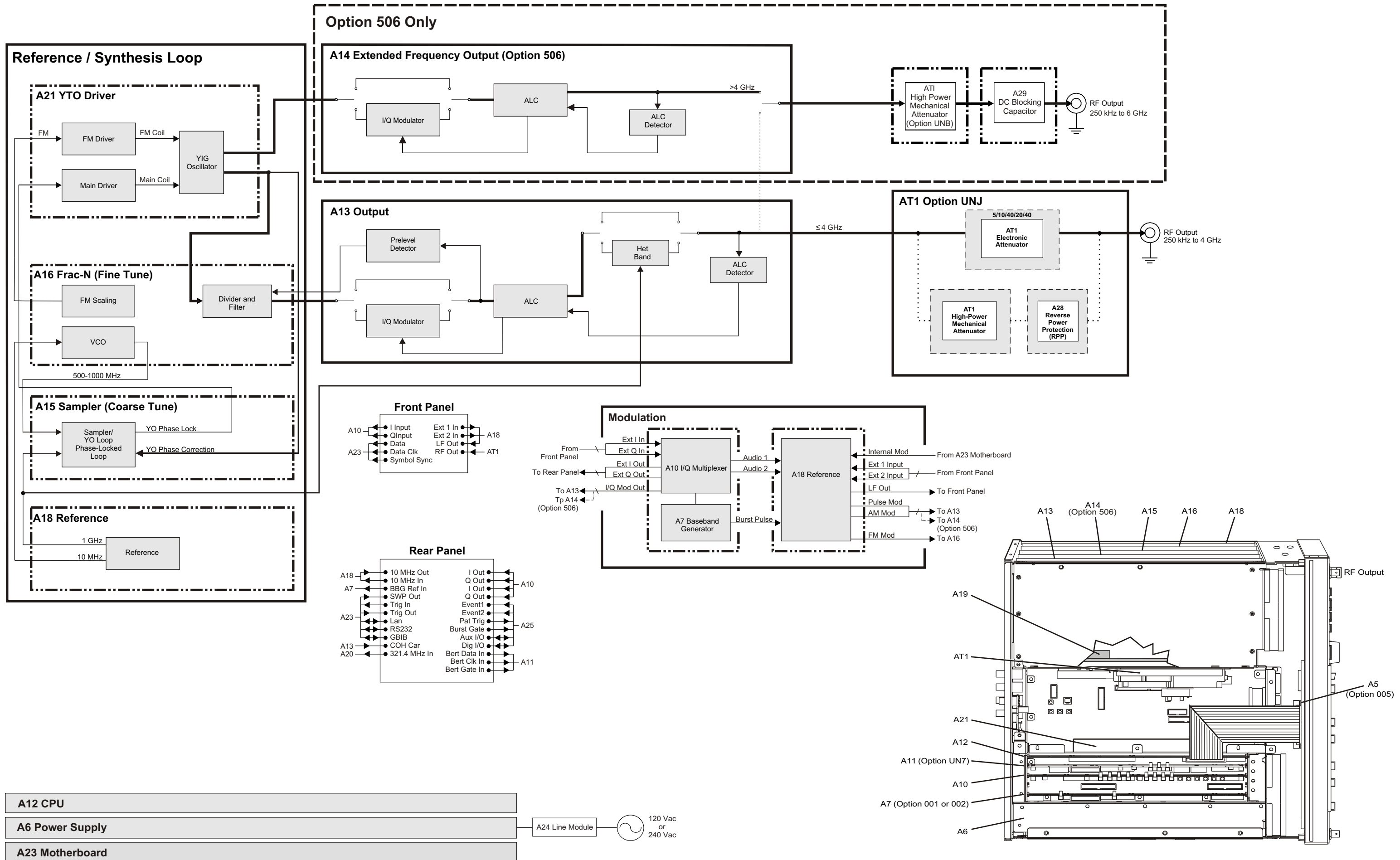
### **Digital Modulation**

The A10 I/Q Multiplexer routes the front panel I/Q baseband signals to the A13 Output and A14 Extended Frequency Output (Option 506) so that they can be used to modulate the RF signal. The A10 I/Q Multiplexer also routes the front panel I/Q signals to the rear panel. Along with the original I/Q signal, an inverted I/Q signal is available at the rear panel.

If the signal generator contains an A7 Baseband Generator (Option 001/601 or Option 002/602), it may provide the baseband I/Q signals; it also has the capability of burst modulation. The A10 I/Q Multiplexer may select which baseband signal to use and route the signal in the same manner as the external front panel I/Q signals.

The I/Q signals are simultaneously routed by the A10 I/Q Multiplexer to the A18 Reference so they may be used to modulate AM, FM, and PM.

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Overall Block Diagram for Option UNJ and Option 506





## Frequency Synthesis Block Description for Option UNJ and Option 506

### Overview

Frequency synthesis for Option UNJ and Option 506 signal generators is made from portions of the following:

- A18 Reference (Option UNJ or Option 506)
- A15 Sampler (Option UNJ or Option 506)
- A16 Frac-N (Option UNJ or Option 506)
- A21 YTO Driver (Option UNJ or Option 506)
- A13 Output (Option UNJ)  
or A13 Output (Option UNB)
- A14 Extended Frequency Output (Option 506)
- A23 Motherboard

The Frequency Synthesis Block consists of the A18 Reference, A15 Sampler, A16 Frac-N, and the A21 YTO Driver. Frequency synthesis begins on the A18 Reference with a 10 MHz reference *Oven Controlled Xtal Oscillator* (OCXO). The OCXO may be phase-locked to an external reference or operate stand-alone. The 10 MHz OCXO phase locks a 1 GHz *Surface Transverse Wave* (STW) resonator on the A18 Reference. The frequency accuracy of the 10 MHz reference is translated to the final output frequency accuracy. The 10 MHz and 1 GHz signals are distributed throughout the signal generator. In this discussion, we will concentrate on the 10 MHz going to the A16 Frac-N and the 1 GHz going to the A15 Sampler. The 10 MHz is also routed to the *Internal Modulation Source Function Generator* located on the A23 Motherboard, and to the rear panel. The 1 GHz is also routed to the heterodyne circuit on the A13 Output.

Frequency generation is accomplished with a YIG Oscillator (YO) on the A21 YTO Driver. When the instrument's frequency is changed, a DAC on the A21 YTO Driver is used to coarse tune the YIG.

The 10 MHz reference is part of a Phase Lock Loop (PLL) on the A16 Frac-N. The PLL tunes a Voltage Controlled Oscillator (VCO), which is then routed to the A15 Sampler.

On the A15 Sampler, the 1 GHz from the A18 Reference is divided down and mixed to generate 750 MHz. It is then mixed with the VCO from the A16 Frac-N with the resulting IF being one input to a phase comparator. The other input to the phase comparator is derived from a portion of the YO signal being fed back to the A15 Sampler. The A15 Sampler down converts the YO signal to a Sampler IF. The Sampler IF is the other input to the phase comparator. The output of the phase comparator is integrated and routed to the A21 YTO Driver. The A21 YTO Driver sums the coarse tune DAC with the integrator signal from the A15 Sampler and uses the signal to fine tune the YO.

### A18 Reference

Essential to frequency accuracy and low phase noise are clean and stable reference signals. The reference signals on the A18 Reference are a 10 MHz OCXO and a 1 GHz STW. To minimize frequency drift, the 10 MHz oscillator has an oven to maintain a constant crystal temperature. If the instrument has been without electrical power, a "Reference Oven Cold" warning is displayed when it is powered on. The warning message is controlled by a timer and should go out after five minutes of being connected to ac power. The 10 MHz OCXO may also be phase locked to an external 2, 5, or 10 MHz reference. The 10 MHz reference is also available at the signal generator's rear panel and may be used to phase lock other instruments in a system. The 10 MHz OCXO is routed to the A16 Frac-N and is part of a PLL to tune a VCO.

The 1 GHz STW is split with one leg being divided down to 10 MHz. This signal is then phase compared to the 10 MHz OCXO with the difference driving an integrator. The integrator tunes the 1 GHz STW ensuring both reference signals are

phase locked together. The other leg of the 1 GHz signal is routed to the A15 Sampler and A13 Output.

### **A16 Frac-N (Option UNJ or Option 506)**

The A16 Frac-N is used to fine tune the YO on the A21 YTO Driver. Two proprietary application specific integrated circuits (ASIC), named Paren and Tesera, are used to tune a VCO on the A16 Frac-N. The VCO is capable of being tuned from 500 MHz to 1000 MHz. The VCO is routed to the A15 Sampler where it is down converted and compared to a sampled frequency of the YO.

The tuning of the VCO begins with a 10 MHz signal from the A18 Reference. The 10 MHz reference is divided down to 5 MHz to reduce fractional spurs in the phase detector. The 5 MHz reference is then phase compared to a divided down VCO output. The primary division of the VCO is performed within the Tesera pre scaler ASIC. The pre scaler divisor is provided by the Paren ASIC. The output of the phase detector is integrated and then used to tune the VCO.

The baseband FM and PM from the A18 Reference are added to the VCO on the A16 Frac-N. There are two different paths for the modulation to be added to the VCO which are dependent on the modulation rate. If the modulation rate is outside the VCO PLL bandwidth, the baseband signal is scaled and applied to the VCO. A portion of the baseband signal is also split off and routed to the A21 YTO Driver's FM coil. If the baseband rate is within the PLL bandwidth, the signal is scaled and sent to the Sigma-Delta Modulator for digitizing. The digitized signal is used by Paren and Tesera to change the VCO tuning and implements the FM.

### **A15 Sampler (Option UNJ or Option 506)**

A portion of the A21 YTO Driver's YO signal is split off and routed to the A15 Sampler. This signal is sampled and phase compared to a down converted VCO from the A16 Frac-N. The difference signal is integrated and sent back to the A21 YTO Driver's main coil to tune the YO.

The process begins with a 1 GHz signal from the A18 Reference. The signal is split with one leg being divided down to 250 MHz. This signal is used as an LO for a mixer. The RF to the mixer is the 1 GHz reference. The 750 MHz IF is split twice, which means that there are three 750 MHz reference signals being used on the A15 Sampler. For clarity, let us label these references as 750 MHz (A), 750 MHz (B), and 750 MHz (C). The 750 MHz (C) reference is multiplied by a variable fraction, which is less than one, to reduce the frequency. This signal is phase compared to a Mixer IF (14 to 150 MHz). The difference is integrated and used to tune a VCO (618 to 905 MHz) on the A15 Sampler.

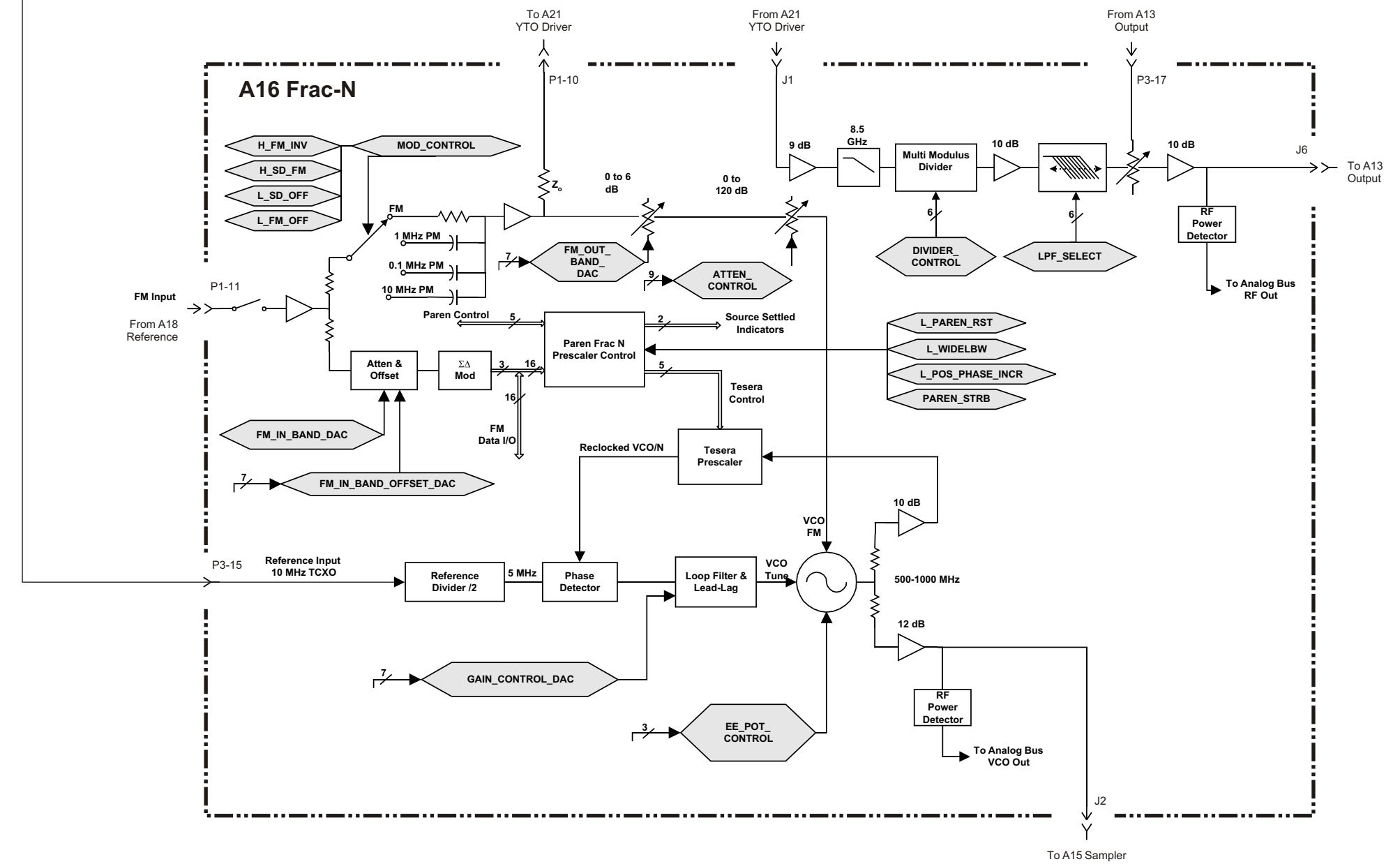
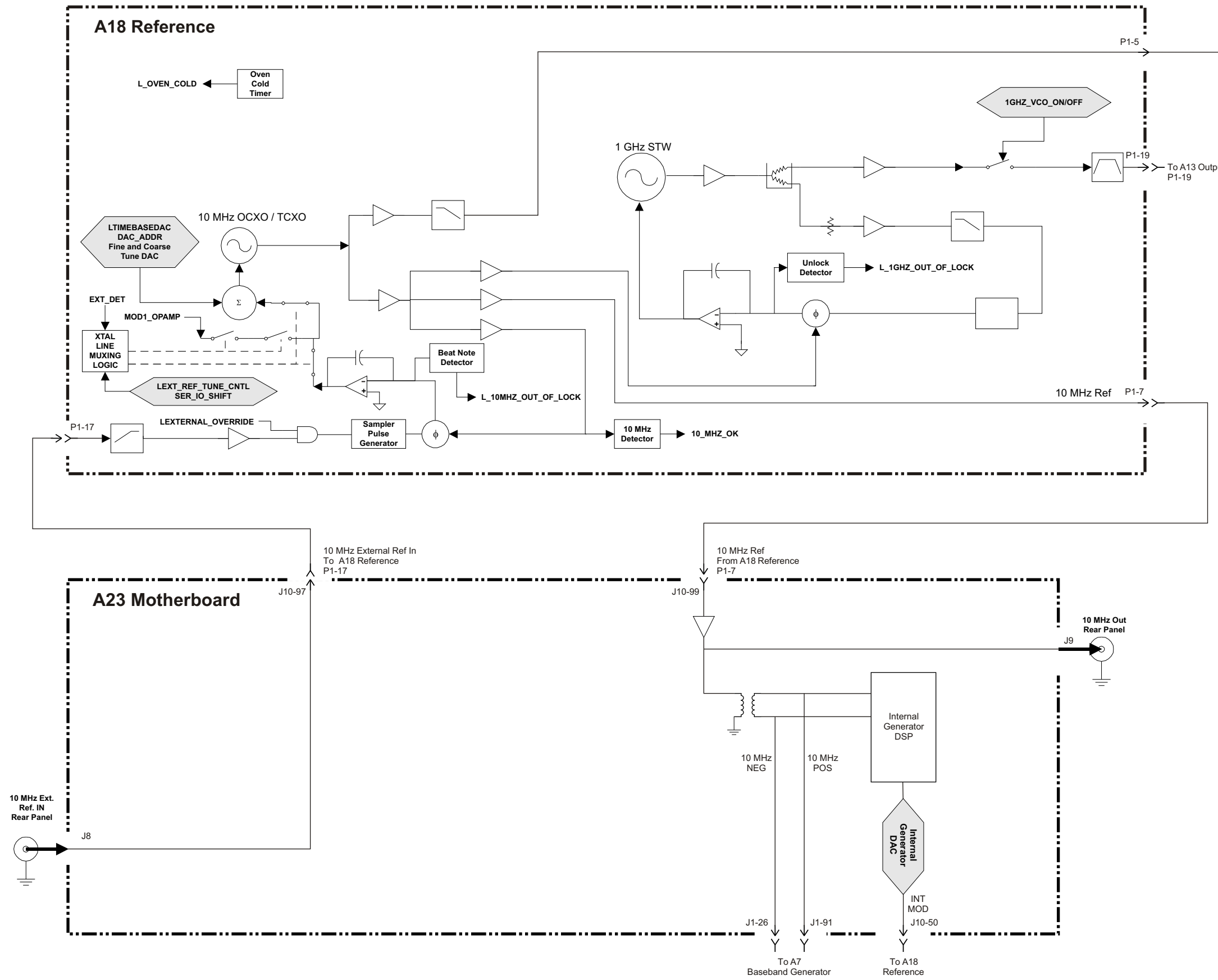
The output of the VCO on the A15 Sampler is split with one leg being mixed with the 750 MHz (B) reference signal to produce the Mixer IF. The other leg of the VCO on the A15 Sampler is used as the LO for the sampler. The RF for the sampler is provided by the A21 YTO Driver's YO signal. The Sampler IF (30 to 70 MHz) is one input to a phase comparator. The other input to the phase comparator is the down converted VCO on the A16 Frac-N. Depending on the instrument mode of operation, the VCO on the A16 Frac-N has two independent paths for down conversion. Mode 1 optimizes phase noise for offsets < 10 kHz by dividing down the VCO on the A16 Frac-N. Mode 2 optimizes phase noise for offsets > 10 kHz, by mixing the VCO on the A16 Frac-N with the 750 MHz (A) reference. In either mode, the down converted signal is the other input to the phase converter. The output of the phase comparator is integrated and routed back to the A21 YTO Driver's main coil to tune the YO.

### **A21 YTO Driver**

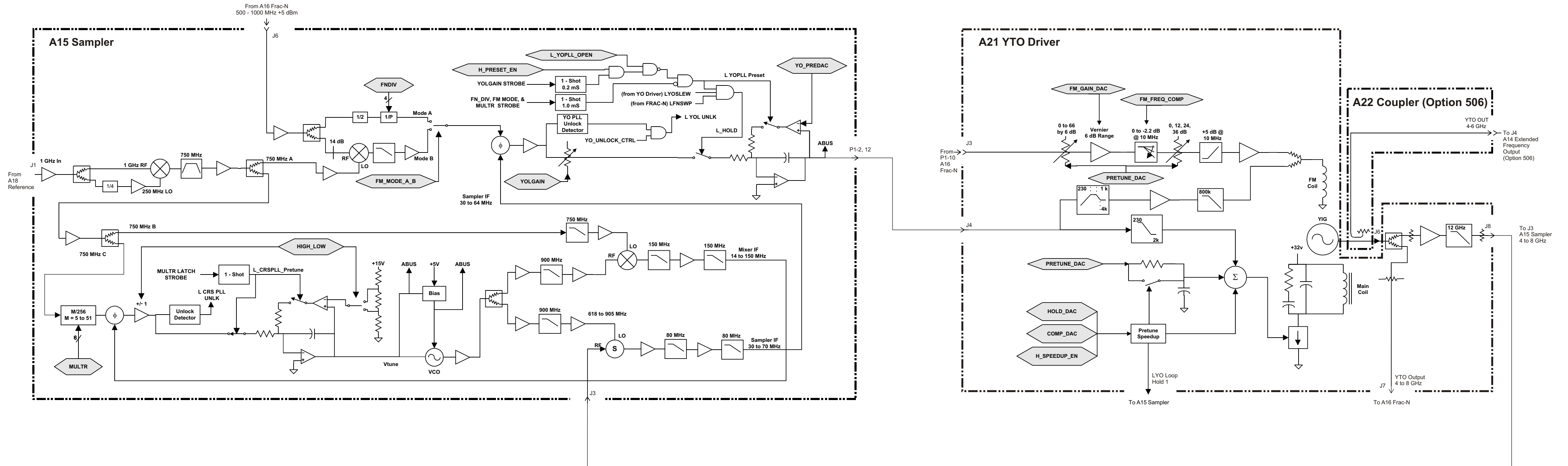
The operating frequency of the YO on the A21 YTO Driver is from 4 to 8 GHz. YO tuning is accomplished by varying the current through the main coil. This causes the coil's magnetic field to change, which tunes the YO resonant frequency. When the signal generator's frequency is changed, a coarse tune DAC voltage is converted to a current to bias the main coil. The YO output is split with a portion of the YO signal being fed back to the A15 Sampler. The other portion of the YO signal is routed through the A16 Frac-N to the A13 Output. For Option 506, the YO is coupled off of the A21 YTO Driver and routed to the A14 Extended Frequency Output. The A15 Sampler uses the YO signal to generate a fine tune voltage, which is routed back to the A21 YTO Driver. This voltage is summed with the coarse tune DAC voltage and converted to a current. The current is used to bias the main coil and fine tunes the YO. The YO is split on the A13 Output with a portion of it being routed to the rear panel as the coherent carrier (COH Carrier).

For high modulation rates, the baseband FM is routed from the A16 Frac-N to the A21 YTO Driver. The signal is scaled and then used to bias the FM coil and tune the YIG.

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## RF Path Block Description for Option UNJ or Option 506

### Overview

The RF Path for signal generators with Option UNJ and Option 506 is made from portions of the following:

- A18 Reference (Option UNJ or Option 506)
- A15 Sampler (Option UNJ or Option 506)
- A16 Frac-N (Option UNJ or Option 506)
- A21 YTO Driver (Option UNJ or Option 506)
- A13 Output (Option UNJ)  
or A13 Output (Option UNB)
- AT1 Electronic Attenuator (Option 501, 502, 503, 504 or Option UNJ)  
with A28 Reverse Power Protection (Option 501, 502, 503, 504 or Option UNJ)  
or AT1 High-Power Mechanical Attenuator (Option UNB)  
with A29 DC Blocking Capacitor (Option 506)

The RF Path consists of a portion of the A16 Frac-N, A21 YTO Driver, and the A13 Output. Frequency generation is accomplished with a YIG Oscillator (YO) on the A21 YTO Driver. The YO is tuned by the A16 Frac-N and the A15 Sampler. The YO signal is then routed through the A16 Frac-N to the A13 Output. The A13 Output contains the Automatic Level Control (ALC) loop for maintaining leveled power. For Option 506, an additional output board, A14 Extended Frequency Output, is added to control power for frequencies greater than 4 GHz to 6 GHz.

For RF frequencies below 250 MHz, a heterodyne mixer is switched into the RF path. The RF signal is mixed with a 1 GHz LO, provided by the A18 Reference, to produce the final 100 kHz to < 250 MHz output; specifications apply to frequencies  $\geq$  250 kHz.

AM, Pulse/Burst, and I/Q baseband signals modulate the RF on the A13 Output. The AM and Pulse modulation originate on the A17 Reference board. While the I/Q and Burst modulation originate on the A7 Baseband Generator. The I/Q signal is routed via the A10 I/Q Multiplexer to the A13 Output. The Burst modulation signal is routed via the A18 Reference to the A13 Output.

The RF signal leaving the A13 Output is then routed through an electronic attenuator. The attenuator also includes a Reverse Power Protection (RPP) circuit to prevent damage to the signal generator. Option UNB replaces the electronic attenuator with a mechanical attenuator. Option 506 doesn't include an RPP circuit, but instead uses the A29 DC Blocking Capacitor (Option 506).

The signal generator's maximum frequency and power are dependent on its options.

### A21 YTO Driver (Option UNJ or Option 506)

The operating frequency of the YO on the A21 YTO Driver is from 4 to 8 GHz. YO tuning is accomplished by varying the current through the main coil. This causes the coil's magnetic field to change, which tunes the YO resonant frequency. When FM is enabled, both the FM coil and main coil are used to tune the YO. The A15 Sampler and A16 Frac-N tune the main coil and FM coil respectively. For more details on the tuning of the YO, refer to [“Frequency Synthesis Block Description for Option UNJ and Option 506”](#) on page 1-133.

The YO output is then split and routed to the A16 Frac-N and the A15 Sampler. The A15 Sampler path is part of the Frequency Synthesis Block.

### A16 Frac-N (Option UNJ or Option 506)

The A16 Frac-N is part of the Frequency Synthesis Block and the RF path. Depending on the desired output frequency, the

A16 Frac-N divides down the YO frequency. The A16 Frac-N also provides amplification and filtering for the YO signal prior to it being routed to the A13 Output.

A pre-level bias voltage generated by the A13 Output is fed back to the A16 Frac-N. The pre-level voltage is used to bias a pin diode and control the amount of RF amplification on the A16 Frac-N.

### A13 Output

The functions of the A13 Output include output power leveling, A16 Frac-N pre-leveling, pulse/burst, I/Q and amplitude modulation (AM).

The filtered RF signal from the A16 Frac-N is amplified and filtered by the A13 Output. After the first stage of amplification on the A13 Output, the RF signal is split with one path becoming the coherent carrier. The coherent carrier is free of any AM or Pulse/Burst modulation and is available to the user at the COH Carrier on the signal generator's rear panel. The other path continues through the A13 Output. If I/Q modulation is enabled, the RF signal is routed through the I/Q modulator, otherwise it is bypassed. Depending on the output frequency, different bandpass filters are switched in to remove unwanted components from the RF signal. After filtering, the RF signal is split with a portion of the signal being detected and becoming the pre-level bias voltage used by the A16 Frac-N.

Output power leveling is accomplished with an Automatic Leveling Control (ALC) loop. The ALC has two modes of operation: either ALC open or ALC closed. For normal operation, the ALC loop is closed and the output power is leveled. For some modulation applications, the ALC is opened and an internal Power Search routine may be used to achieve amplitude accuracy.

When the loop is closed, a portion of the RF output is detected and compared to the sum of the ALC reference voltage and baseband AM. The difference is then applied to an integrator. The integrator output will ramp either positive or negative, based on the power of the input signal. If there isn't any difference between the detected voltage and the sum of the ALC reference voltage and baseband AM, the integrator output will remain unchanged. The output voltage ramp is converted to a current and used to bias the pin diodes in the ALC modulator. The pin diodes vary the amount of attenuation in the RF path and thus maintain leveled output power.

The A13 Output has two independent integrators to facilitate fast power switching. The feedback detector voltage is compared to one ALC reference voltage for one power level. While at the same time, a second independent ALC reference voltage is set up for the second power level. When the user changes from one power level to the other, the hardware switches between the two integrator circuits and their respective references. This function and its related parameters are available through the front panel **Alternate Amplitude** softkey; this softkey is only present when an electronic attenuator is installed.

Depending upon the application, the user may change the ALC bandwidth from normal to narrow. The defaults are normal if I/Q modulation is off and narrow if external I/Q is on. The softkey is not available if internal I/Q is selected.

When AM is enabled, the baseband signal originates on the A18 Reference. The signal enters the A13 Output as a Log AM signal. An Antilog circuit converts the Log AM back to Linear AM. As the baseband AM signal changes amplitude, it causes the ALC modulator bias to change proportionately; this is due to the AM signal being summed with the ALC reference voltage prior to the integrator. Care must be taken to ensure the ALC doesn't become unlevelled due to a combination of high carrier power and large modulation depth. The signal generator's total output power equals the sum of the carrier power and the AM sideband power. If this sum is greater than the maximum output power rating, the ALC loop may become unlevelled.

The ALC loop also contains a pulse/burst modulator. The pulse signal originates on the A18 Reference and the burst signal originates on the A7 Baseband Generator (Option 001/601 or Option 002/602). The pin diodes within the modulator are normally biased to provide minimum attenuation. When pulse/burst modulation is used, the pin diodes are biased off and on thus providing maximum or minimum attenuation. This causes the RF signal to pulse on and off. For narrow pulse widths, the ALC is opened. To maintain power level accuracy, a Power Search routine may be used through the front panel. The Power Search routine shuts down the modulation, momentarily closes the ALC loop, and levels. The ALC is then opened up and the modulation is turned back on.

The I/Q modulator is also located on the A13 Output and may be switched in or out of the RF path. Baseband modulation for the I/Q modulator is provided by the A10 I/Q Multiplexer. User selectable low pass filtering may be applied to the baseband I and Q signals. The user may also perform a front panel I/Q calibration. The calibration doesn't require any external stimulus

and will minimize quadrature and gain errors. A portion of the I and Q signals are fed back to the A10 I/Q Multiplexer to minimize offset. When I/Q modulation is enabled, the pre-level bias for the A16 Frac-N is provided by the I/Q modulator.

#### **A14 Extended Frequency Output (Option 506)**

Option 506 extends the output frequency from 4 GHz to 6 GHz by adding a second output board. For CW and modulated signals 4 GHz and above, the A14 Extended Frequency Output is used. The board has the same modulation capability as the A13 Output and the baseband modulation signals are routed to both boards.

The A13 Output and A14 Extended Frequency Output are almost identical with the following differences:

- The YO signal on the A21 YTO Driver is coupled off and routed to the input of the A14 Extended Frequency Output.
- Having the YO bypass the A16 Frac-N, pre-leveling occurs on the A14 Extended Frequency Output.
- The pre-level circuit is prior to the I/Q modulator.
- Unlike the A13 Output, the I/Q modulator on the A14 Extended Frequency Output does not have a bypass path. The modulator is biased off for CW operation.
- The A14 Extended Frequency Output doesn't contain a heterodyne circuit; since this circuit is only used for frequencies below 250 MHz, the heterodyne circuit on the A13 Output is used.
- The A13 Output, 250 kHz to 4 GHz, is routed to the A14 Extended Frequency Output which contains a switch that routes either the A13 Output or the A14 Extended Frequency Output to the optional AT1 Attenuator and to the RF Output connector.

#### **AT1 Electronic Attenuator (Option 501, 502, 503, 504 or Option UNJ) with A28 Reverse Power Protection (Option 501, 502, 503, 504 or Option UNJ) or AT1 High-Power Mechanical Attenuator (Option UNB) with A29 DC Blocking Capacitor (Option 506)**

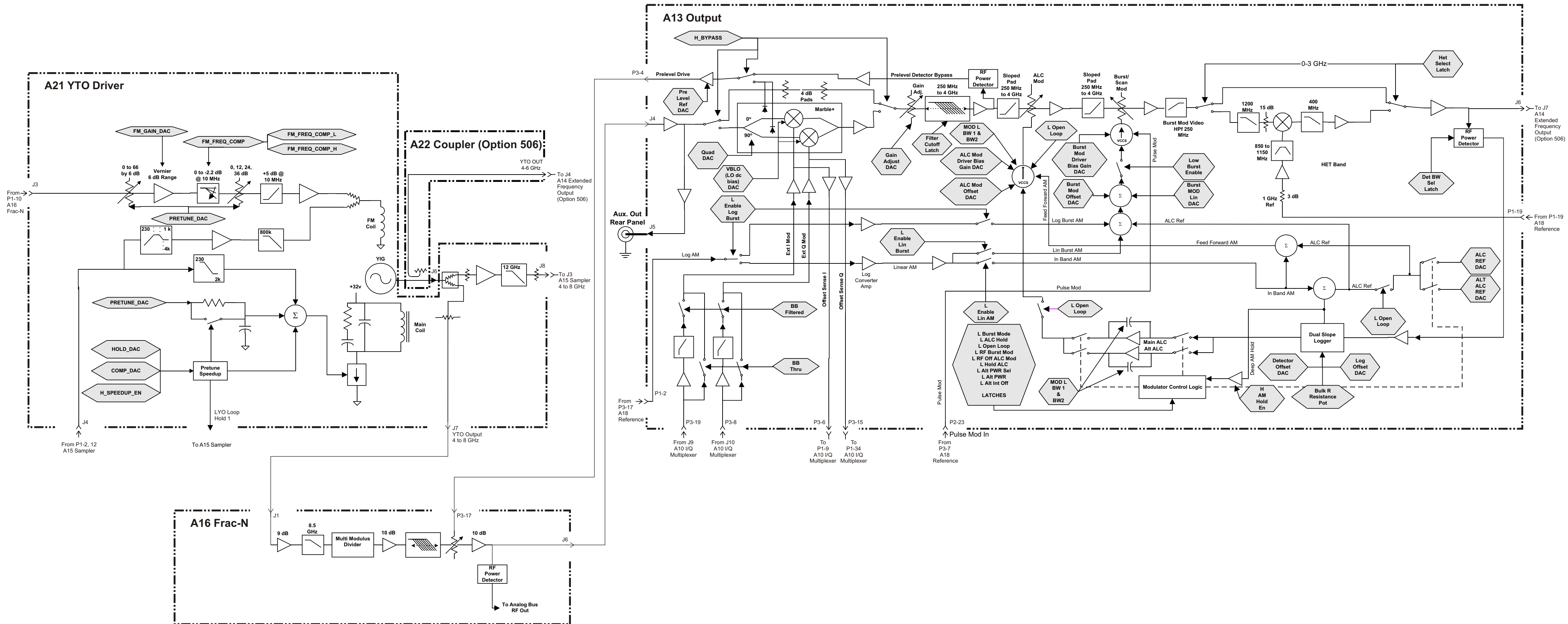
There are two different attenuator modules available. A high-speed, solid state, 130 dB electronic attenuator is available for signal generators with Option 501, 502, 503, 504 and Option UNJ. Signal generators with Options UNB and Option 506 have a 135 dB mechanical attenuator. The mechanical attenuator is standard hardware for Option 506. Both attenuators step in 5 dB increments. When the attenuator changes values the new section is switched into the RF path prior to switching the unused attenuator section out. This prevents damage to sensitive devices under test due to high power being applied. Attenuation sections are switched into operation in a specific order to achieve the best input and output matching for each level of attenuation. The attenuator modules are field replaceable, but not field repairable.

The standard electronic attenuator has the RPP incorporated internally to the attenuator module circuitry. The mechanical attenuator has the RPP circuit as an external module with used with Option 501, 502, 503, 504 and Option UNJ signal generators; the RPP is externally attached to the mechanical attenuator. Signal generators with Option 506 do not use this same RPP because the frequency range of the RPP is limited to 4 GHz.

The RPP circuitry prevents instrument damage when power is mistakenly applied to the instrument's RF output. The RPP circuitry opens an RF relay when 1 Watt of energy is sensed on the RF output. Repeated high levels of reverse power degrade the performance of the RPP or completely bypass the open RF relay. The RPP may be reset through the front panel softkeys.

There is no RPP module available for Option 506, which covers the frequency range of 4 GHz to 6 GHz. Instead, an A29 DC Blocking Capacitor (Option 506) is used to block dc from damaging the AT1 High-Power Mechanical Attenuator (Option UNB).

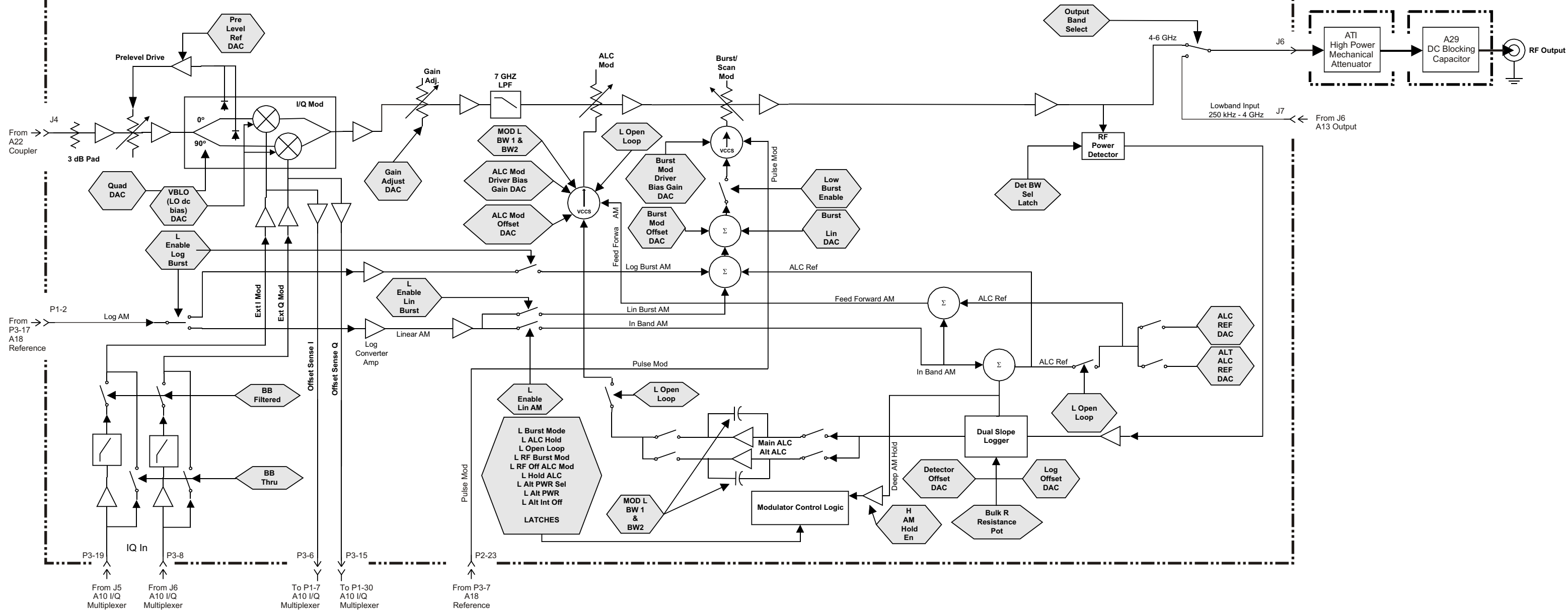
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RF Path Block Diagram for Option UNJ and Option 506 ( Page 1 of 2)



# A14 Extended Frequency Output (Option 506)







## Analog Modulation Block Description for All Options

### Overview

Analog modulation for all options of signal generators is made from portions of the following:

- A18 Reference (Option 501, 502, 503, 504)  
or A18 Reference (Option 501, 502, 503, 504 with Option 1E5)  
or A18 Reference (Option UNJ or Option 506)
- A17 Synthesizer (Option 501, 502, 503, 504)
- A10 I/Q Multiplexer (Option 501, 502, 503, 504 or Option UNJ)  
or A10 I/Q Multiplexer (Option 506)
- A13 Output (Option 501, 502, 503, 504 or Option UNJ)  
or A13 Output (Option UNB)
- A7 Baseband Generator (Option 001/601 or Option 002/602)
- A23 Motherboard
- A15 Sampler (Option UNJ or Option 506)
- A21 YTO Driver (Option UNJ or Option 506)

The analog modulation block implements all analog modulations such as AM, FM, PM, and PULSE. The signal generator can be modulated using internal or external signals:

- Internal modulating signals are supplied by an *Internal Modulation Source Function Generator* located on the A23 Motherboard.
- External modulating signals can be ac or dc coupled through the EXT 1 and EXT 2 inputs.

The input impedance for the EXT 1 and EXT 2 inputs are 50 ohms nominal and are designed so that a +1 V<sub>p</sub> or a -1 V<sub>p</sub> produces the indicated deviation or depth. When ac modulating signals are used and ac coupled inputs are selected for FM/PM or AM, HI/LO annunciators will light if the peak input voltage differs by more than  $\pm 3\%$  of 1 volt. This is accomplished using the over/under Mod detector.

I/Q and Burst signals from the *Digital Modulation* block can also be used as analog modulating signals. With the exception of Burst Pulse, all modulation signals can be utilized to drive external equipment using the LF OUTPUT port on the front panel.

Except for FM with PM, all modulation types can be simultaneously enabled. AM, FM, and PM can sum simultaneous inputs from any two sources (INT, EXT 1, EXT 2, AUDIO 1, AUDIO 2) allowing for composite modulating signals. Any given source (INT, EXT 1, EXT 2, AUDIO 1, AUDIO 2) can only be routed to one activated modulation type.

### A10 I/Q Multiplexer (Option 501, 502, 503, 504 or Option UNJ) or A10 I/Q Multiplexer (Option 506)

The A10 I/Q Multiplexer supplies the Audio 1 (I) and the Audio 2 (Q) signals to the A18 Reference. These signals are either from the A7 Baseband Generator (Option 001/601 or Option 002/602) or supplied externally through the EXT IQ-IN on the front panel. Once on the A18 Reference, these two signals can be switched to function as the AM, FM/PM, and PULSE modulating signals. In the wideband-AM mode, the user inputs the modulation signal through the "I" input on the front panel; this signal is routed through the A10 I/Q Multiplexer and then to the *I/Q Modulator* located on the A13 Output. Once there, the "I" input signal can directly modulate the main RF signal, thus avoiding the bandwidth limitation of the ALC. This method

allows for faster AM rates of dc to 10 MHz (wideband-AM mode) compared to dc to 10 kHz (AM mode).

### **A7 Baseband Generator (Option 001/601 or Option 002/602)**

The A7 Baseband Generator (Option 001/601 or Option 002/602) supplies the Burst Pulse signal to the A18 Reference. The Burst Pulse is used to drive the analog pulse modulation circuitry. This signal is not routed through the multiplexer circuit on the A18 Reference, but goes directly to the pulse drive output-summing node. Here, the Burst Pulse signal can be summed with the multiplexed pulse drive signals or be used alone to drive the pulse modulation circuitry located on the A13 Output.

### **A23 Motherboard**

The Internal Modulation Source Function Generator is located on the A23 Motherboard. This circuit uses a Digital Signal Processor (DSP) chip to create waveforms digitally. This DSP chip uses the 10 MHz reference standard from the A18 Reference as its main clock. The digital output from the DSP chip is converted to analog by an output Digital-to-Analog Converter (DAC) and the analog waveform is then directed to the A18 Reference. The *Internal Modulation Source Function Generator* produces the following waveforms: Sine, Triangle, Square, Ramp, Noise, Dual Sine, Swept Sine, Pulse, and dc. The amplitude can be set from 0 volts up to +5 volts.

### **A18 Reference (Option 501, 502, 503, 504) or A18 Reference (Option 501, 502, 503, 504 with Option 1E5) or A18 Reference (Option UNJ or Option 506)**

The A18 Reference is the heart of the *Analog Modulation Block* and is responsible for routing all analog modulation drive input signals to the appropriate drive circuits. The *Internal Modulation Source Function Generator* (located on the A23 Motherboard), EXT 1 and EXT 2 (located on the front panel), as well as Audio 1 and Audio 2 (located on the A10 I/Q Multiplexer) input signals can all be individually switched to AM, FM, and Pulse modulation drive circuits.

The modulation drive outputs are:

- Frequency Modulation (FM)
  - On Option 501, 502, 503, 504 signal generators, this drive signal goes to the A17 Synthesizer (Option 501, 502, 503, 504) and is used to modulate the VCO.
  - On Option UNJ and Option 506 signal generators, this drive signal goes to the A16 Frac-N (Option UNJ or Option 506) and is used to modulate the Frac-N VCO.

It is also routed to the YO DRIVER and used to modulate the FM coil of the YIG; the YIG is located on the A21 YTO Driver (Option UNJ or Option 506).

- Phase Modulation (PM)

PM uses the same signal flow as FM, but is filtered differently.

- PULSE Modulation

This drive signal is directed to the A13 Output and is used to drive the Scan/Burst modulator, which in turn, pulse-modulates the main RF signal.

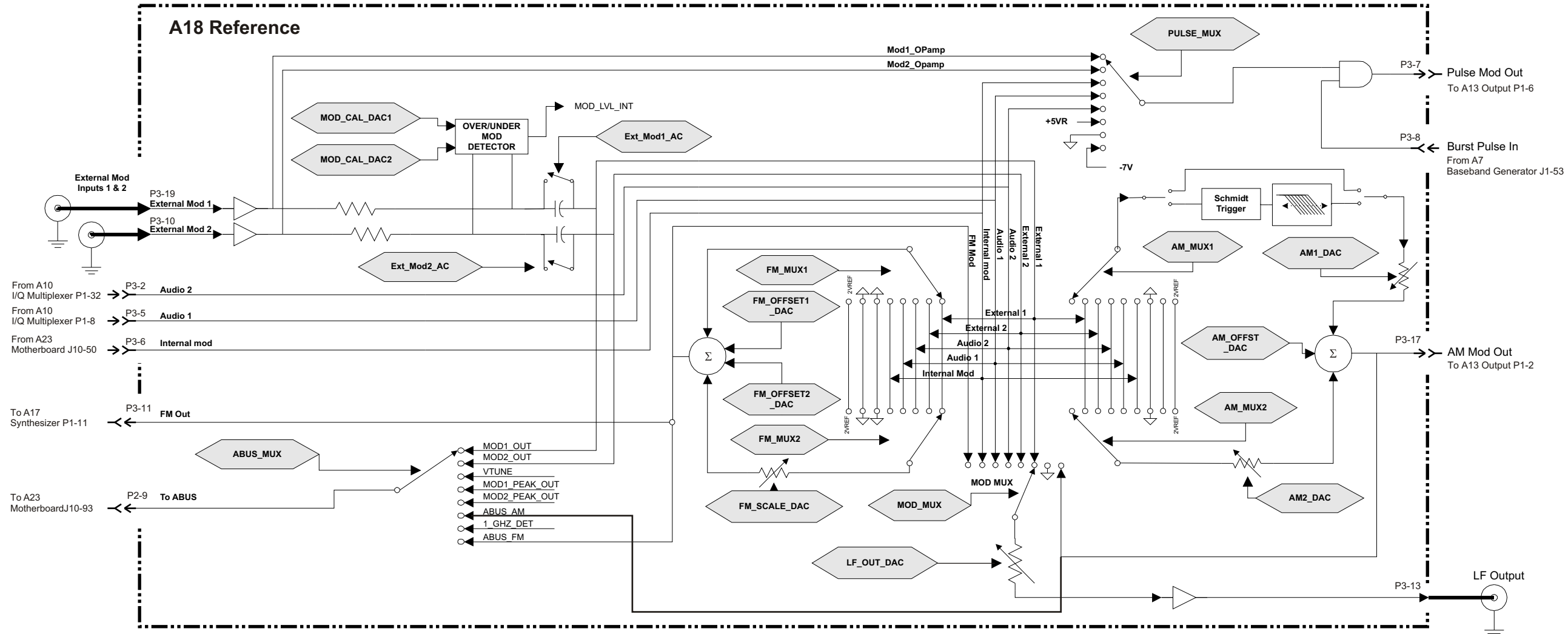
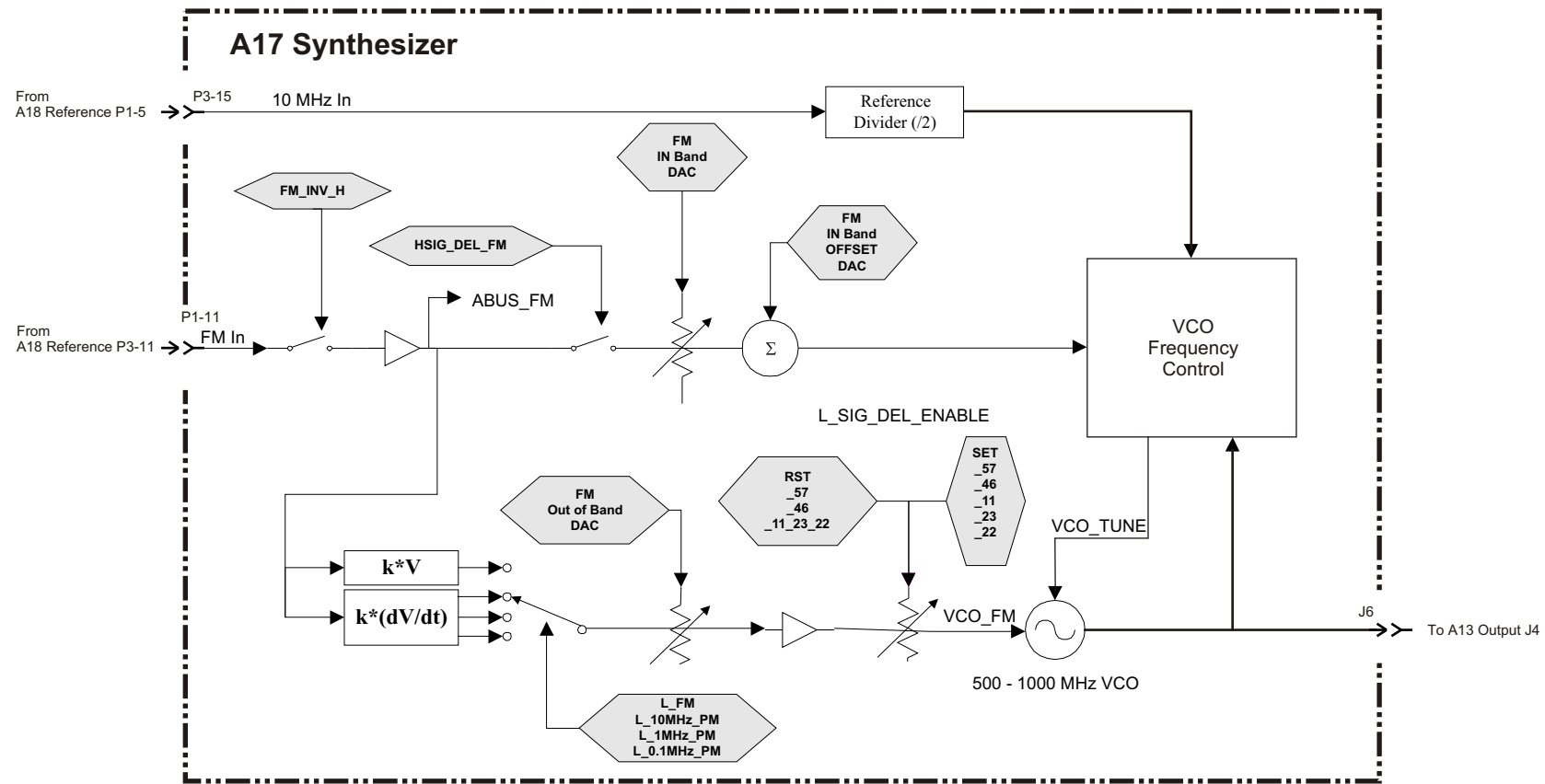
- Amplitude Modulation (AM)

This drive signal is directed to the A13 Output and is used to drive the Scan/Burst modulator and the ALC modulators, which in turn, amplitude modulate the main RF signal.

- LFO out drive signal

This drive signal is directed to the front panel and is used to modulate external equipment.

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## Digital Modulation Block Description for All Options

### Overview

Digital modulation for all options of signal generators is made from portions of two major assemblies:

- A7 Baseband Generator (Option 001/601 or Option 002/602)
- A10 I/Q Multiplexer (Option 501, 502, 503, 504 or Option UNJ)  
or A10 I/Q Multiplexer (Option 506)

Together, these assemblies are responsible for generating and routing the baseband signals to the I/Q modulator located on the A13 Output. The A7 Baseband Generator (Option 001/601 or Option 002/602) consists of data generation, burst control, and symbol building circuitry. The output section of the A7 Baseband Generator (Option 001/601 or Option 002/602) consists of two 16-bit DACs: one for the I signal and one for the Q signal. The data produced from these I/Q signals is converted to analog, amplified, and filtered before being routed to the A10 I/Q Multiplexer. The I/Q signals are routed from the A10 I/Q Multiplexer to the I/Q modulator located on the A13 Output. These I/Q signals are used to modulate the main RF signal that represents one of the many “Digital Modulation/Communications Standard” formats such as TDMA, CDMA, W-CDMA, GSM, NADC and others.

### A7 Baseband Generator (Option 001/601 or Option 002/602)

The A7 Baseband Generator (Option 001/601 or Option 002/602) can be used as a *Dual Arbitrary Waveform Generator*, a *Real Time I/Q Baseband Generator*, or a *Noise Generator*. Waveform personalities can be purchased in order to generate standard formats. Customer supplied waveforms can also be downloaded into the waveform memory to generate customized formats.

The I/Q bandwidth is 80 MHz for internally generated I/Q signals and 160 MHz for externally generated I/Q signals. The A7 Baseband Generator (Option 001/601 or Option 002/602) can generate symbol rates of up to 6.25 Msymbol/sec.

- A7 Baseband Generator (Option 001/601) has 8 Msamples of waveform memory
- A7 Baseband Generator (Option 002) has 32 Msamples of waveform memory
- A7 Baseband Generator (Option 602) has 64 Msamples of waveform memory

Data is sequenced from waveform memory by the Field Programmable Gate Array block (A1-FPGA), passed on to the format builder block (A2-FPGA/Modulator/Filter), and finally to the output section. I and Q DACs at the output convert the digital waveform data to analog. The I and Q output DACs are driven by a sample clock generated by an on-board VCO. The VCO has a frequency range of 200 to 400 MHz. The 10 MHz reference signal from the A18 Reference is supplied and used to phase-lock the sample clock signal of the DACs to the A18 Reference; an external 10 MHz reference signal could also be used.

The Burst Pulse signal from the A7 Baseband Generator (Option 001/601 or Option 002/602) goes to the analog modulation block on the A18 Reference (which is part of the *Frequency Synthesis Block*) and is used to drive the pulse modulator located on the A13 Output. The Burst Pulse signal is used by the digital modulation formats to synchronize the main RF path and ensure maximum on/off level ratio.

The I and Q output signals are the main signals out of the A7 Baseband Generator (Option 001/601 or Option 002/602). They are also the digital modulating signals that go to the A10 I/Q Multiplexer which are then routed to the I/Q modulator on the A13 Output.

### A10 I/Q Multiplexer

The A10 I/Q Multiplexer receives I/Q input signals from the A7 Baseband Generator (Option 001/601 or Option 002/602), external I/Q inputs signals from the front panel, and an I/Q sense feedback signal from the I/Q modulator on the A13 Output. The A10 I/Q Multiplexer inverts and routes the I and Q signals to the rear panel where these signals can then be used to

modulate external equipment. Both inverted and non-inverted signals are available at the rear panel. The A10 I/Q Multiplexer routes the main internal I and Q modulating signal into the I/Q modulator on the A13 Output which is used to modulate the main RF output. These main internal I and Q modulating signals can be either from the A7 Baseband Generator (Option 001/601 or Option 002/602) or supplied through the external I/Q inputs.

The A10 I/Q Multiplexer has two identical paths in which to switch the incoming I/Q signals. Each path has individual gain control and can be switched to a summing node. The output of the summing node can be switched to the EXT outputs and in addition continues on through more gain control circuitry and finally out to the A13 Output. I/Q signals are supplied simultaneously to the A13 Output and to both the inverted and non-inverted external jacks.

The I and Q inputs are 16-bit data generated in the A7 Baseband Generator (Option 001/601 or Option 002/602) either by a waveform personality or by downloading externally. This data is converted to analog, filtered by the I and Q DACs, filters on the A7 Baseband Generator (Option 001/601 or Option 002/602), and then goes to the A10 I/Q Multiplexer.

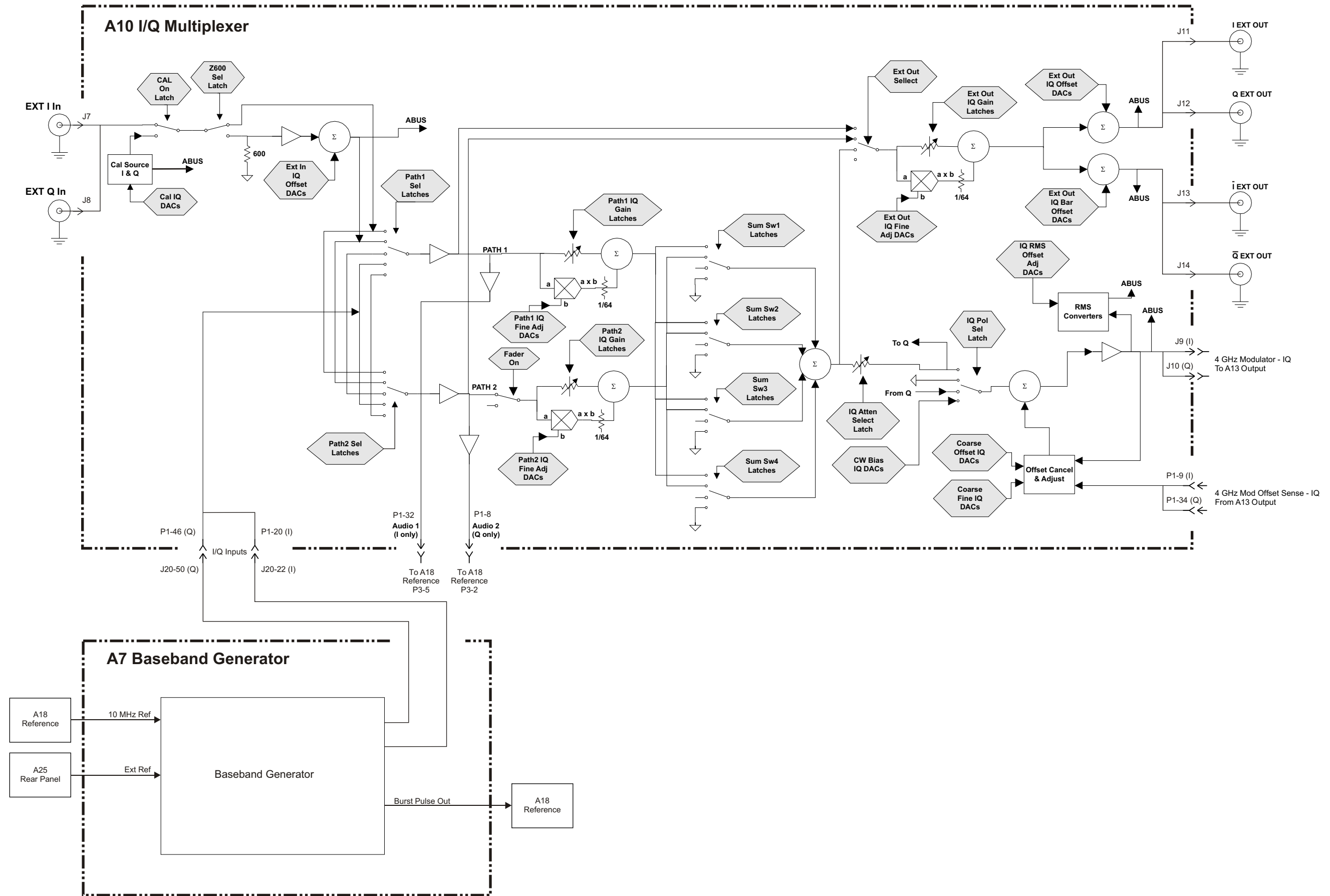
The user, through the front panel, supplies the EXT IN I and EXT IN Q signals. These signals enter the A10 I/Q Multiplexer and can either be switched into a 50 or 600 ohm input impedance.

An internal calibration source supplies proper input levels in order to perform independent self tests. This allows the firmware to check the A10 I/Q Multiplexer board by setting known levels and reading the various nodes without actually needing an I/Q signal.

An Offset Sense signal comes in from the I/Q modulator on the A13 Output and is used to adjust for offsets between the I and Q signals. The Offset Sense signal is compared to the I/Q signal output and the result is used to control the amplitude; the feedback signal minimizes quadrature and gain errors.

The A10 I/Q Multiplexer also routes the Audio 1 and Audio 2 signals over to the *Analog Modulation Block*, located on the A18 Reference, which enables the I (Audio 1) and Q (Audio 2) signals to be used as analog modulation signals. These signals originate from either the I/Q signals supplied by the A7 Baseband Generator (Option 001/601 or Option 002/602) or from the External I/Q inputs as selected by the A10 I/Q Multiplexer.







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## **2 Assembly Replacement**

## **Before You Replace an Assembly**

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**CAUTION** Many of the assemblies in this instrument are very susceptible to damage from electrostatic discharge (ESD). Perform service procedures only at a static-safe workstation and wear a grounding strap.

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Be sure to review the warning and caution statements described in [Chapter 5, “Safety and Regulatory,”](#) on page 5-1 prior to replacing an assembly in your signal generator.

## **After Replacing an Assembly**

After you have replaced or repaired an assembly, certain performance tests and adjustments may have to be performed. [Chapter 4, “Post-Repair Procedures,”](#) on page 4-1, lists the performance tests and adjustments required for each assembly.

## Assemblies That You Can Replace

- “Outer and Inner Instrument Covers” on page 2-4
- “Front Panel” on page 2-6
- “A1 Keyboard” on page 2-8
- “A2 Display” on page 2-11
- “A2DS1 Display Backlight” on page 2-14
- “A3 Front Panel Auxiliary Board” on page 2-17
- “A4 Inverter” on page 2-19
- “A5 Hard Drive (Option 005)” on page 2-21
- “A6 Power Supply” on page 2-24
- “A7 Baseband Generator (Option 001/601 or Option 002/602)” on page 2-26
- “A9 Digital Demodulator (Option 501, 502, 503, 504 with Option 300)” on page 2-28
- “A10 I/Q Multiplexer” on page 2-30
- “A11 Internal Bit Error Rate Analyzer (Option UN7)” on page 2-32
- “A12 CPU” on page 2-35
- “A13 Output” on page 2-37
- “A15 Sampler (Option UNJ or Option 506)” on page 2-41
- “A16 Frac-N (Option UNJ or Option 506)” on page 2-43
- “A17 Synthesizer (Option 501, 502, 503, 504)” on page 2-45
- “A18 Reference” on page 2-47
- “A19 Daughterboard” on page 2-49
- “A19 Daughterboard Cable Removal” on page 2-51
- “AT1 High-Power Mechanical Attenuator (Option UNB) with A28 Reverse Power Protection (Option 501, 502, 503, 504 or Option UNJ)” on page 2-68
- “A20 Downconverter (Option 501, 502, 503, 504 with Option 300)” on page 2-53
- “A21 YTO Driver (Option UNJ or Option 506) and A22 Coupler (Option 506)” on page 2-55
- “A23 Motherboard” on page 2-57
- “A23BT1 Battery” on page 2-60
- “A24 Line Module” on page 2-61
- “Rear Panel” on page 2-62
- “A25 Rear Panel Board” on page 2-64
- “A26 Rear Panel LVDS Board (Option 1EM) and A27 Rear Panel SMB Board (Option 1EM)” on page 2-66
- “AT1 High-Power Mechanical Attenuator (Option UNB) with A28 Reverse Power Protection (Option 501, 502, 503, 504 or Option UNJ)” on page 2-68
- “AT1 High-Power Mechanical Attenuator (Option UNB)” on page 2-70
- “AT1 Electronic Attenuator (Option 501, 502, 503, 504 or Option UNJ)” on page 2-72
- “B1 Power Supply Fan” on page 2-76
- “B2 Small Fan” on page 2-78
- “B3 Daughterboard/Card Cage Fan” on page 2-80
- “RF Output Connector” on page 2-82

## Outer and Inner Instrument Covers

### Tools Required

- T-10 driver
- T-15 driver
- T-20 driver

### Removal Procedures

#### Removing the Outer Cover

Refer to [Figure 2-1](#) for this procedure.

1. Disconnect the power cord.
2. Using a T-20 driver, remove the two strap handles (1) by loosening the screws.
3. Using a T-15 driver, remove the center screws (3) on the four rear-panel feet (2).
4. Remove the four bottom feet (5) and (6) from the cover by pushing and pulling the tab.
5. Slide the outer cover (4) off the frame.

#### Removing the Inner Covers

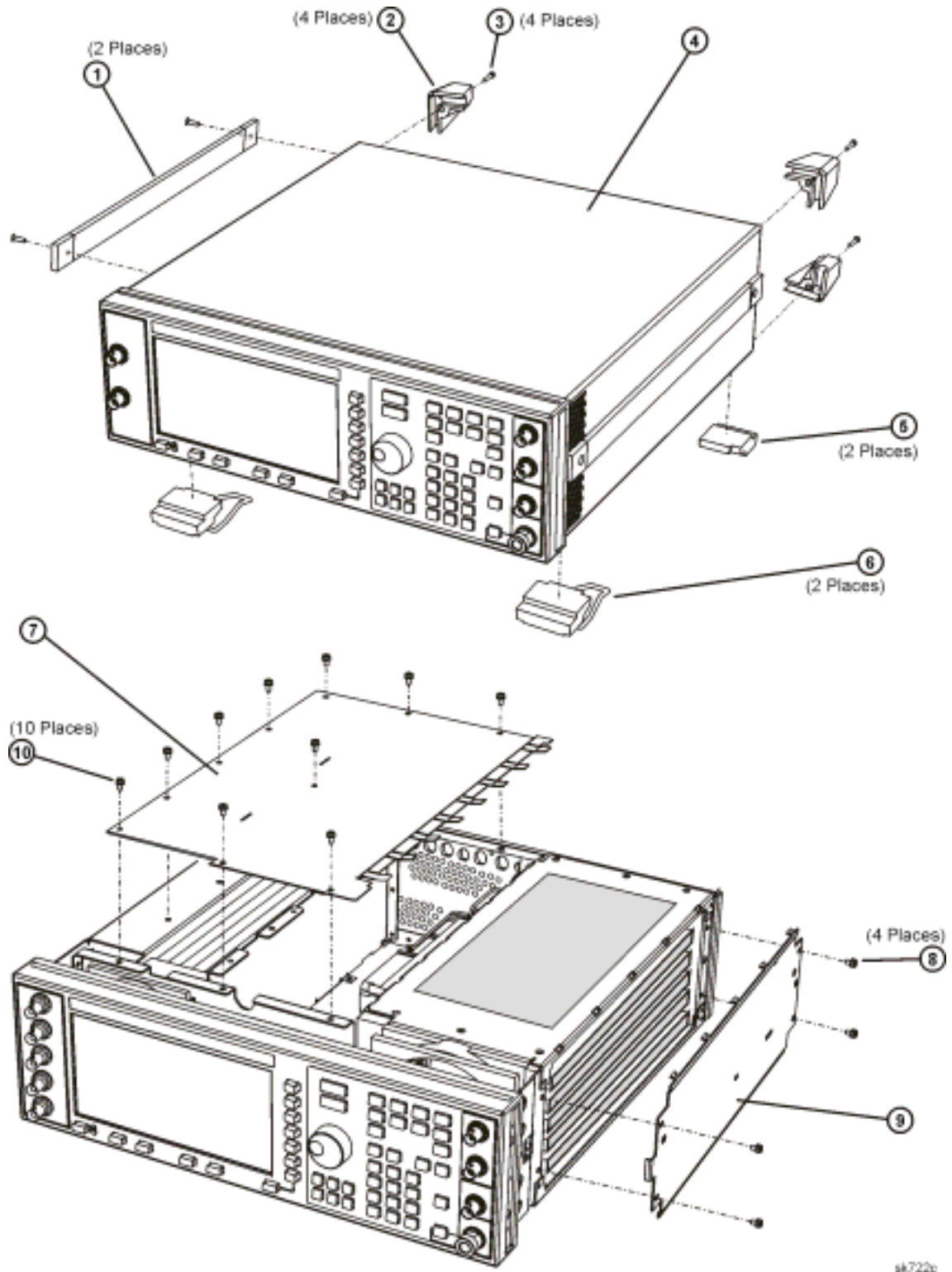
Refer to [Figure 2-1](#) for this procedure.

1. Using a T-10 driver, remove the ten screws (10) from the inner-top cover (7).
2. Remove the inner-top cover.
3. Using a T-10 driver, remove the four screws (8) from the inner-side cover (9).
4. Slide the inner side cover forward to unlock the sheet metal from the chassis.
5. Remove the inner-side cover.

### Replacement Procedure

1. Reverse the order of the removal procedures.
2. Torque all T-10 screws to 9 in-lbs.
3. Torque all T-15 screws to 21 in-lbs.
4. Torque all T-20 screws to 21 in-lbs.

Figure 2-1 Outer and Inner Instrument Cover Removal



## Front Panel

### Tools Required

- T-15 driver
- long-nose pliers

### Removal Procedure

Refer to [Figure 2-2](#) and [Figure 2-3](#) for this procedure.

1. Disconnect the power cord.
2. Remove the covers from the signal generator. Refer to [“Outer and Inner Instrument Covers” on page 2-4](#).
3. Remove the side trims that cover the four screws (2).
4. Using a T-15 driver, remove the four screws (1) from the sides of the frame.

---

**CAUTION** Before removing the front panel from the signal generator, lift and support the front of the signal generator’s frame.

---

5. Slide the front panel over the RF output connector.
6. If you have an A5 Hard Drive (Option 005), disconnect W26 from the A12 CPU.
7. Disconnect the W35 A3 Front Panel Auxiliary Board cable from J30 of the A23 Motherboard.
8. Disconnect the W36 A1 Keyboard cable from J32 of the A23 Motherboard.
9. Using needle-nose pliers, disconnect the following cables:
  - W1 (I INPUT) from J7 of the A10 I/Q Multiplexer
  - W2 (Q INPUT) from J8 of the A10 I/Q Multiplexer
  - W3 (DATA INPUT) from J24 of the A23 Motherboard
  - W4 (DATA CLOCK) from J21 of the A23 Motherboard
  - W5 (SYMBOL SYNC) from J23 of the A23 Motherboard
  - W6 (EXT 1 INPUT) from the A19 Daughterboard (refer to the connector diagram located on the A19 Daughterboard cover)
  - W7 (EXT 2 INPUT) from the A19 Daughterboard (refer to the connector diagram located on the A19 Daughterboard cover)
  - W8 (LF OUTPUT) from the A19 Daughterboard (refer to the connector diagram located on the A19 Daughterboard cover)
10. Remove the front panel.



### Replacement Procedure

1. Reverse the order of the removal procedure.
2. Perform the post-repair adjustments and performance tests that pertain to this removal procedure.
3. Torque all T-10 screws to 9 in-lbs.
4. Torque all T-15 screws to 21 in-lbs.
5. Torque all T-20 screws to 21 in-lbs.

Figure 2-2 Front Panel Cover Strips

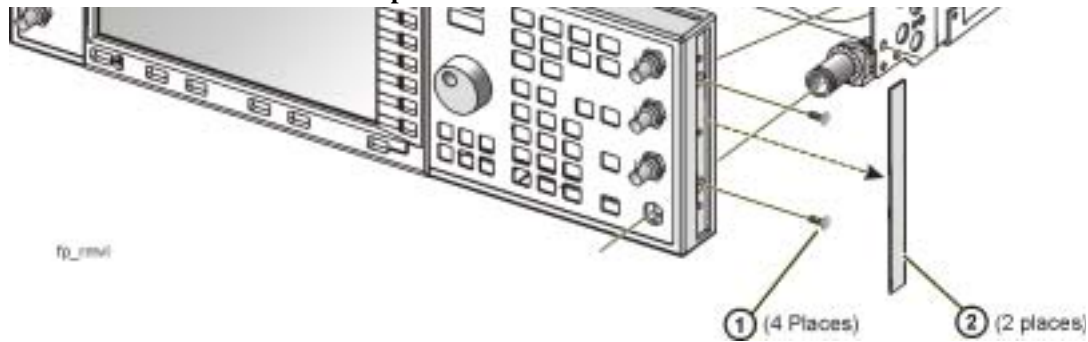
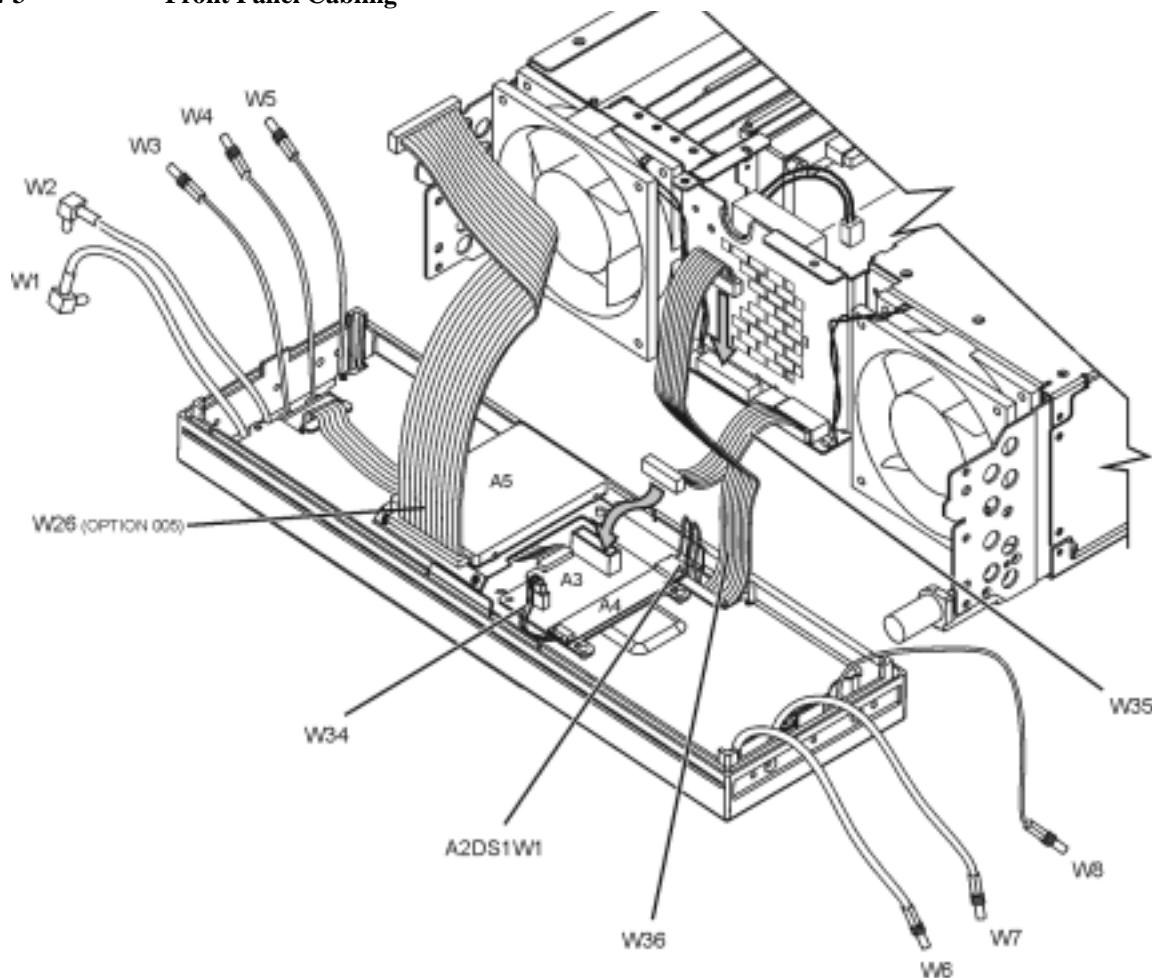


Figure 2-3 Front Panel Cabling



## A1 Keyboard

### Tools Required

- T-10 driver

### Removal Procedure

For instruments with serial prefixes <US4426/MY4426, refer to [Figure 2-4](#) for this procedure.

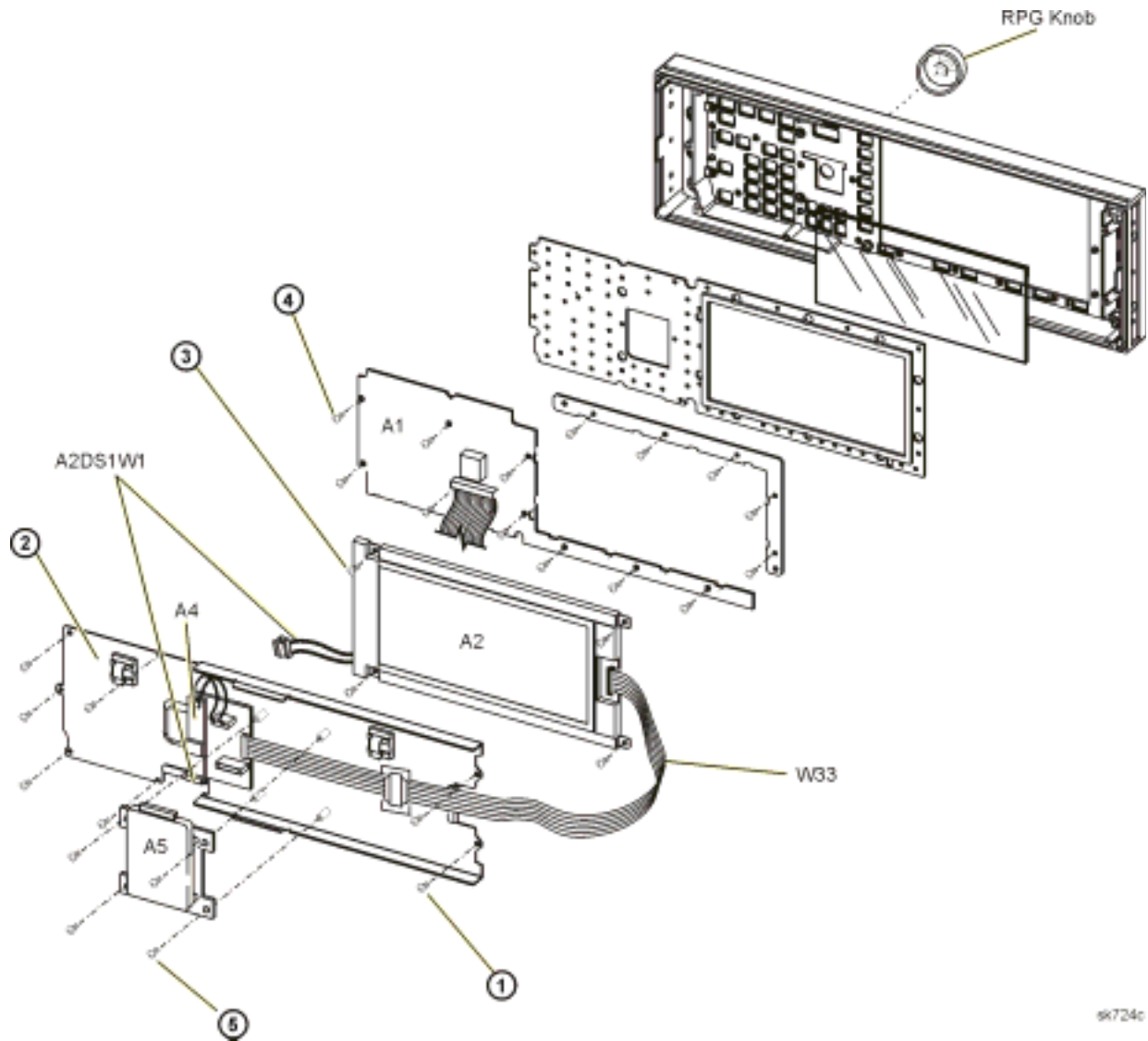
For instruments with serial prefix ≥US4426/MY4426, refer to [Figure 2-5](#) for this procedure.

1. Disconnect the power cord.
2. Remove the front panel. Refer to [“Front Panel” on page 2-6](#).
3. Disconnect the W1 cable of the A2DS1 Display Backlight cable from the A4 Inverter.
4. If you have an A5 Hard Drive (Option 005), use the T-10 driver to remove the screws (5) and lift the hard drive from the display panel’s sheet metal. For instruments with serial prefixes <US4426/MY4426, there are four screws. For instrument with serial prefix ≥US4426/MY4426, there are two screws.
5. Disconnect the W33 ribbon cable from the A3 Front Panel Auxiliary Board by lifting the white tabs on the connector.
6. Using the T-10 driver, remove the seven screws (1) that secure the RFI shielding (2) to the front panel.
7. Remove the metal shielding.
8. Using the T-10 driver, remove the four screws (3) that secure the A2 Display to the front panel.
9. Remove the A2 Display.
10. Remove the RPG-knob by pulling it away from the front panel.
11. Using the T-10 driver, remove the nine screws (4) that secure the A1 Keyboard to the front panel.
12. Remove the A1 Keyboard.

### Replacement Procedure

1. Reverse the order of the removal procedure.
2. Torque all T-10 screws to 9 in-lbs.
3. Perform the post-repair adjustments and performance tests that pertain to this removal procedure.

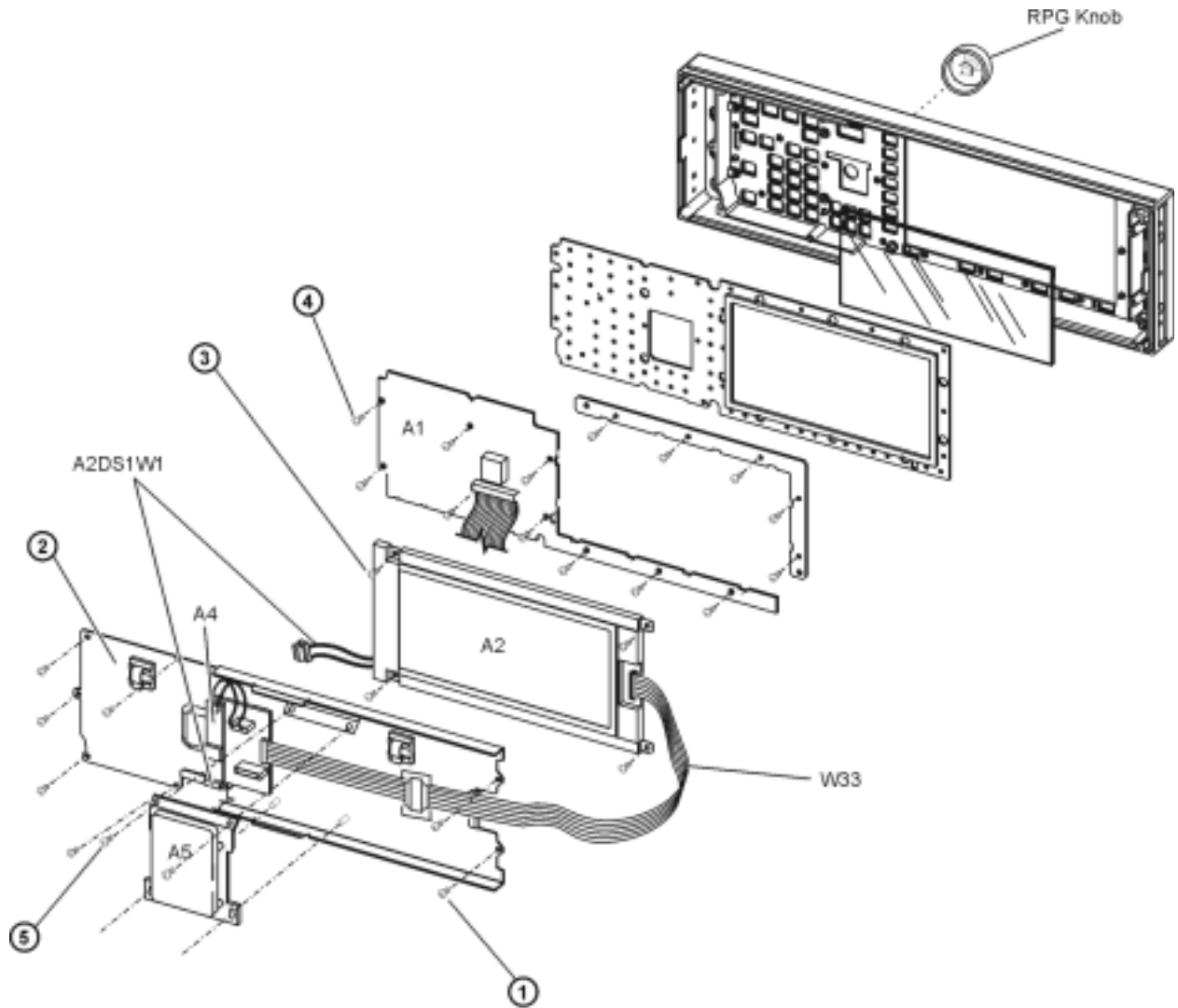
Figure 2-4 A1 Keyboard, for instruments with serial prefixes <US4426/MY4426



9K724c

Assembly Replacement  
A1 Keyboard

Figure 2-5 A1 Keyboard, for instruments with serial prefix  $\geq$ US4426/MY4426



## A2 Display

### Tools Required

- T-10 driver

### Removal Procedure

For instruments with serial prefixes <US4426/MY4426, refer to [Figure 2-6](#) for this procedure.

For instruments with serial prefixes ≥US4426/MY4426, refer to [Figure 2-7](#) for this procedure.

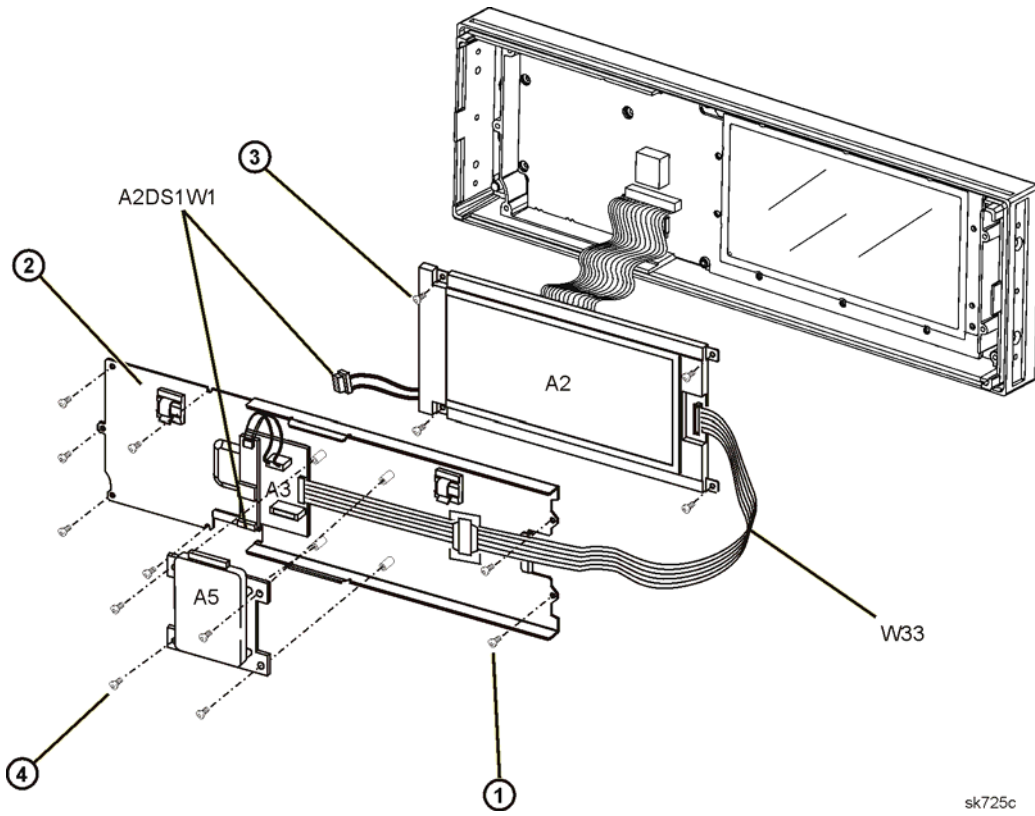
1. Disconnect the power cord.
2. Remove the front panel. Refer to [“Front Panel” on page 2-6](#).
3. Disconnect the W1 cable of the A2DS1 Display Backlight from the A4 Inverter.
4. If you have an A5 Hard Drive (Option 005), use the T-10 driver to remove the screws (4) and lift the hard drive from the display panel's sheet metal. For instruments with serial prefixes <US4426/MY4426, there are four screws. For instrument with serial prefix ≥US4426/MY4426, there are two screws.
5. Disconnect the W33 ribbon cable from the A3 Front Panel Auxiliary Board by lifting the white tabs on the connector.
6. Using the T-10 driver, remove the seven screws (1) that secure the RFI shielding (2) to the front panel.
7. Remove the metal shielding.
8. Using the T-10 driver, remove the four screws (3) that secure the A2 Display to the front panel.
9. Remove the A2 Display.

### Replacement Procedure

1. Reverse the order of the removal procedure.
2. Torque all T-10 screws to 9 in-lbs.
3. Perform the post-repair adjustments and performance tests that pertain to this removal procedure.

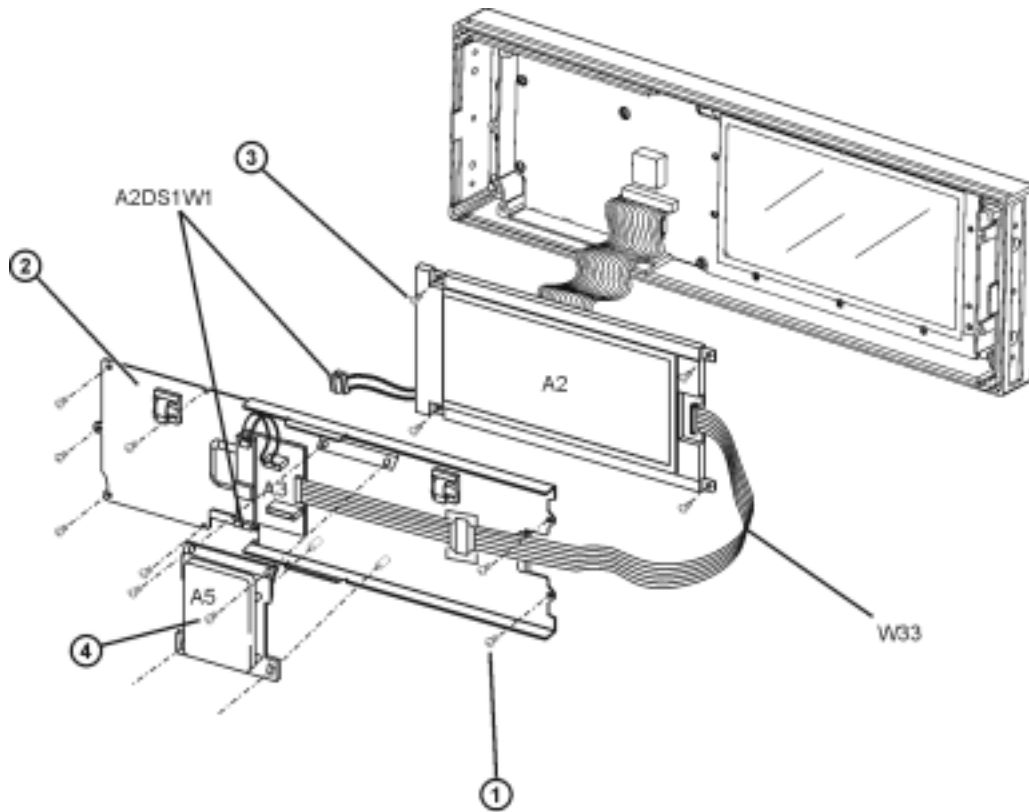
Assembly Replacement  
A2 Display

Figure 2-6 A2 Display, for instruments with serial prefixes <US4426/MY4426



sk725c

Figure 2-7 A2 Display, for instruments with serial prefix  $\geq$ US4426/MY4426



## **A2DS1 Display Backlight**

### **Tools Required**

- T-10 driver
- small phillips screw driver

### **Removal Procedure**

For instruments with serial prefixes <US4426/MY4426, refer to [Figure 2-8](#) for this procedure.

For instruments with serial prefix ≥US4426/MY4426, refer to [Figure 2-9](#) for this procedure.

1. Disconnect the power cord.
2. Remove the front panel. Refer to [“Front Panel” on page 2-6](#).
3. Disconnect the W1 cable of the A2DS1 Display Backlight from the A4 Inverter.
4. If you have an A5 Hard Drive (Option 005), use the T-10 driver to remove the screws (1) and lift the hard drive from the display panel's sheet metal. For instruments with serial prefixes <US4426/MY4426, there are four screws. For instrument with serial prefix ≥US4426/MY4426, there are two screws.
5. Disconnect the W33 ribbon cable from the A3 Front Panel Auxiliary Board by lifting the white tabs on the connector.
6. Using the T-10 driver, remove the seven screws (2) that secure the RFI shielding (3) to the front panel.
7. Remove the metal shielding.
8. Using the T-10 driver, remove the four screws (4) that secure the A2 Display to the front panel.
9. Remove the A2 Display.
10. Using a small phillips screw driver, remove the screw (5) that secures the cover (6) over the backlight.
11. Remove the A2DS1 Display Backlight.

### **Replacement Procedure**

---

**NOTE** When replacing the A2DS1 Display Backlight, make sure the reflective paper is behind the light.

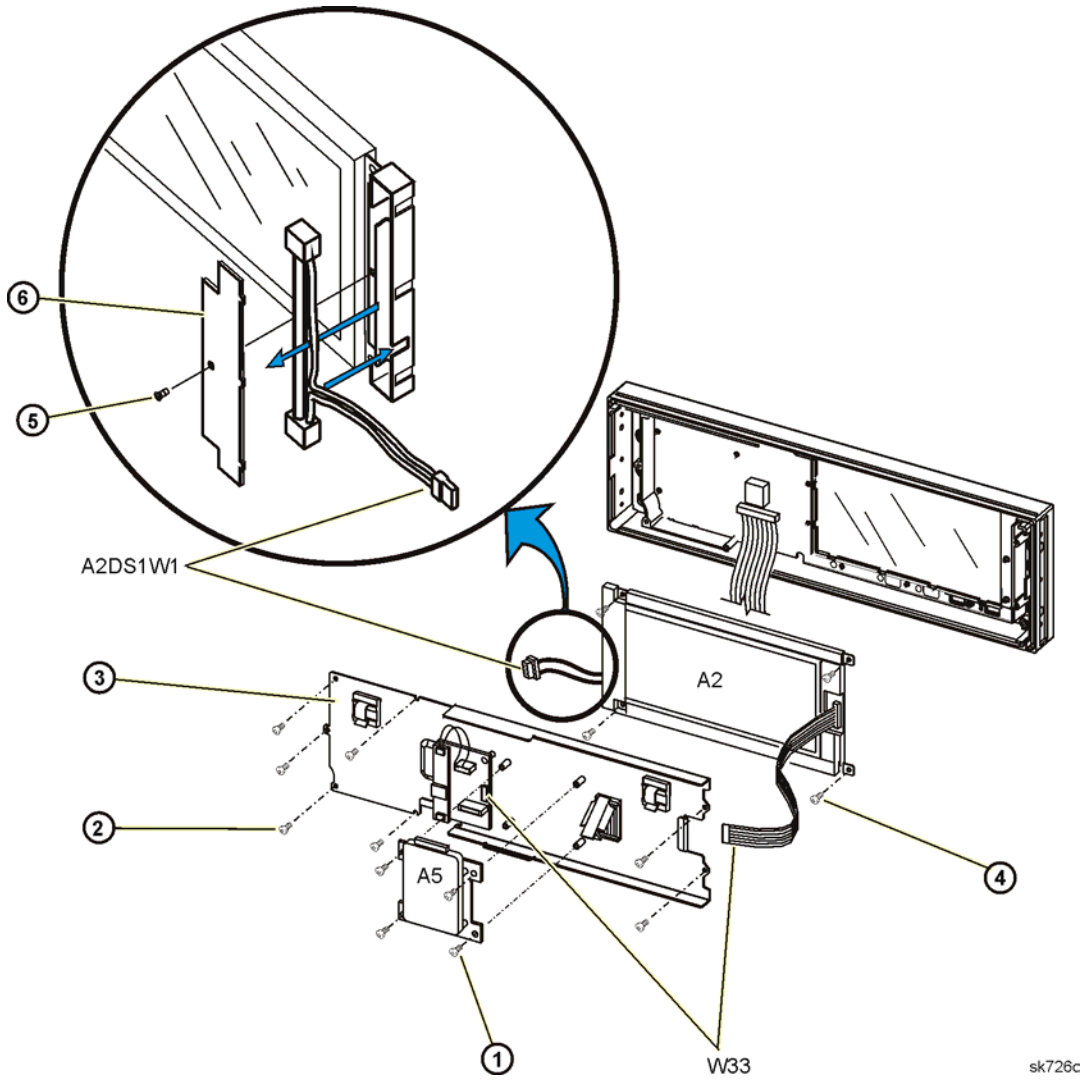
---

1. Reverse the order of the removal procedure.
2. Torque all T-10 screws to 9 in-lbs.
3. Perform the post-repair adjustments and performance tests that pertain to this removal procedure.



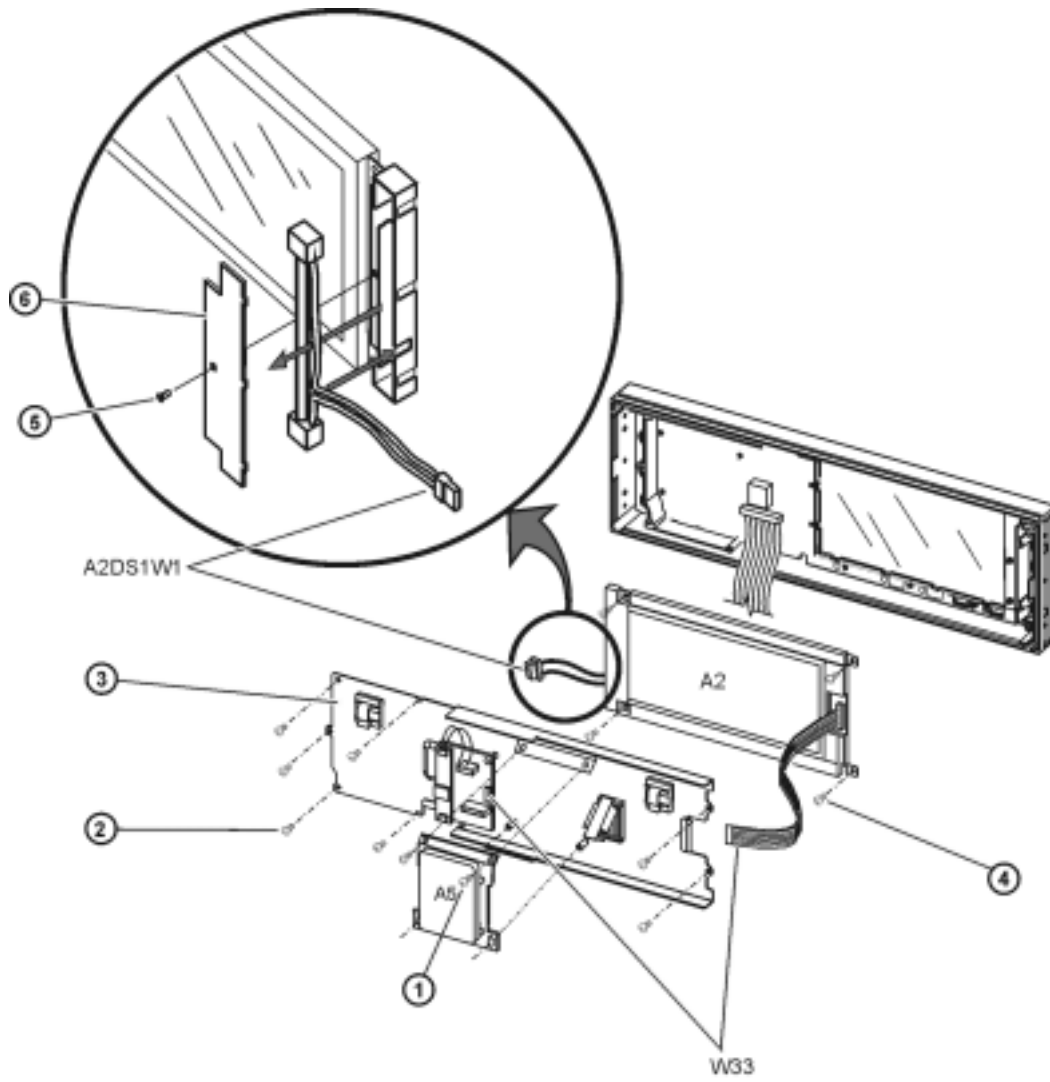
Figure 2-8

A2DS1 Backlight, for instruments with serial prefixes <US4426/MY4426



Assembly Replacement  
A2DS1 Display Backlight

Figure 2-9 A2DS1 Backlight, for instruments with serial prefix  $\geq$ US4426/MY4426



## **A3 Front Panel Auxiliary Board**

### **Tools Required**

- T-10 driver

---

**WARNING** High voltage is present at the A4 Inverter output (CN2-1). Also, be careful of the fan blades on B1, B2, and B3. Keep hands and fingers away from the fan blades to avoid injury!

---

### **Removal Procedure**

Refer to [Figure 2-10](#) for this procedure.

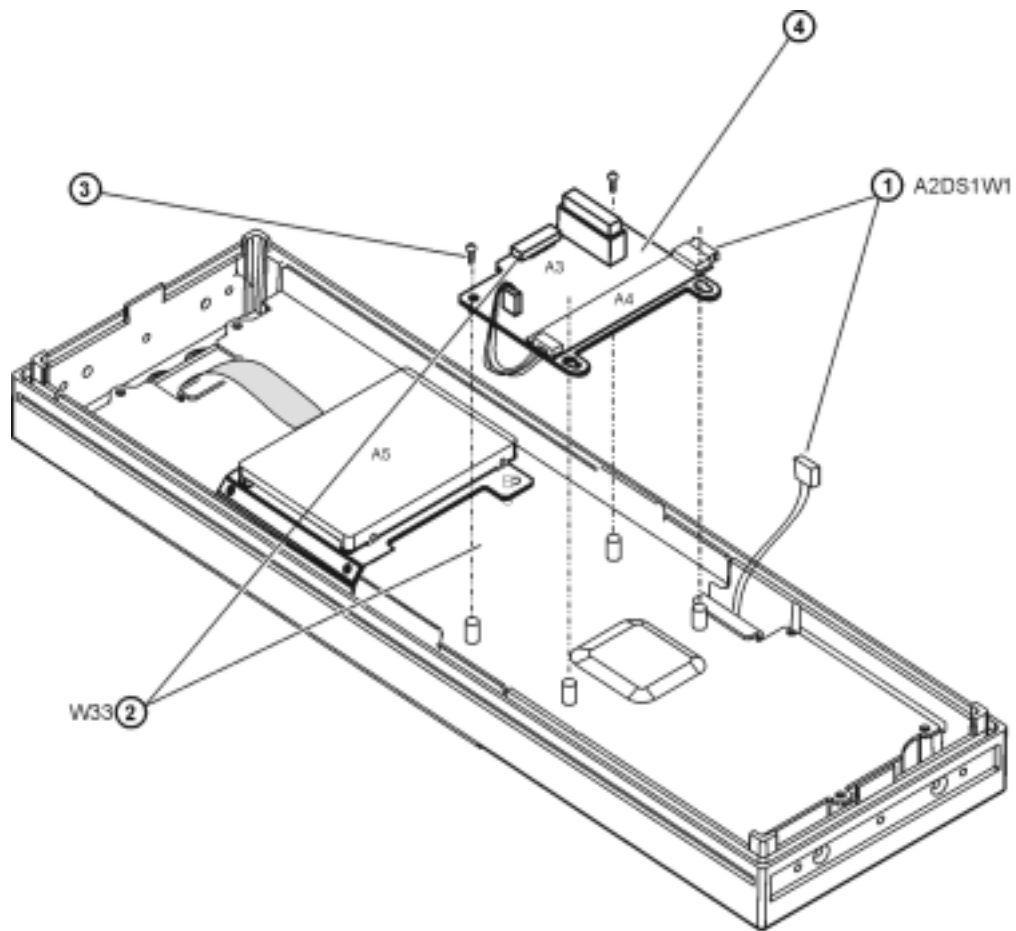
1. Disconnect the power cord.
2. Remove the front panel. Refer to [“Front Panel” on page 2-6](#).
3. If you have an A5 Hard Drive (Option 005), it may be removed to aid the removal and replacement of W33. Refer to [“A5 Hard Drive \(Option 005\)” on page 2-21](#).
4. Disconnect the W1 cable of the A2DS1 Display Backlight (1) from the A4 Inverter.
5. Disconnect the W33 ribbon cable (2) from the A3 Front Panel Auxiliary Board by lifting the white tabs on the connector.
6. Using the T-10 driver, remove the two screws (3) from the A3 Front Panel Auxiliary Board (4) and slide the board to remove it from the key slots.
7. Remove the A3 Front Panel Auxiliary Board.

### **Replacement Procedure**

1. Reverse the order of the removal procedure.
2. Torque all T-10 screws to 9 in-lbs.
3. Perform the post-repair adjustments and performance tests that pertain to this removal procedure.

Assembly Replacement  
A3 Front Panel Auxiliary Board

Figure 2-10 A3 Front Panel Auxiliary Board



---

## A4 Inverter

### Tools Required

- T-10 driver

---

**WARNING** High voltage is present at the A4 Inverter output (CN2-1). Also, be careful of the fan blades on B1, B2, and B3. Keep hands and fingers away from the fan blades to avoid injury!

---

### Removal Procedure

Refer to [Figure 2-11](#) for this procedure.

1. Disconnect the power cord.
2. Remove the front panel. Refer to [“Front Panel” on page 2-6](#).
3. Disconnect the W1 cable (2), of the A2DS1 Display Backlight, from the A4 Inverter.
4. Disconnect the W34 cable (1) from the A4 Inverter.

---

**CAUTION** The A4 Inverter is fragile and can be damaged when pulled away from the velcro attachment.

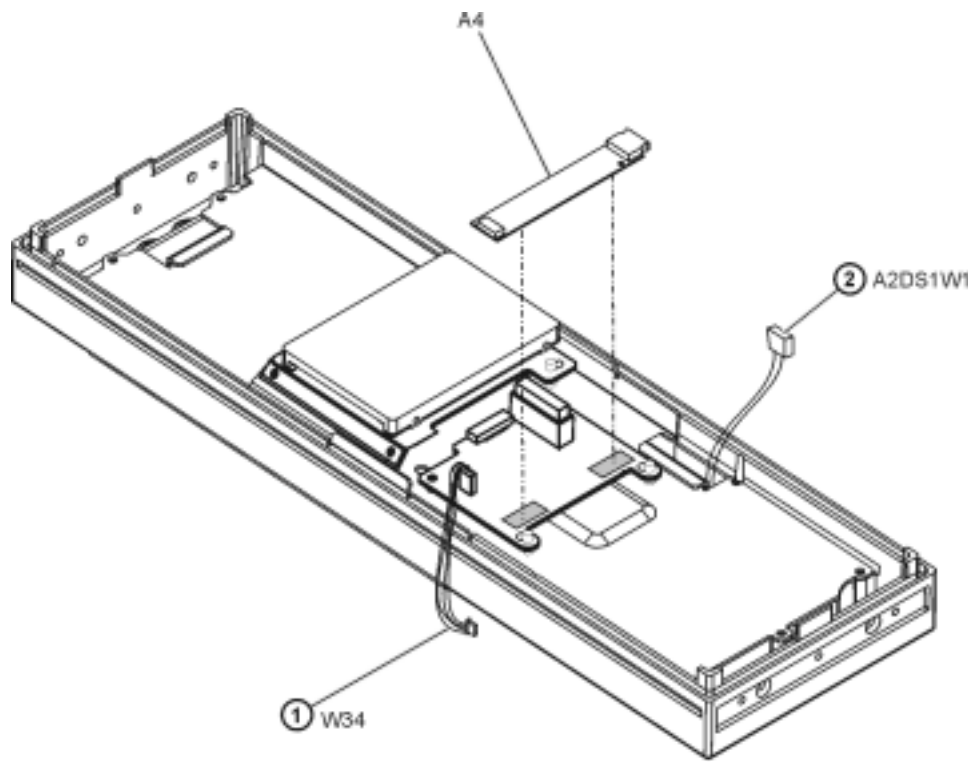
---

5. Remove the A4 Inverter from the A3 Front Panel Auxiliary Board by pulling it away from the printed circuit board. The A4 Inverter is attached with velcro; there are no additional hardware attachments.

### Replacement Procedure

1. Reverse the order of the removal procedure.
2. Torque all T-10 screws to 9 in-lbs.
3. Perform the post-repair adjustments and performance tests that pertain to this removal procedure.

**Figure 2-11**      **A4 Inverter**



## A5 Hard Drive (Option 005)

### Tools Required

- T-10 driver

### Removal Procedure

For instruments with serial prefixes <US4426/MY4426, refer to [Figure 2-12](#) for this procedure.

For instruments with serial prefix ≥US4426/MY4426, refer to [Figure 2-13](#) for this procedure.

1. Disconnect the power cord.
2. Remove the front panels. Refer to [“Front Panel” on page 2-6](#).
3. Using the T-10 driver, remove the screws (1). For instruments with serial prefixes <US4426/MY4426, there are four screws. For instrument with serial prefix ≥US4426/MY4426, there are two screws.
4. Lift and remove the A5 Hard Drive (Option 005) from the display panel's sheet metal.

### Replacement Procedure

1. Reverse the order of the removal procedure.
2. Torque all T-10 screws to 9 in-lbs.
3. Perform the post-repair adjustments and performance tests that pertain to this removal procedure.

Assembly Replacement  
A5 Hard Drive (Option 005)

Figure 2-12 A5 Hard Drive (Option 005), for instruments with serial prefixes <US4426/MY4426

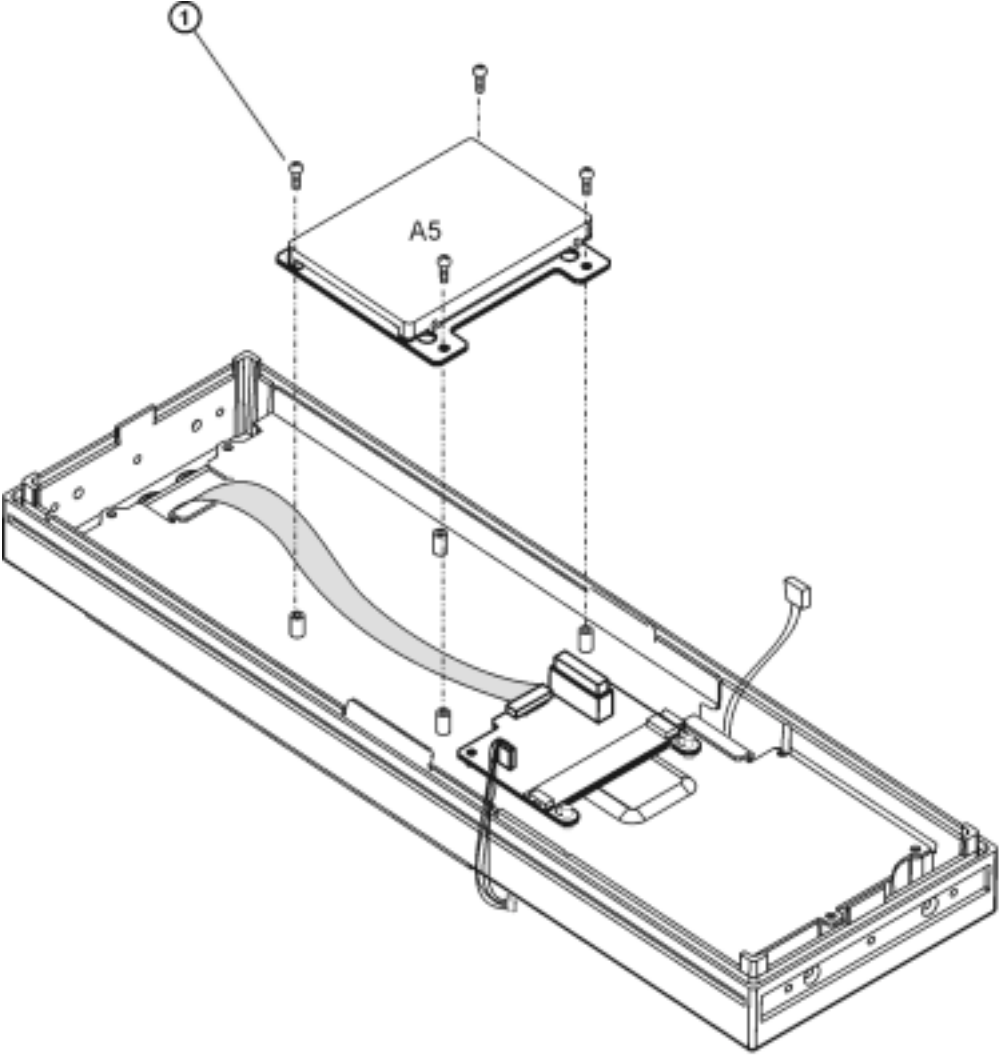
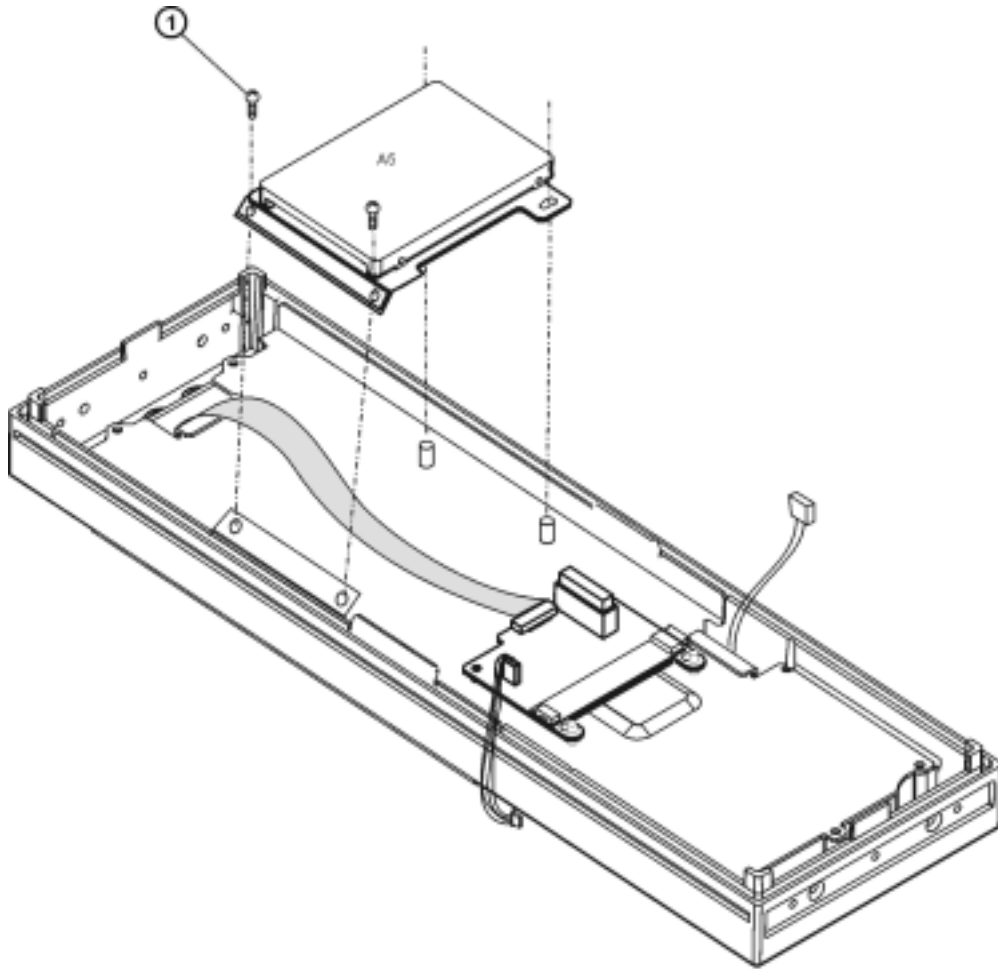




Figure 2-13 A5 Hard Drive (Option 005), for instruments with serial prefix  $\geq$ US4426/MY4426



## **A6 Power Supply**

### **Tools Required**

- T-10 driver

### **Removal Procedure**

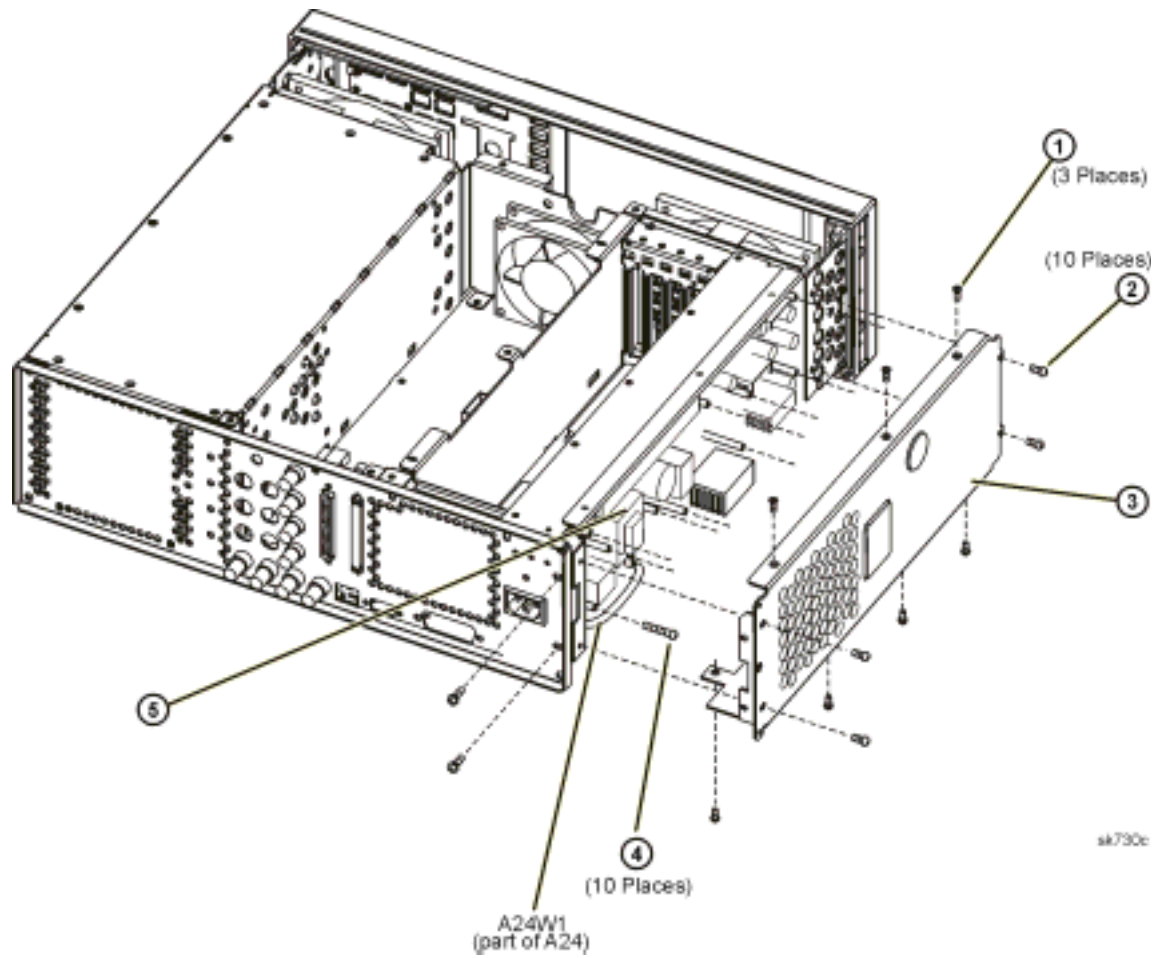
Refer to [Figure 2-14](#) for this procedure.

1. Disconnect the power cord.
2. Remove the outer cover. Refer to [“Outer and Inner Instrument Covers” on page 2-4](#).
3. Using the T-10 driver, remove the three flathead screws (1) and the ten pan-head screws (2) from the A6 Power Supply’s sheet metal cover (3).
4. Disconnect the A24 Line Module cable (A24W1) from the A6 Power Supply (5).
5. Using the T-10 driver, remove the ten screws (4) that hold the A6 Power Supply (5) in place.
6. Remove the A6 Power Supply.

### **Replacement Procedure**

1. Reverse the order of the removal procedure.
2. Torque all T-10 screws to 9 in-lbs.
3. Perform the post-repair adjustments and performance tests that pertain to this removal procedure.

Figure 2-14 A6 Power Supply



## **A7 Baseband Generator (Option 001/601 or Option 002/602)**

### **Tools Required**

- Needle-nose pliers

### **Removal Procedure**

Refer to [Figure 2-15](#) for this procedure and to the connector/cable diagram located on the card cage.

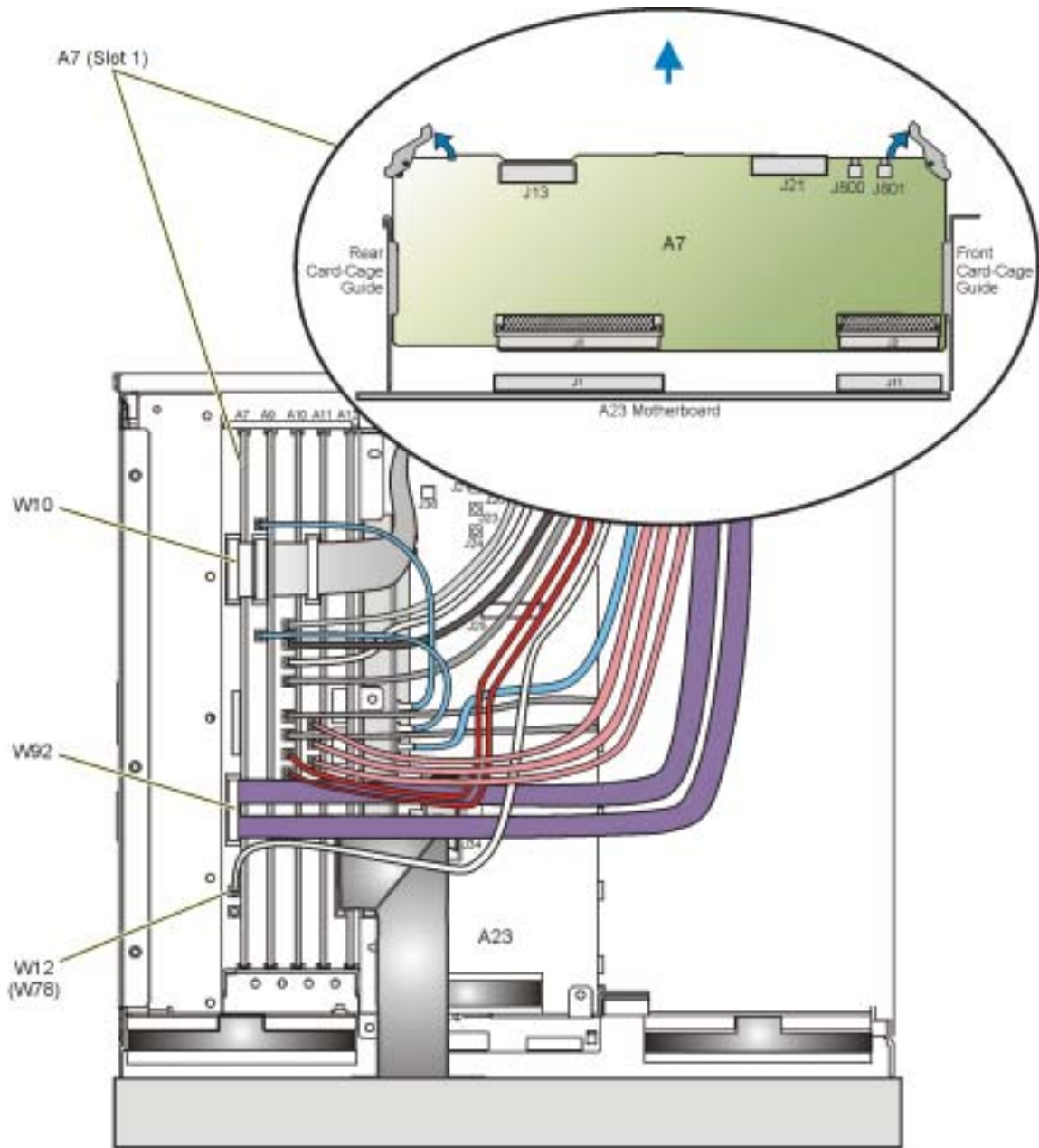
1. Disconnect the power cord.
2. Remove the outer cover. Refer to [“Outer and Inner Instrument Covers”](#) on page 2-4.
3. Disconnect the W10 ribbon cable from A7J13.
4. Disconnect the W92 ribbon cable from A7J21 (Option 601/602 only).
5. Using needle-nose pliers, disconnect the W12 cable from A7J800.  
(This is W78 on an Option 1EM.)
6. Locate Slot 1, lift the retention levers, and disconnect the A7 Baseband Generator from the A23 Motherboard.
7. Lift out the A7 Baseband Generator.

### **Replacement Procedure**

1. Reverse the order of the removal procedure.
2. Perform the post-repair adjustments and performance tests that pertain to this removal procedure.

**A7 Baseband Generator (Option 001/601 or Option 002/602)**

**Figure 2-15 A7 Baseband Generator (Option 001/601 or Option 002/602)**



a7\_brebld\_rmv

## **A9 Digital Demodulator (Option 501, 502, 503, 504 with Option 300)**

### **Tools Required**

- needle-nose pliers

### **Removal Procedure**

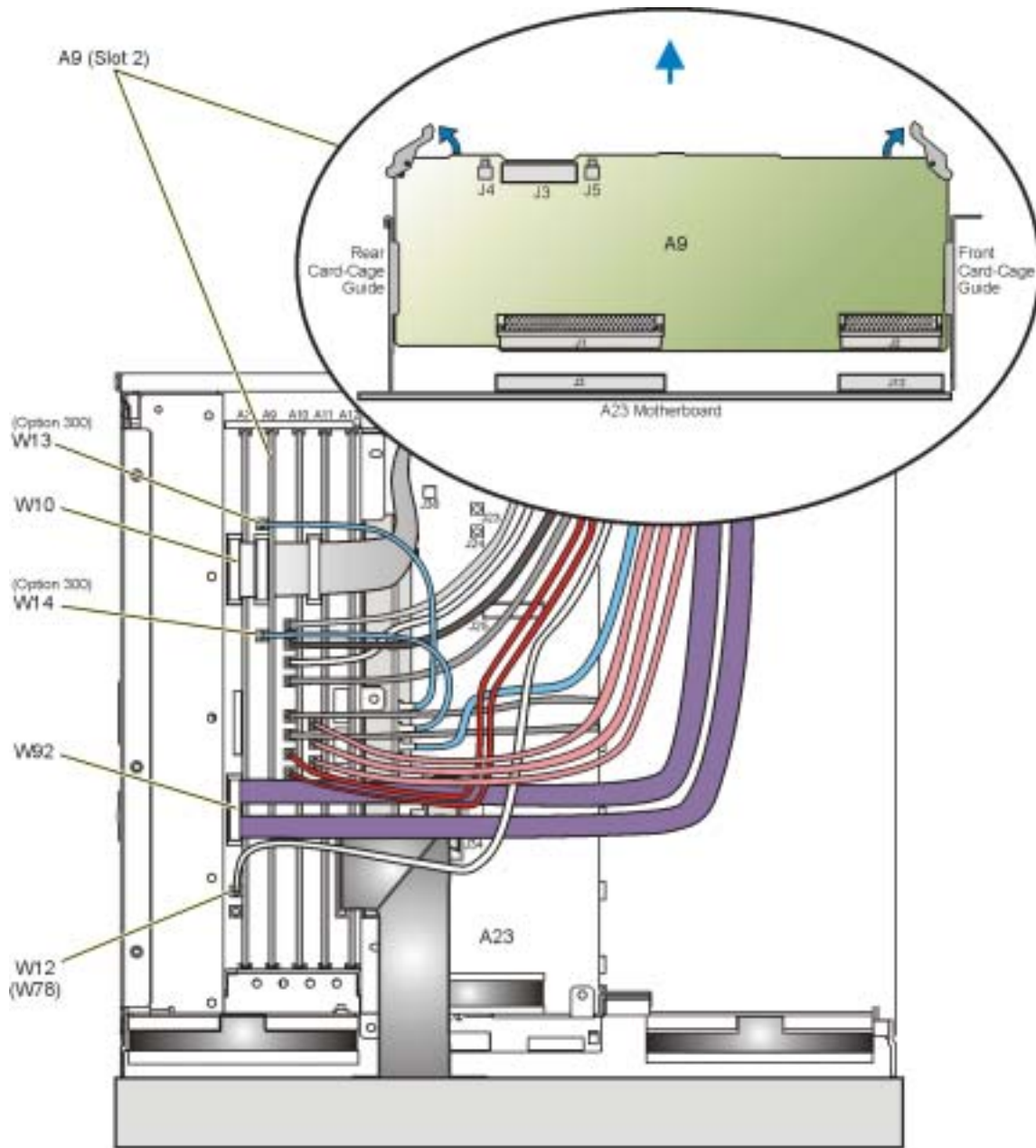
Refer to [Figure 2-16](#) for this procedure and to the connector/cable diagram located on the card cage.

1. Disconnect the power cord.
2. Remove the outer cover. Refer to “[Outer and Inner Instrument Covers](#)” on page 2-4.
3. Using needle-nose pliers, disconnect the following cables:
  - W13 cable from A9J4
  - W14 cable from A9J5
  - W12 cable from A7J800.  
(This is W78 on an Option 1EM.)
4. Disconnect W10 from A7J13 and A9J3.
5. Disconnect W92 from A7J21.
6. Locate Slot 2, lift the retention levers, and disconnect the A9 Digital Demodulator (Option 501, 502, 503, 504 with Option 300) from the A23 Motherboard.
7. Lift out the A9 Digital Demodulator (Option 501, 502, 503, 504 with Option 300).

### **Replacement Procedure**

1. Reverse the order of the removal procedure.
2. Perform the post-repair adjustments and performance tests that pertain to this removal procedure.

Figure 2-16 A9 Digital Demodulator (Option 501, 502, 503, 504 with Option 300)



a9-demod\_rml

## A10 I/Q Multiplexer

---

**NOTE** This procedure applies to the A10 I/Q Multiplexer (Option 501, 502, 503, 504 or Option UNJ) and A10 I/Q Multiplexer (Option 506).

---

### Tools Required

- needle-nose pliers

### Removal Procedure

Refer to [Figure 2-17](#) for this procedure and to the connector/cable diagram located on the card cage.

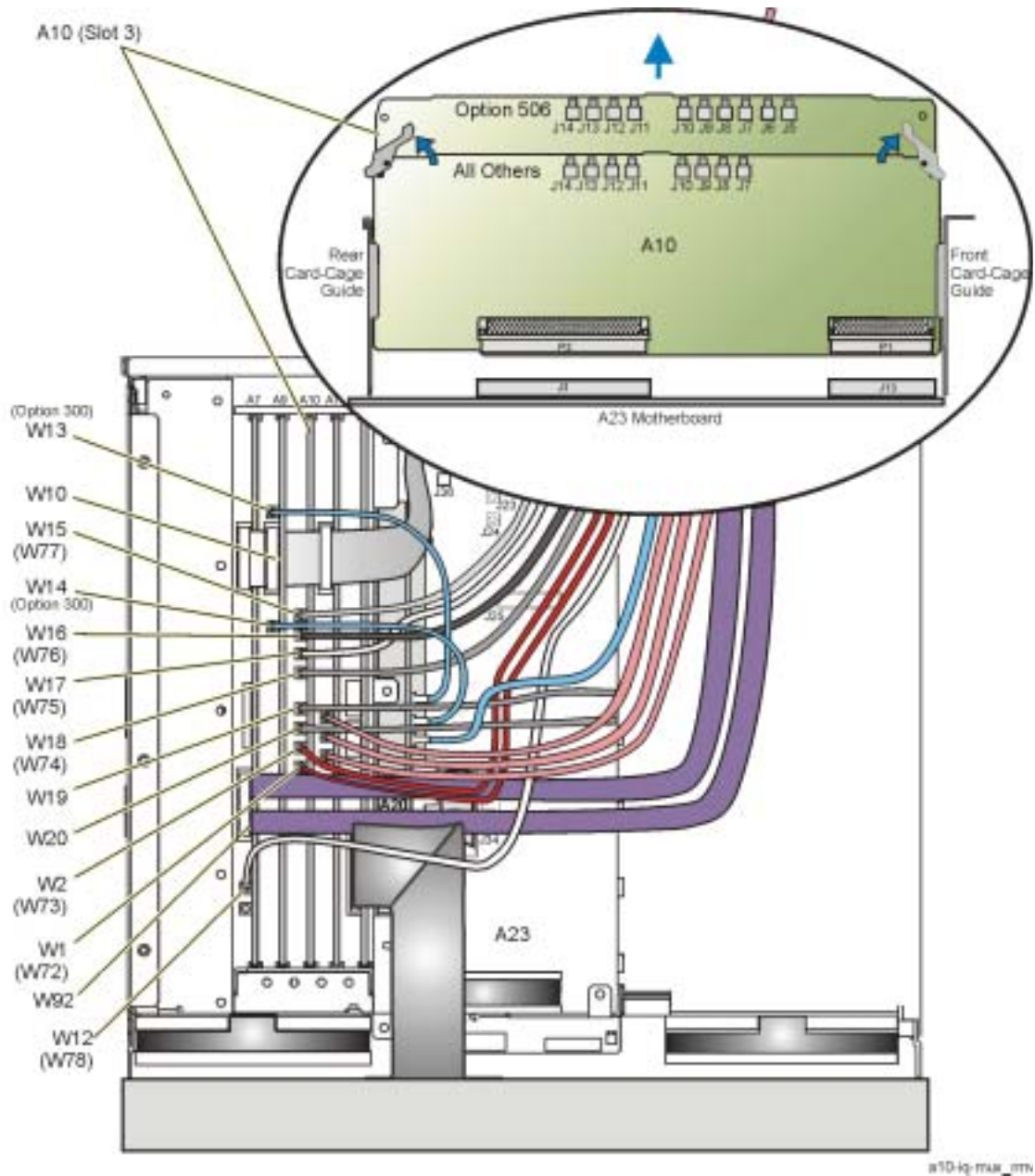
1. Disconnect the power cord.
2. Remove the outer cover. Refer to [“Outer and Inner Instrument Covers” on page 2-4](#).
3. Using needle-nose pliers, disconnect the following cables:
  - W15 cable from A10J14  
(This is W77 on an Option 1EM.)
  - W16 cable from A10J13  
(This is W76 on an Option 1EM.)
  - W17 cable from A10J12  
(This is W75 on an Option 1EM.)
  - W18 cable from A10J11  
(This is W74 on an Option 1EM.)
  - W19 cable from A10J10
  - W20 cable from A10J9
  - W1 cable from A10J7  
(This is W72 on an Option 1EM.)
  - W2 cable from A10J8  
(This is W73 on an Option 1EM.)
  - W12 cable from A7J800.  
(This is W78 on an Option 1EM.)
4. If you have an instrument with Option 506, use needle-nose pliers and disconnect the following cables:
  - W21 cable from A10J6 (not shown)
  - W22 cable from A10J5 (not shown)
5. Disconnect W10 from A7J13 and A9J3.
6. Disconnect W92 from A7J21.
7. Locate Slot 3, lift the retention levers, and disconnect the A10 I/Q Multiplexer from the A23 Motherboard.
8. Lift out the A10 I/Q Multiplexer.



## Replacement Procedure

1. Reverse the order of the removal procedure.
2. Perform the post-repair adjustments and performance tests that pertain to this removal procedure.

Figure 2-17 A10 I/Q Multiplexer



a10-iq-max\_01n

## A11 Internal Bit Error Rate Analyzer (Option UN7)

### Tools Required

- needle-nose pliers

### Removal Procedure

Refer to [Figure 2-18](#) for this procedure and to the connector/cable diagram located on the card cage.

1. Disconnect the power cord.
2. Remove the outer cover. Refer to “[Outer and Inner Instrument Covers](#)” on page 2-4.
3. Using needle-nose pliers, disconnect the following cables:
  - W15 cable from A10J14  
(This is W77 on an Option 1EM.)
  - W16 cable from A10J13  
(This is W76 on an Option 1EM.)
  - W17 cable from A10J12  
(This is W75 on an Option 1EM.)
  - W18 cable from A10J11  
(This is W74 on an Option 1EM.)
  - W19 cable from A10J10
  - W20 cable from A10J9
  - W1 cable from A10J7  
(This is W72 on an Option 1EM.)
  - W2 cable from A10J8  
(This is W73 on an Option 1EM.)
  - W12 cable from A7J800.  
(This is W78 on an Option 1EM.)
4. If you have an instrument with Option 506, use needle-nose pliers and disconnect the following cables:
  - W21 cable from A10J6 (not shown)
  - W22 cable from A10J5 (not shown)
5. Using needle-nose pliers, disconnect the following Option UN7 cables:
  - W23 from A11J4
  - W24 from A11J5
  - W25 from A11J6
6. Disconnect W10 from A7J13, A9J3, and A11J3.
7. Disconnect W92 from A7J21.
8. Locate Slot 4, lift the retention levers, and disconnect the A11 Internal Bit Error Rate Analyzer

(Option UN7) from the A23 Motherboard.

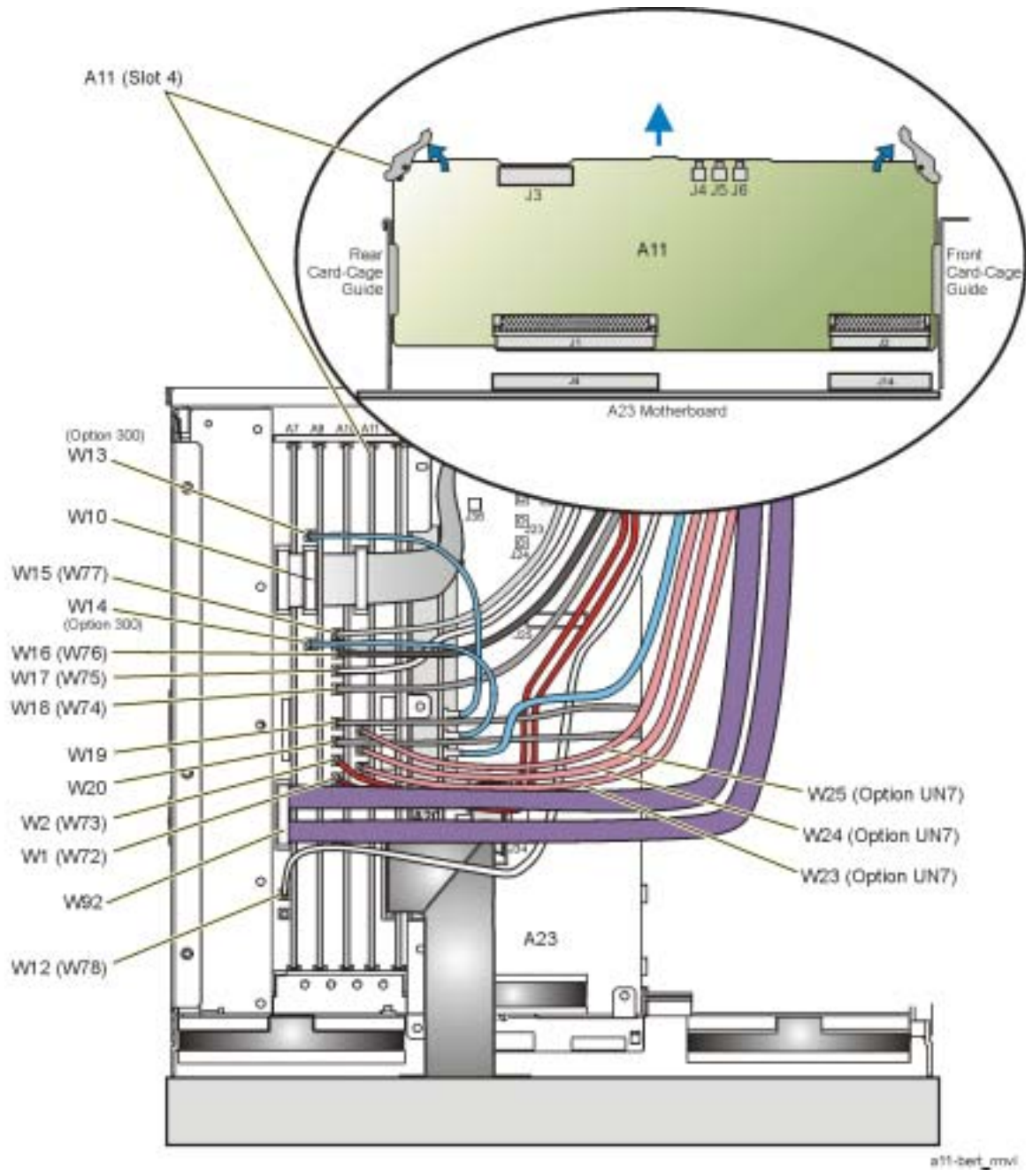
9. Lift out the A11 Internal Bit Error Rate Analyzer (Option UN7).

### **Replacement Procedure**

1. Reverse the order of the removal procedure.
2. Perform the post-repair adjustments and performance tests that pertain to this removal procedure.

Assembly Replacement  
A11 Internal Bit Error Rate Analyzer (Option UN7)

Figure 2-18 A11 Internal Bit Error Rate Analyzer (Option UN7)



## A12 CPU

### Tools Required

- needle-nose pliers

### Removal Procedure

Refer to [Figure 2-19](#) for this procedure and to the connector/cable diagram located on the card cage.

1. Disconnect the power cord.
2. Remove the outer cover. Refer to “[Outer and Inner Instrument Covers](#)” on page 2-4.
3. Using needle-nose pliers, disconnect the following cables:
  - W15 cable from A10J14  
(This is W77 on an Option 1EM.)
  - W16 cable from A10J13  
(This is W76 on an Option 1EM.)
  - W17 cable from A10J12  
(This is W75 on an Option 1EM.)
  - W18 cable from A10J11  
(This is W74 on an Option 1EM.)
  - W19 cable from A10J10
  - W20 cable from A10J9
  - W1 cable from A10J87  
(This is W72 on an Option 1EM.)
  - W2 cable from A10J8  
(This is W73 on an Option 1EM.)
  - W12 cable from A7J800.  
(This is W78 on an Option 1EM.)
4. If you have an instrument with Option 506, use needle-nose pliers and disconnect the following cables:
  - W21 cable from A10J6 (not shown)
  - W22 cable from A10J5 (not shown)
5. Using needle-nose pliers, disconnect the following Option UN7 cables:
  - W23 from A11J4
  - W24 from A11J5
  - W25 from A11J6
6. Disconnect W10 from A7J13, A9J3, and A11J3.
7. Disconnect W92 from A7J21.
8. If you have an instrument with Option 005, disconnect W26 cable from the A12P3.

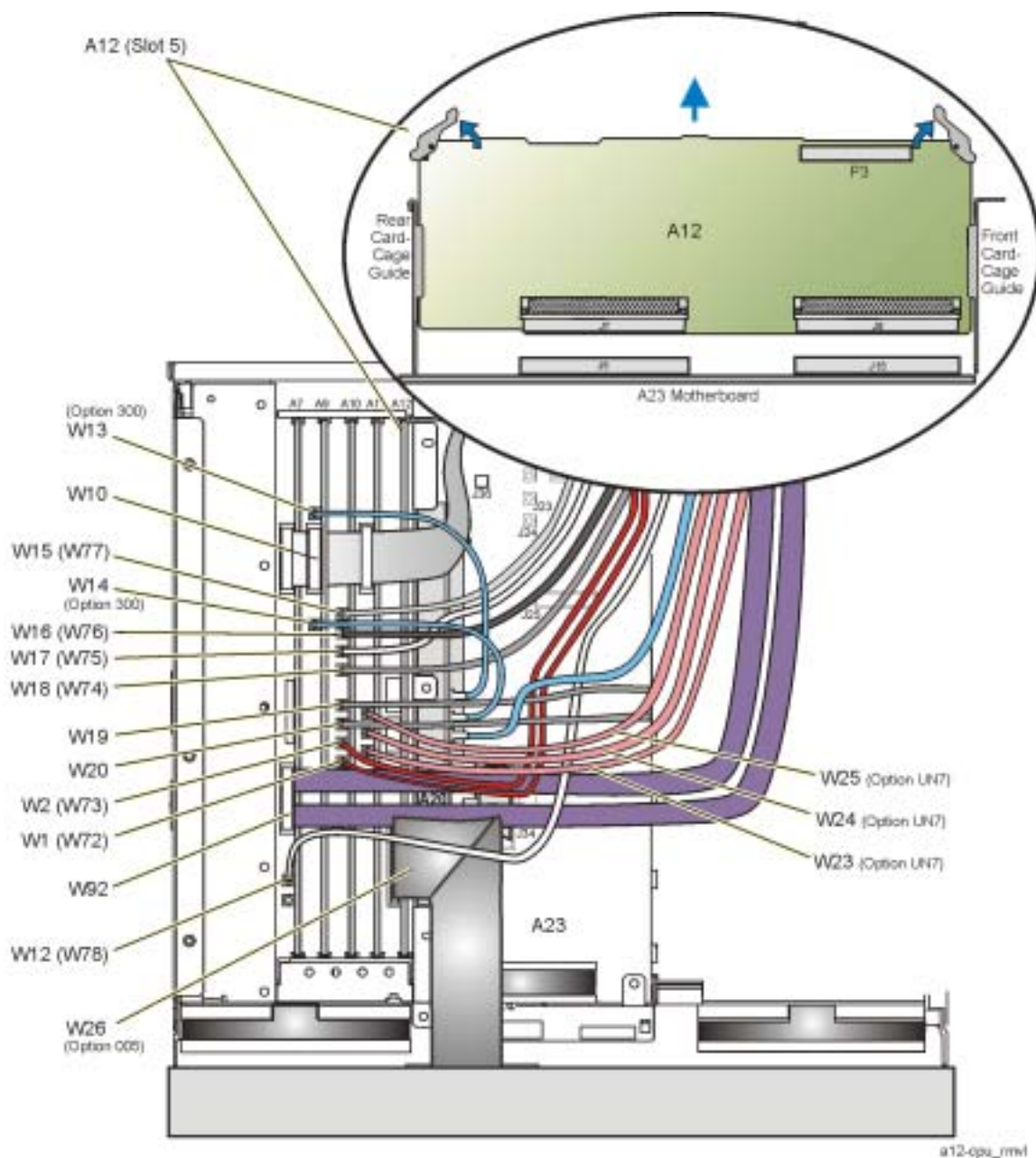
Assembly Replacement  
**A12 CPU**

9. Locate Slot 5, lift the retention levers, and disconnect the A12 CPU from the A23 Motherboard.
10. Lift out the A12 CPU.

**Replacement Procedure**

1. Reverse the order of the removal procedure.
2. Perform the post-repair adjustments and performance tests that pertain to this removal procedure.

**Figure 2-19**      **A12 CPU**



---

## A13 Output

---

**NOTE** This procedure applies to both the A13 Output (Option 501, 502, 503, 504 or Option UNJ) and the A13 Output (Option UNB).

---

### Tools Required

- none

### Removal Procedure

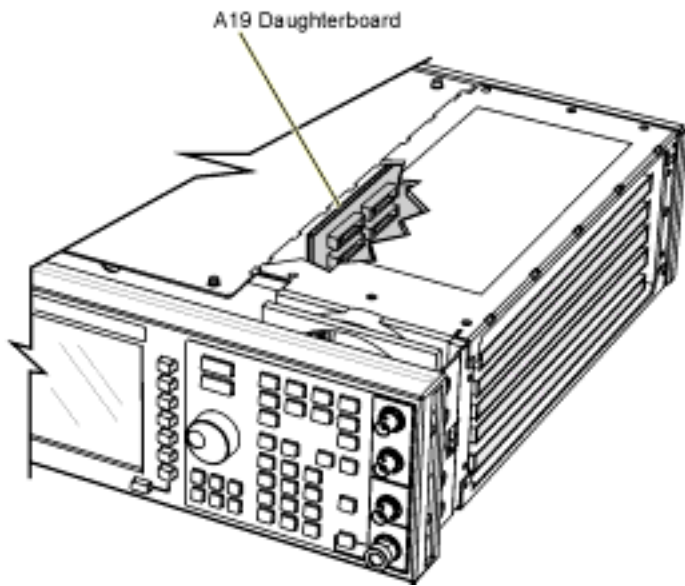
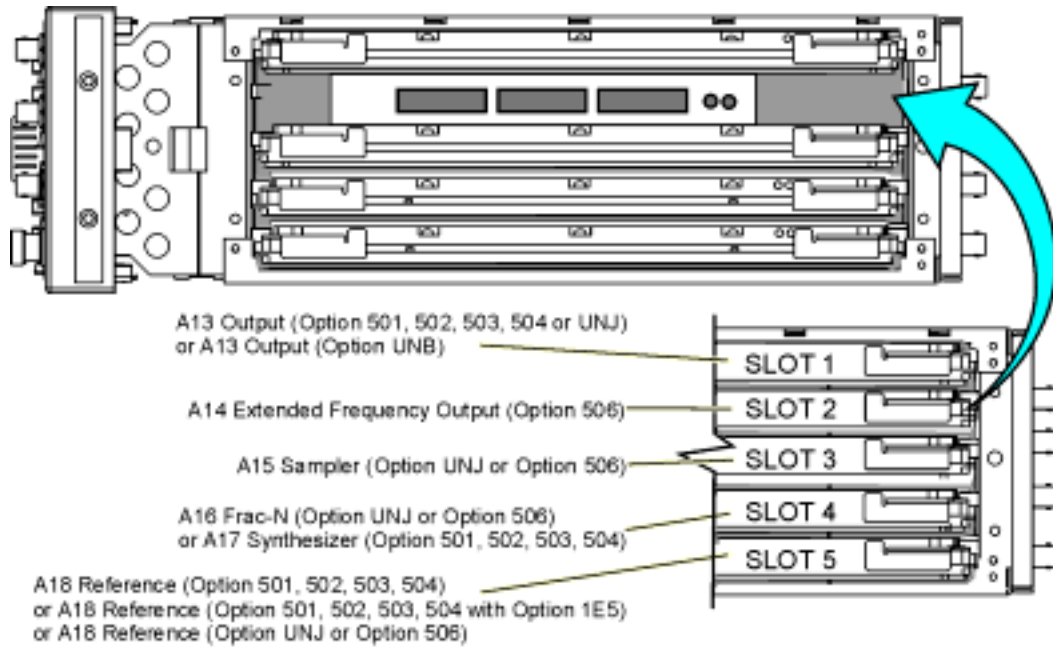
Refer to [Figure 2-20](#) for this procedure and to the connector/cable diagram located on the card cage.

1. Disconnect the power cord.
2. Remove the outer and inner-side covers. Refer to “[Outer and Inner Instrument Covers](#)” on page 2-4.
3. Locate Side-SLOT 1, lift the retention levers, and disconnect the A13 Output from the A19 Daughterboard.
4. Slide out the A13 Output.

### Replacement Procedure

1. Reverse the order of the removal procedure.
2. Perform the post-repair adjustments and performance tests that pertain to this removal procedure.

Figure 2-20 A13 Output



sk701c



## **A14 Extended Frequency Output (Option 506)**

### **Tools Required**

- none

### **Removal Procedure**

Refer to [Figure 2-21](#) for this procedure and to the connector/cable diagram located on the card cage.

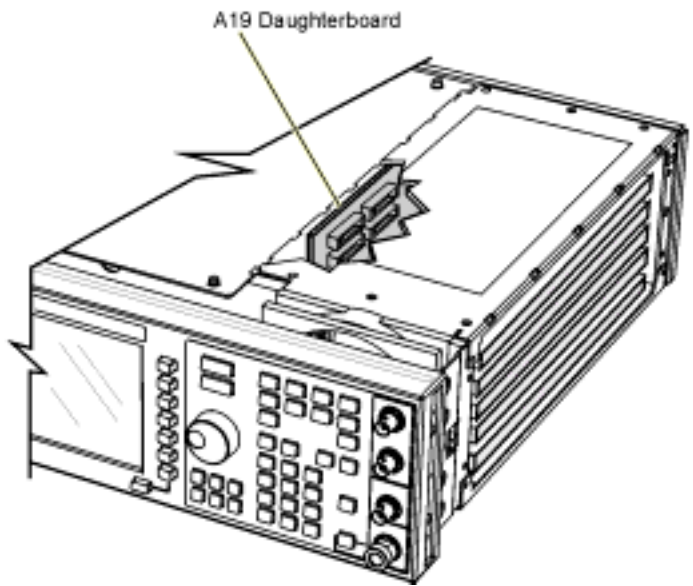
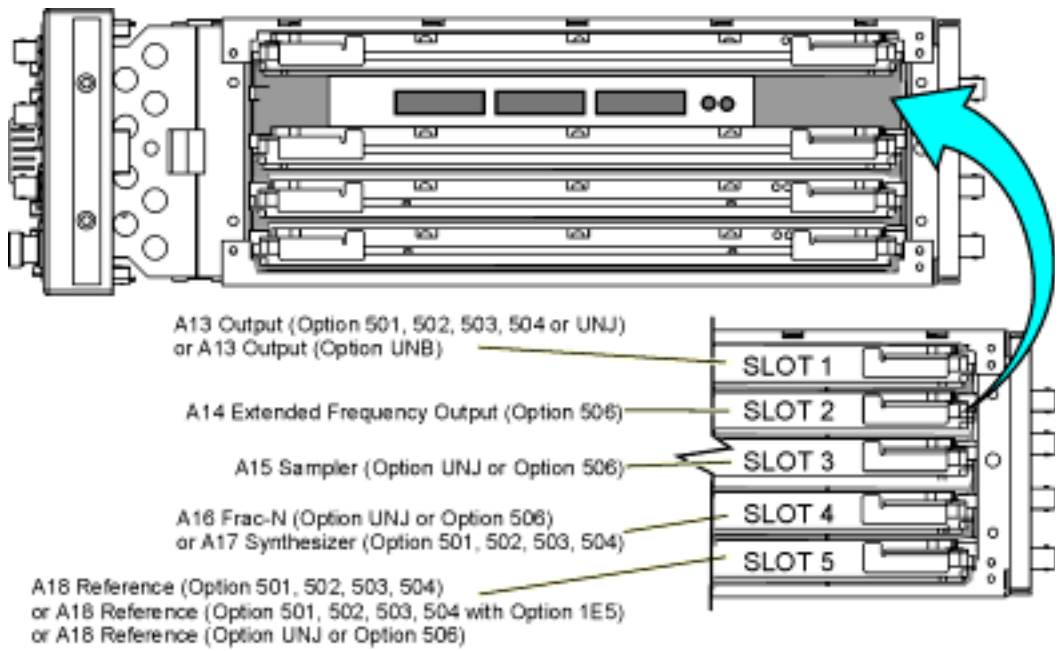
1. Disconnect the power cord.
2. Remove the outer and inner-side covers. Refer to “[Outer and Inner Instrument Covers](#)” on page 2-4.
3. Locate Side-SLOT 2, lift the retention levers, and disconnect the A14 Extended Frequency Output (Option 506) from the A19 Daughterboard.
4. Slide out the A14 Extended Frequency Output (Option 506).

### **Replacement Procedure**

1. Reverse the order of the removal procedure.
2. Perform the post-repair adjustments and performance tests that pertain to this removal procedure.

Assembly Replacement  
A14 Extended Frequency Output (Option 506)

Figure 2-21 A14 Extended Frequency Output (Option 506)



sk701c

## **A15 Sampler (Option UNJ or Option 506)**

### **Tools Required**

- none

### **Removal Procedure**

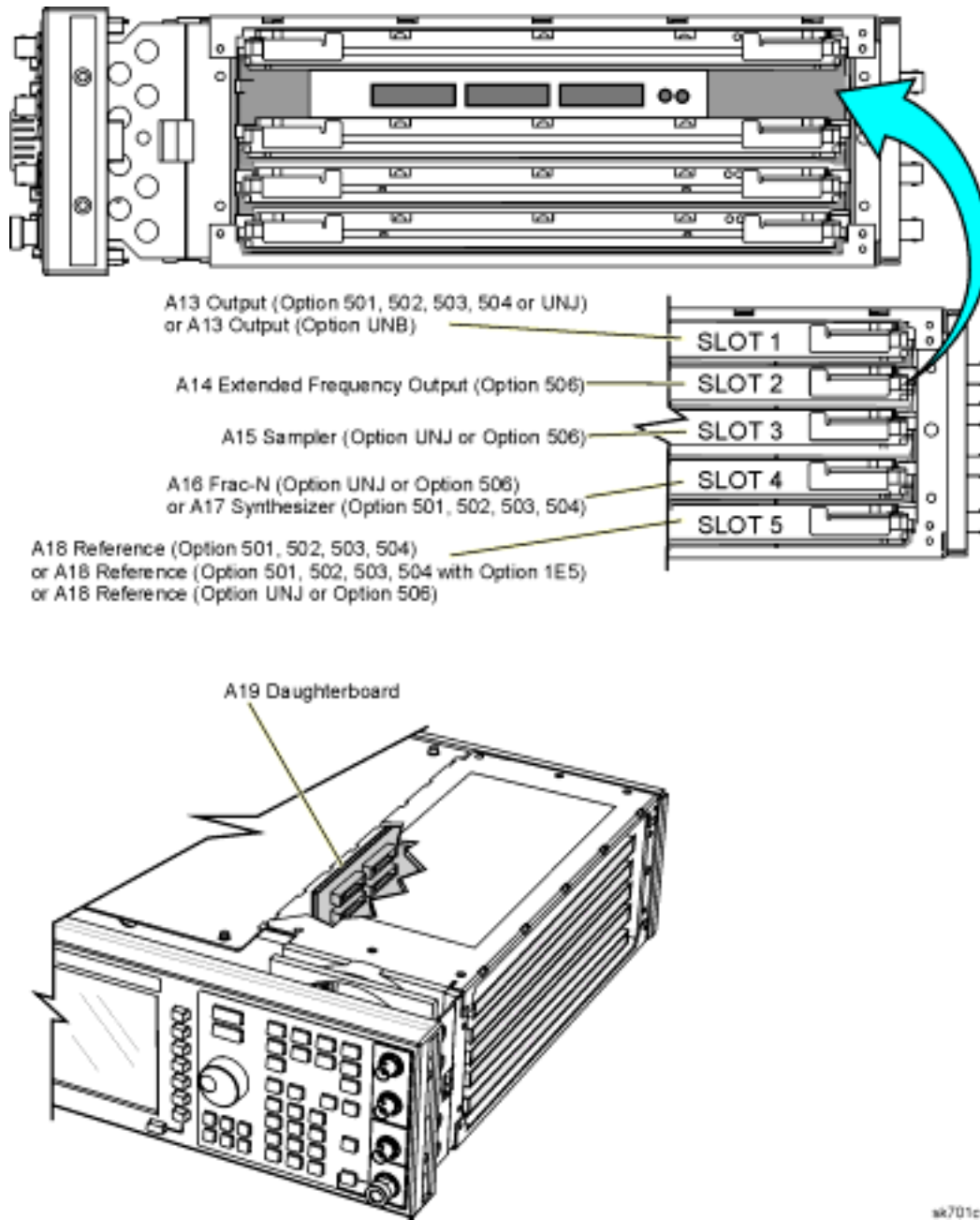
Refer to [Figure 2-22](#) for this procedure and to the connector/cable diagram located on the card cage.

1. Disconnect the power cord.
2. Remove the outer and inner-side covers. Refer to [“Outer and Inner Instrument Covers”](#) on page 2-4.
3. Locate Side-SLOT 3, lift the retention levers, and disconnect the A15 Sampler (Option UNJ or Option 506) from the A19 Daughterboard.
4. Slide out the A15 Sampler (Option UNJ or Option 506).

### **Replacement Procedure**

1. Reverse the order of the removal procedure.
2. Perform the post-repair adjustments and performance tests that pertain to this removal procedure.

Figure 2-22 A15 Sampler (Option UNJ or Option 506)



## **A16 Frac-N (Option UNJ or Option 506)**

### **Tools Required**

- none

### **Removal Procedure**

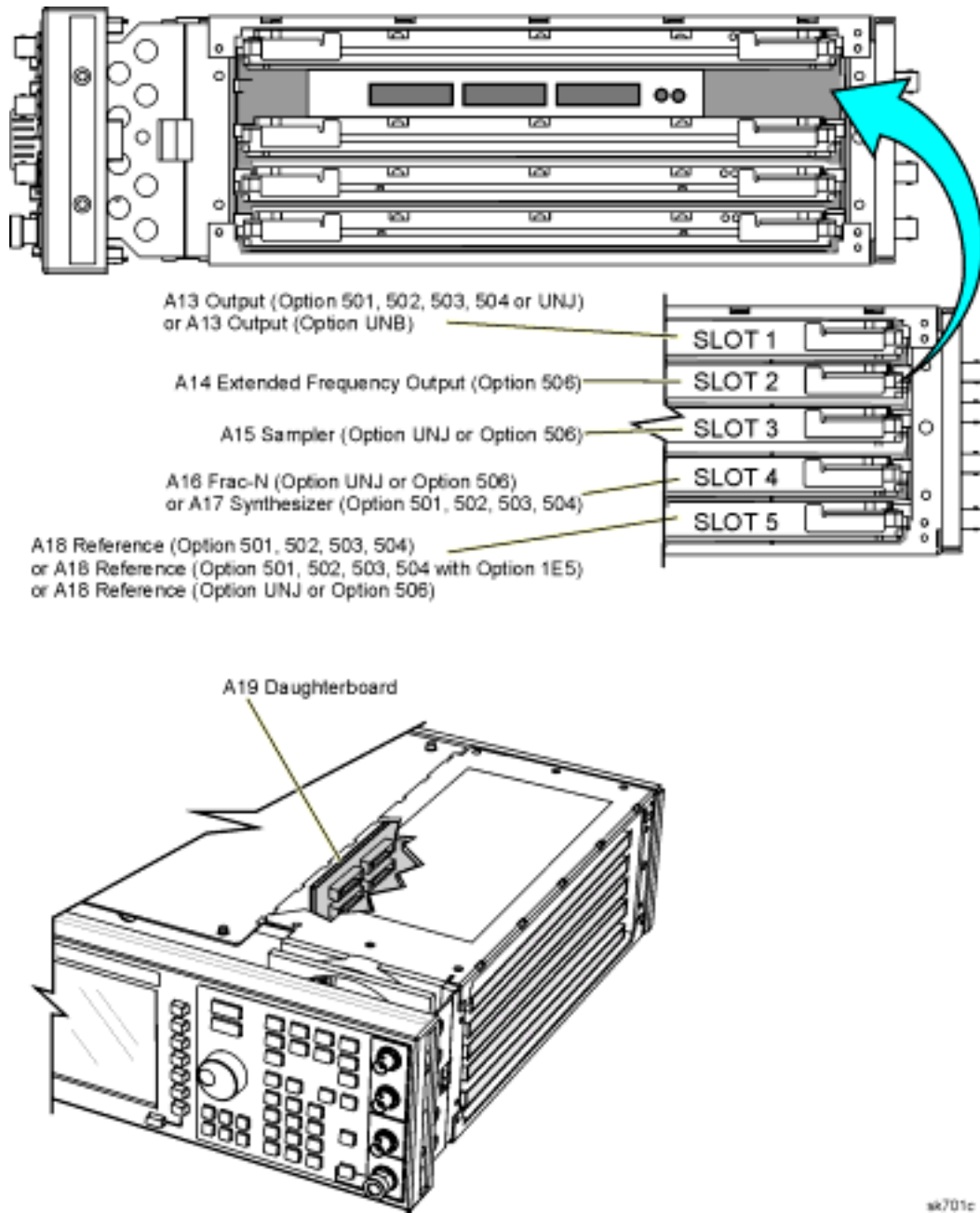
Refer to [Figure 2-23](#) for this procedure and to the connector/cable diagram located on the card cage.

1. Disconnect the power cord.
2. Remove the outer and inner-side covers. Refer to “[Outer and Inner Instrument Covers](#)” on page 2-4.
3. Locate Side-SLOT 4, lift the retention levers, and disconnect the A16 Frac-N (Option UNJ or Option 506) from the A19 Daughterboard.
4. Slide out the A16 Frac-N (Option UNJ or Option 506).

### **Replacement Procedure**

1. Reverse the order of the removal procedure.
2. Perform the post-repair adjustments and performance tests that pertain to this removal procedure.

Figure 2-23 A16 Frac-N (Option UNJ or Option 506)



## **A17 Synthesizer (Option 501, 502, 503, 504)**

### **Tools Required**

- none

### **Removal Procedure**

Refer to [Figure 2-24](#) for this procedure and to the connector/cable diagram located on the card cage.

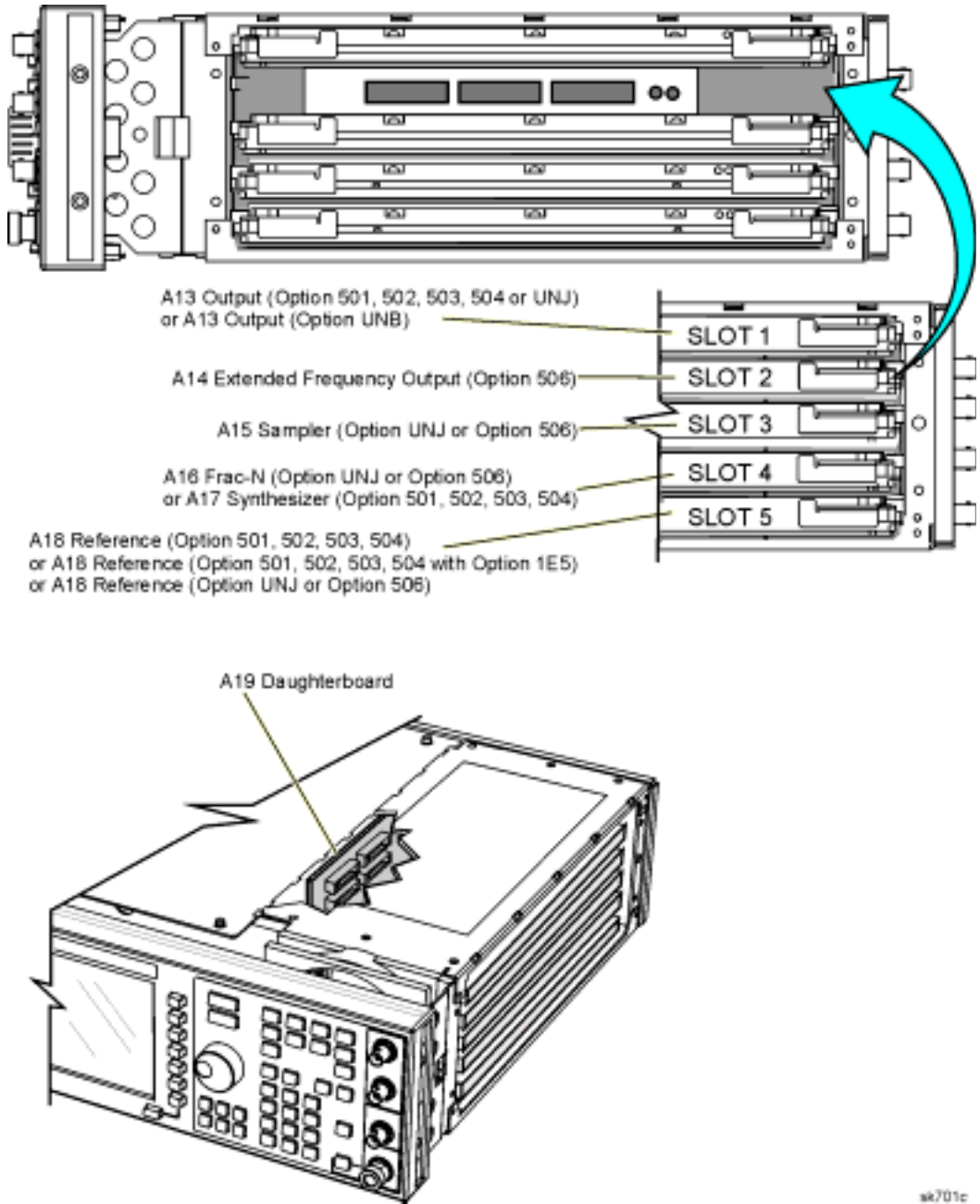
1. Disconnect the power cord.
2. Remove the outer and inner-side covers. Refer to “[Outer and Inner Instrument Covers](#)” on page 2-4.
3. Locate Side-SLOT 4, lift the retention levers, and disconnect the A17 Synthesizer (Option 501, 502, 503, 504) from the A19 Daughterboard.
4. Slide out the A17 Synthesizer (Option 501, 502, 503, 504).

### **Replacement Procedure**

1. Reverse the order of the removal procedure.
2. Perform the post-repair adjustments and performance tests that pertain to this removal procedure.

Assembly Replacement  
**A17 Synthesizer (Option 501, 502, 503, 504)**

Figure 2-24      **A17 Synthesizer (Option 501, 502, 503, 504)**





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## A18 Reference

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**NOTE** This procedure applies to the A18 Reference (Option 501, 502, 503, 504), A18 Reference (Option 501, 502, 503, 504 with Option 1E5), and A18 Reference (Option UNJ or Option 506).

---

### Tools Required

- none

### Removal Procedure

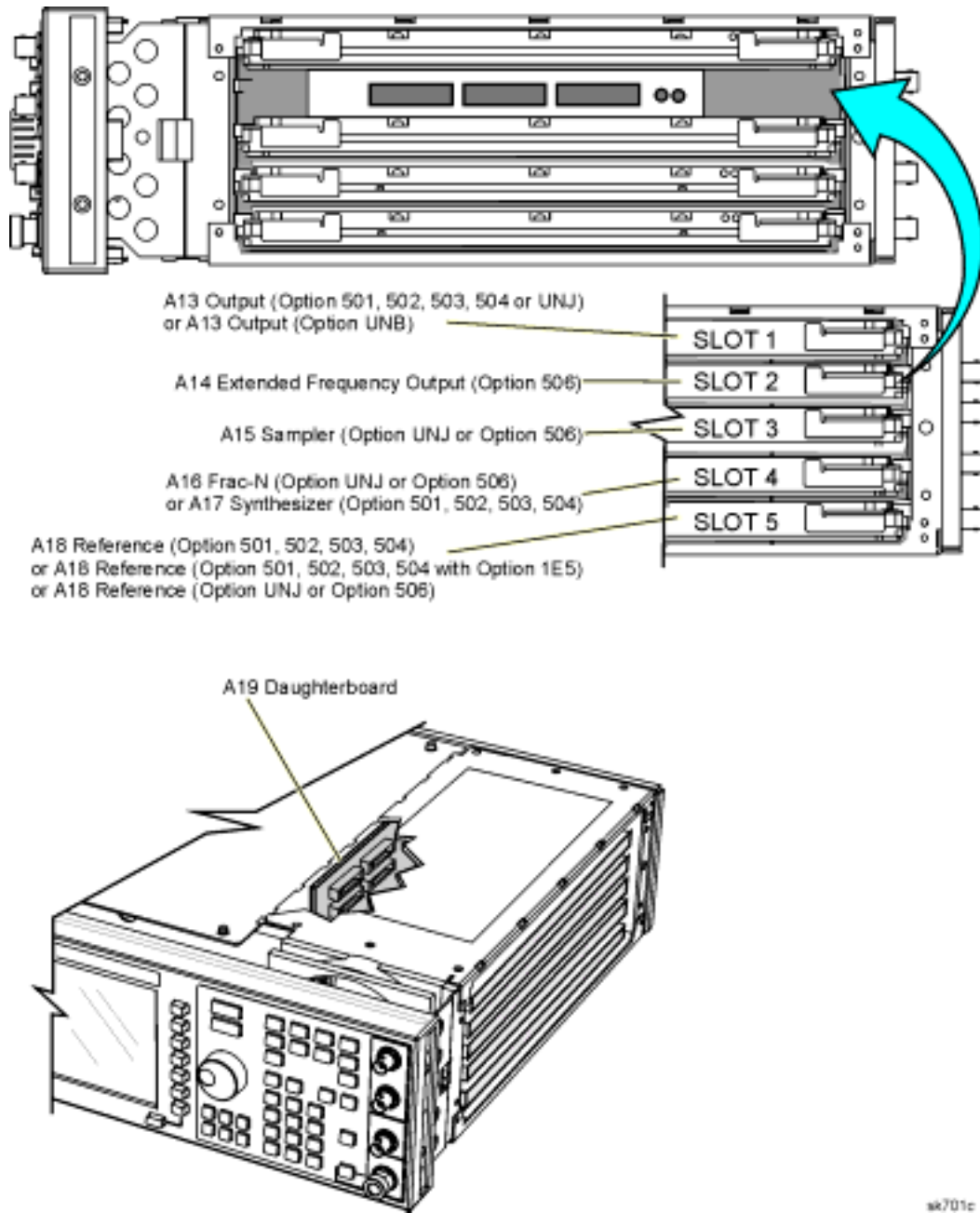
Refer to [Figure 2-25](#) for this procedure and to the connector/cable diagram located on the card cage.

1. Disconnect the power cord.
2. Remove the outer and inner-side covers. Refer to [“Outer and Inner Instrument Covers”](#) on page 2-4.
3. Locate Side-SLOT 5, lift the retention levers, and disconnect the A18 Reference from the A19 Daughterboard.
4. Slide out the A18 Reference.

### Replacement Procedure

1. Reverse the order of the removal procedure.
2. Perform the post-repair adjustments and performance tests that pertain to this removal procedure.

Figure 2-25 A18 Reference



## A19 Daughterboard

### Tools Required

- T-10 driver

### Removal Procedure

Refer to [Figure 2-26](#) for this procedure and to the connector/cable diagram located on the card cage.

1. Disconnect the power cord.
2. Remove the outer and inner-side covers. Refer to “[Outer and Inner Instrument Covers](#)” on page 2-4.

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**NOTE** It is recommended to label the assembly boards before you remove them from the card cage for easy installation.

---

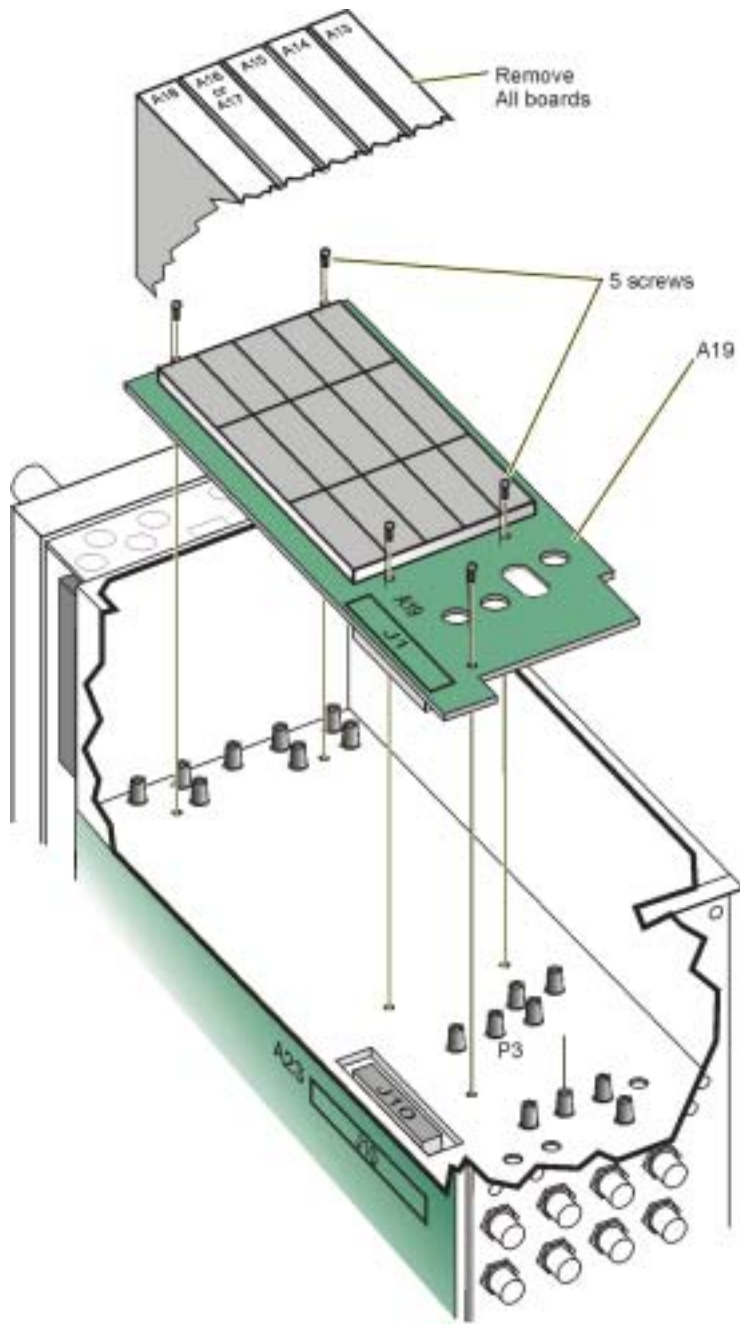
3. Remove all of the board assemblies from the side-card cage (located on the left side of the signal generator) by lifting the retention levers and disconnecting the assembly boards from the A19 Daughterboard.
4. Using the T-10 driver, remove the five screws that attach the A19 Daughterboard to the signal generator’s chassis.
5. Remove the A19 Daughterboard from the card cage.

### Replacement Procedure

1. Reverse the order of the removal procedure.
2. Torque all T-10 screws to 9 in-lbs.
3. Perform the post-repair adjustments and performance tests that pertain to this removal procedure.

Assembly Replacement  
**A19 Daughterboard**

Figure 2-26      A19 Daughterboard



daught\_rml

## **A19 Daughterboard Cable Removal**

### **Tools Required**

- external retaining ring pliers

### **Removal Procedure**

Refer to [Figure 2-27](#) for this procedure.

1. Disconnect the power cord.
2. Remove the outer and inner-side covers. Refer to [“Outer and Inner Instrument Covers”](#) on page 2-4.

---

**NOTE** Before removing assemblies from the card cage, it is recommended that the assembly be labelled. This will help during reassembly.

---

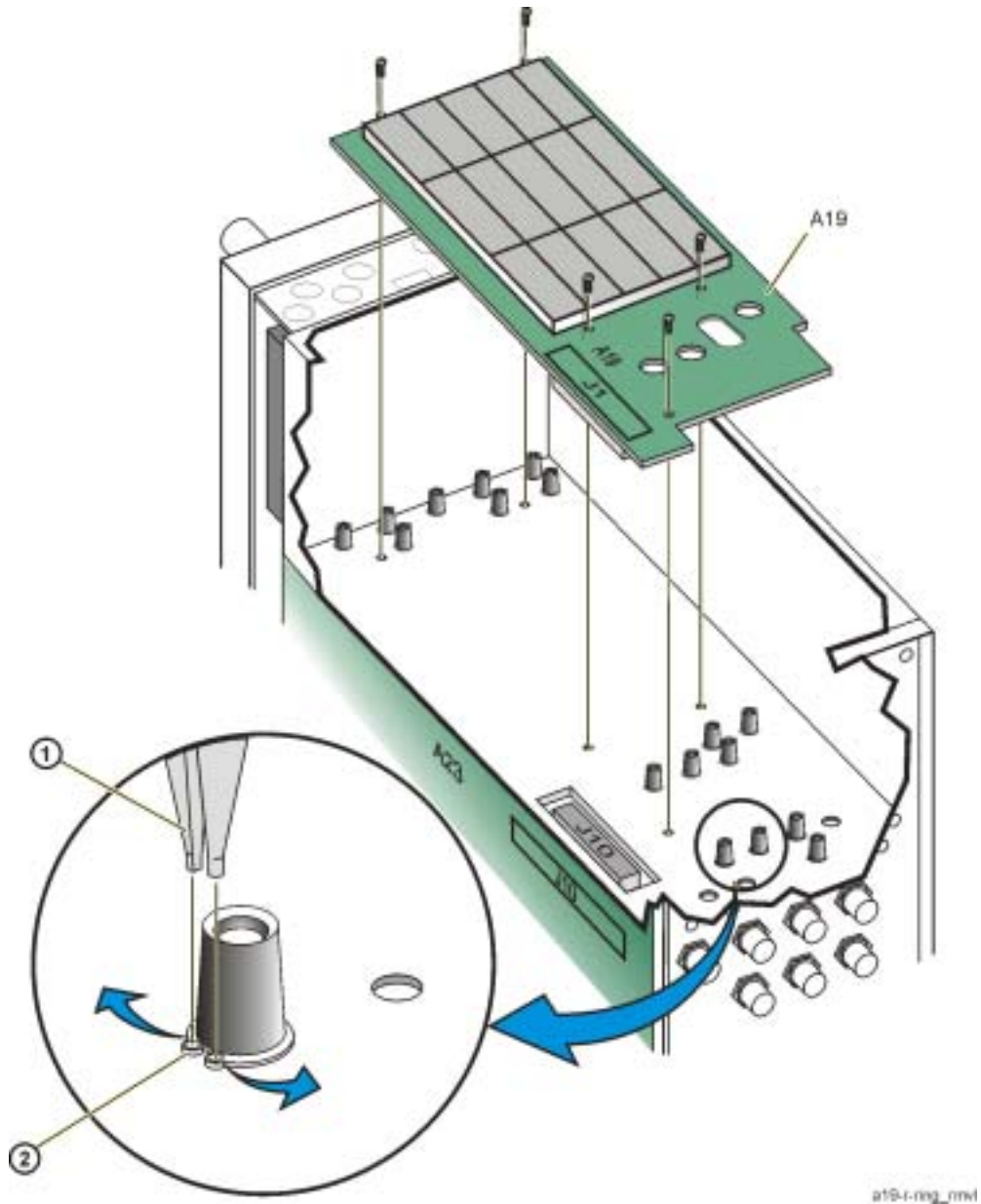
3. Remove all of the assembly boards from the card cage (located on the left side of the signal generator) by lifting the retention levers and disconnecting the assembly boards from the A19 Daughterboard.
4. Slide-out all of the assembly boards in the card cage.
5. Remove the A19 Daughterboard. Refer to [“A19 Daughterboard”](#) on page 2-49.
6. Using the external retaining ring pliers (1), remove the metal retaining rings (2).
7. Remove the cable from the card cage.

### **Replacement Procedure**

1. Reverse the order of the removal procedure.
2. Perform the post-repair adjustments and performance tests that pertain to this removal procedure.

Assembly Replacement  
**A19 Daughterboard Cable Removal**

Figure 2-27      A19 Daughterboard Cable Removal



a19-r-req\_rml

## A20 Downconverter (Option 501, 502, 503, 504 with Option 300)

### Tools Required

- T-10 driver
- needle-nose pliers

### Removal Procedure

Refer to [Figure 2-28](#) for this procedure.

1. Disconnect the power cord.
2. Remove the outer and inner covers. Refer to [“Outer and Inner Instrument Covers” on page 2-4](#).
3. Using needle-nose pliers, disconnect the following cables from the A20 Downconverter (Option 501, 502, 503, 504 with Option 300):
  - W64
  - W13
  - W14
4. Disconnect the W63 ribbon cable from A23J19.
5. Using the T-10 driver, remove the two screws (1) from the hook bracket that are holding the A20 Downconverter (Option 501, 502, 503, 504 with Option 300) to the chassis.
6. Tilt the hook bracket away from the chassis and slide out the tabs from the slots.

The hook bracket has two hinged tabs at the bottom that fit into slots in the signal generator’s chassis.

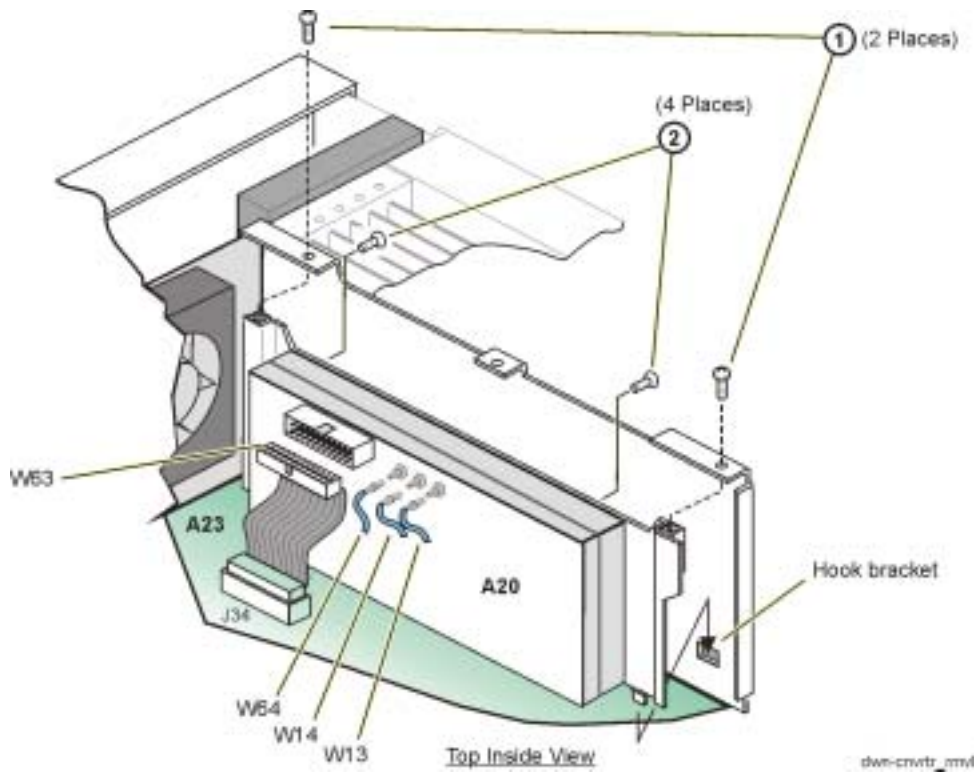
7. Using the T-10 driver, remove the four screws (2) that hold the hook bracket to the A20 Downconverter (Option 501, 502, 503, 504 with Option 300).
8. Remove the A20 Downconverter (Option 501, 502, 503, 504 with Option 300).

### Replacement Procedure

1. Reverse the order of the removal procedure.
2. Torque all T-10 screws to 9 in-lbs.
3. Perform the post-repair adjustments and performance tests that pertain to this removal procedure.

Assembly Replacement  
A20 Downconverter (Option 501, 502, 503, 504 with Option 300)

Figure 2-28 A20 Downconverter (Option 501, 502, 503, 504 with Option 300)





## **A21 YTO Driver (Option UNJ or Option 506) and A22 Coupler (Option 506)**

### **Tools Required**

- T-10 driver
- 5/16" open ended wrench
- needle-nose pliers

### **Removal Procedure**

Refer to [Figure 2-29](#) for this procedure.

1. Disconnect the power cord.
2. Remove the outer and inner covers. Refer to [“Outer and Inner Instrument Covers” on page 2-4](#).
3. Disconnect the W27 ribbon cable from the A21 YTO Driver (Option UNJ or Option 506).
4. Using needle-nose pliers, disconnect the following cables:
  - W28 from A21J3
  - W29 from A21J4
5. Using the 5/16" open ended wrench, disconnect the following RF cables from the A21 YTO Driver (Option UNJ or Option 506):
  - W37
  - W38
  - On signal generators with Option 506:
    - disconnect W56
    - if removing the A22 Coupler (Option 506):
      - disconnect W55
      - disconnect W54
      - remove the two screws (3) holding the A22 Coupler (Option 506) to the A21 YTO Driver (Option UNJ or Option 506)
6. Using the T-10 driver, remove the two screws (1) from the hook bracket that are holding the A21 YTO Driver (Option UNJ or Option 506) to the chassis.
7. Tilt the hook bracket away from the chassis and slide out the tabs from the slots.

The hook bracket has two hinged tabs at the bottom that fit into slots in the signal generator's chassis.

8. Using the T-10 driver, remove the four screws (2) that hold the hook bracket to the A21 YTO Driver (Option UNJ or Option 506).

## Replacement Procedure

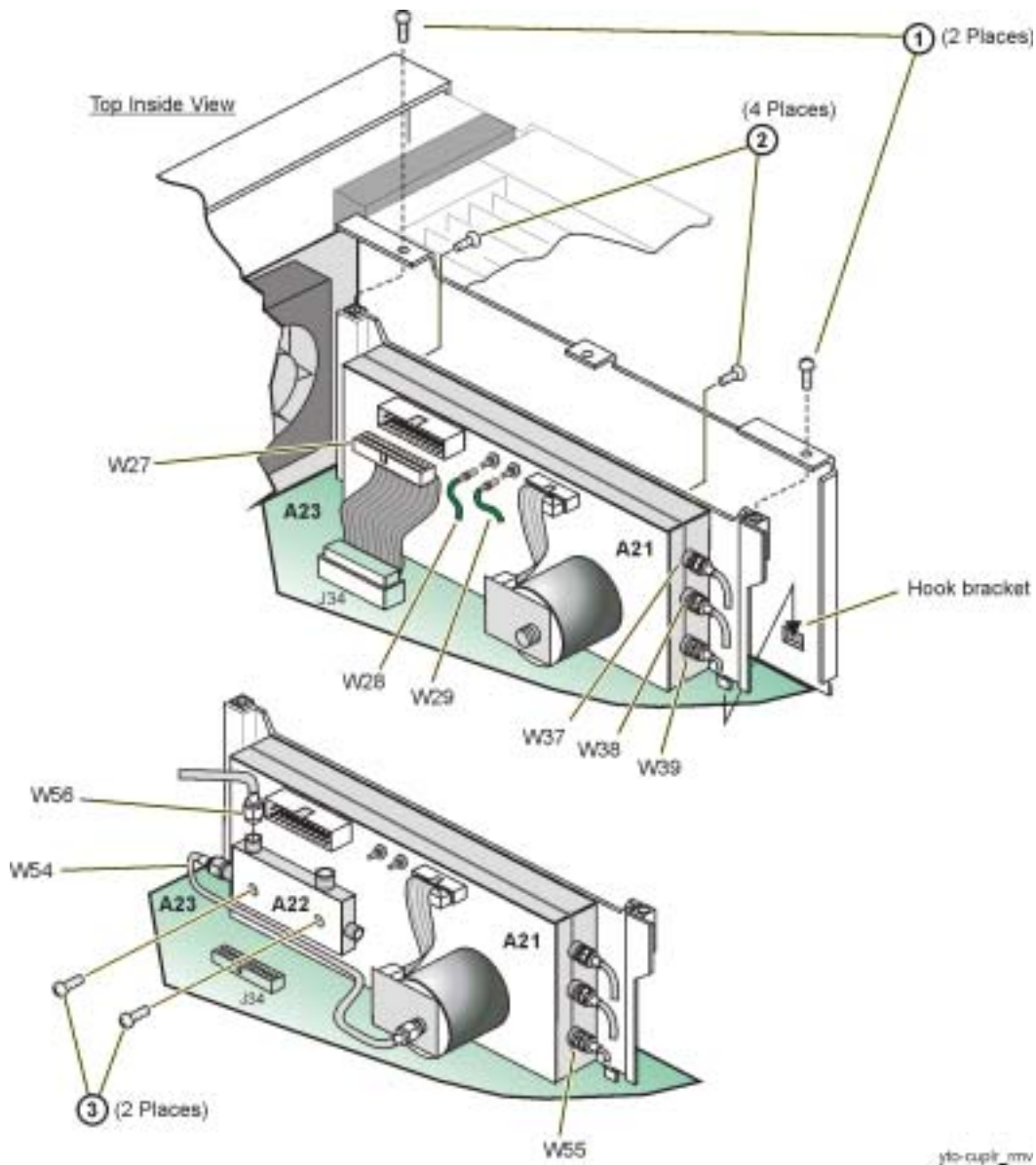
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**NOTE** Make sure that the tabs on the A21 YTO Driver (Option UNJ or Option 506) hook bracket line up with the slots in the chassis.

---

1. Reverse the order of the removal procedure.
2. Torque all T-10 screws to 9 in-lbs.
3. Torque all RF connectors to 9 in-lbs.
4. Perform the post-repair adjustments and performance tests that pertain to this removal procedure.

**Figure 2-29** A21 YTO Driver (Option UNJ or Option 506)



## A23 Motherboard

### Tools Required

- T-10 driver
- 5/8" open-ended wrench
- 3/16" nut driver
- 9/32" nut driver
- needle-nose pliers

### Removal Procedure

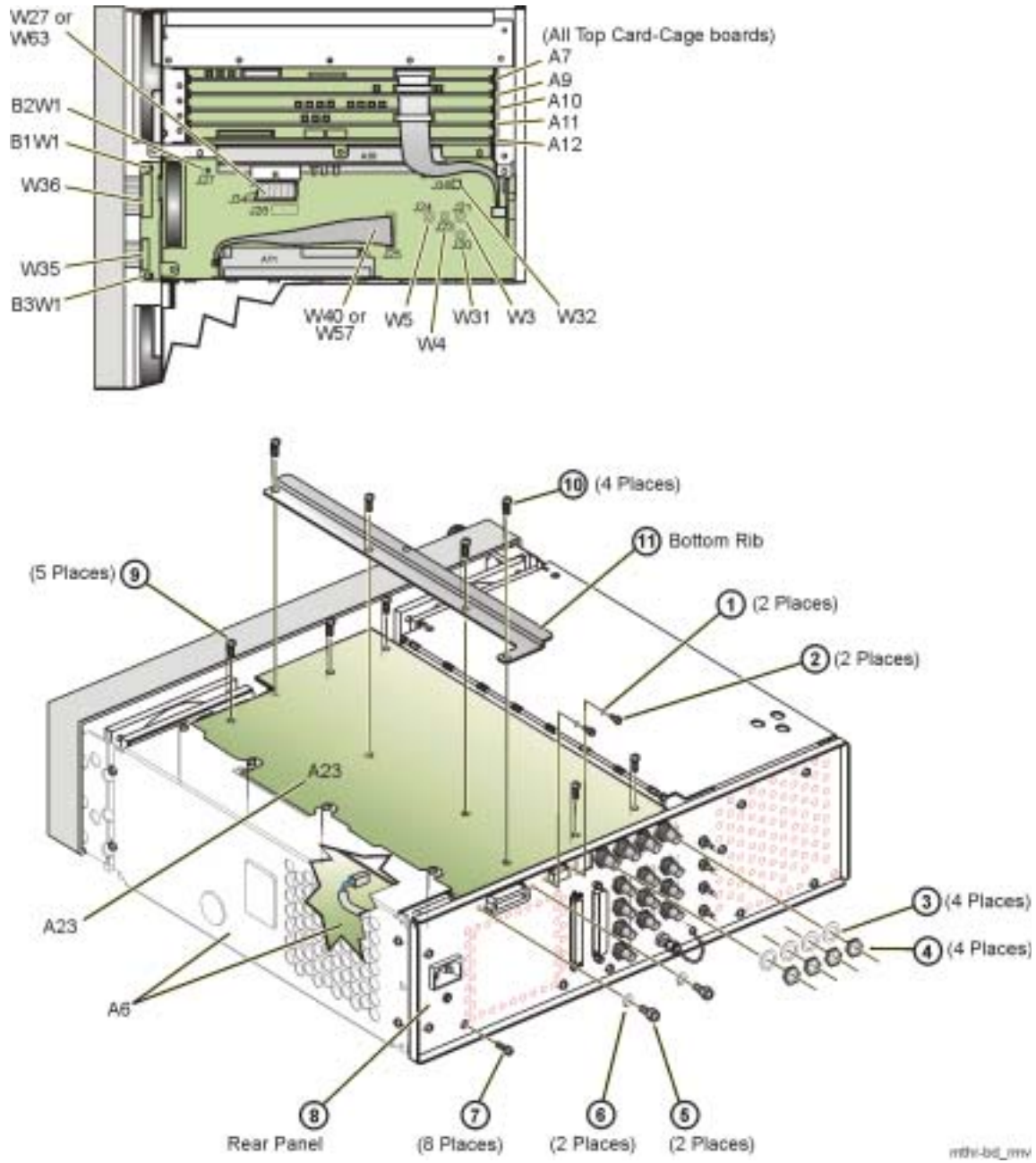
Refer to [Figure 2-30](#) for this procedure.

1. Disconnect the power cord.
2. Remove the outer and top inner covers. Refer to [“Outer and Inner Instrument Covers”](#) on page 2-4.
3. Remove the A6 Power Supply. Refer to [“A6 Power Supply”](#) on page 2-24.
4. Remove all of the assembly boards inserted into the A23 Motherboard.  
For cable removal, refer to [“A12 CPU”](#) on page 2-35.
5. Remove the following cables from the A23 Motherboard:
  - W27 ribbon cable from A23J34 (Option UNJ)
  - W63 ribbon cable from A23J26 (Option 300)
  - W40 ribbon cable from A23J25 (AT1 Electronic Attenuator (Option 501, 502, 503, 504 or Option UNJ)
  - W57 ribbon cable from A23J25 (AT1 High-Power Mechanical Attenuator (Option UNB)
  - W32 wire cable from A23J36
  - W31 from A23J20, using the needle-nose pliers
  - B1W1 fan cable from A23J33
  - B2W1 fan cable from A23J27
  - B3W1 fan cable from A23J29
6. Using the 3/16" nut driver, remove the shoulder nuts (2) and washers (1) on the RS-232 connector.
7. Using the 5/8" open-ended wrench, remove the nuts (3) and washers (4) from the 10 MHz OUT, 10 MHz IN, TRIG IN, and TRIG OUT.
8. Using the 9/32" nut driver, remove the shoulder nuts (5) and washers (6) on the GPIB connector.
9. Using the T-10 driver, remove the eight screws (7) on the Rear Panel (8).
10. Using the T-10 driver, remove the five screws (9) that hold the A23 Motherboard to the chassis.
11. Using the T-10 driver, remove the four screws (10) that hold the bottom rib (11) to the A23 Motherboard.
12. Gently pry out the Rear Panel, guide out the BNCs, and remove the A23 Motherboard from the chassis.

## **Replacement Procedure**

1. Reverse the order of the removal procedure.
2. Torque all T-10 screws to 9 in-lbs.
3. Torque all RF connectors to 9 in-lbs.
4. Torque all 5/8" nuts to 9 in-lbs.
5. Torque all 5/16" nuts to 9 in-lbs.
6. Perform the post-repair adjustments and performance tests that pertain to this removal procedure.

Figure 2-30 A23 Motherboard



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## A23BT1 Battery

### Tools Required

- none

### Removal Procedure

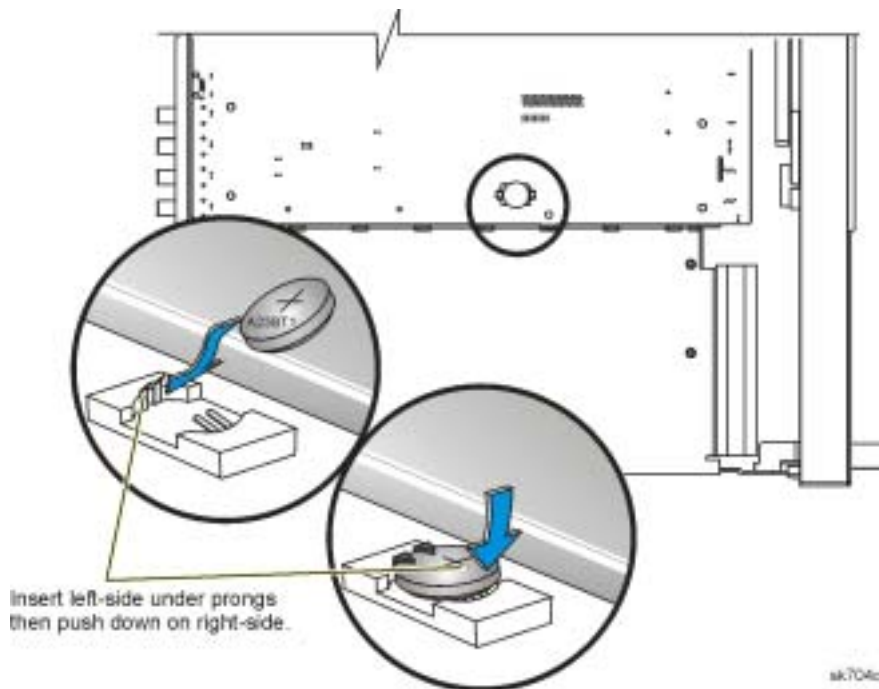
Refer to [Figure 2-31](#) for this procedure.

1. Disconnect the power cord.
2. Remove the outer cover. Refer to “[Outer and Inner Instrument Covers](#)” on page 2-4.
3. Place the signal generator on its side so that the bottom of the signal generator is visible.
4. Remove the battery.

### Replacement Procedure

1. Reverse the order of the removal procedure, but pay special attention when replacing the battery; angle the left-side of the battery so that it goes under the positive prongs before pushing it down.
2. Perform the post-repair adjustments and performance tests that pertain to this removal procedure.

**Figure 2-31**                    **A23BT1 Battery**  
(Option 501, 502, 503, 504, Option 300, Option UNJ, or Option 506)



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## A24 Line Module

### Tools Required

- T-10 driver

### Removal Procedure

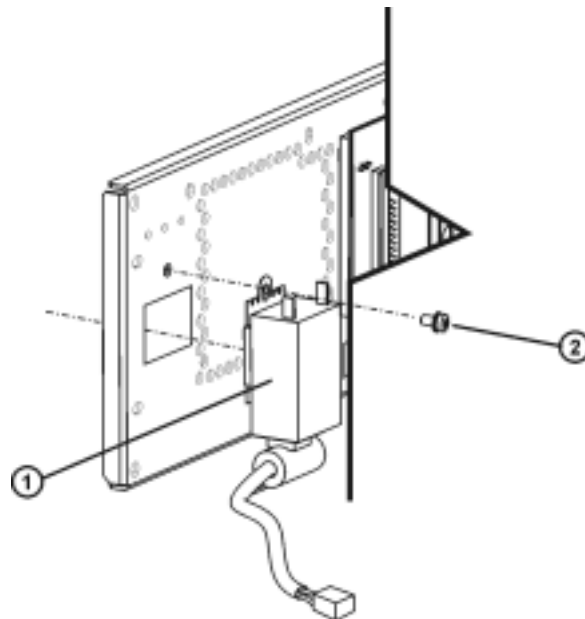
Refer to [Figure 2-32](#) for this procedure.

1. Disconnect the power cord.
2. Remove the outer cover. Refer to “[Outer and Inner Instrument Covers](#)” on page 2-4.
3. Remove the Rear Panel. Refer to “[Rear Panel](#)” on page 2-62.
4. Using a T-10 driver, remove the screw (2) that secures the A24 Line Module (1) to the Rear Panel.
5. Remove the A24 by pulling the metal tab at the top of the line module, and at the same time, pushing up at the bottom of the line module to release it from the rear panel.

### Replacement Procedure

1. Reverse the order of the removal procedure.
2. Torque all T-10 screws to 9 in-lbs.
3. Perform the post-repair adjustments and performance tests that pertain to this removal procedure.

**Figure 2-32**      **A24 Line Module**



2-756

## Rear Panel

### Tools Required

- T-10 driver
- 5/8" hex-nut driver
- 9/32" nut driver
- 3/16" nut driver
- needle-nose pliers

### Removal Procedure

Refer to [Figure 2-33](#) for this procedure.

1. Disconnect the power cord.
2. Remove the outer cover. Refer to [“Outer and Inner Instrument Covers” on page 2-4](#).
3. Disconnect all cables connected to the Rear Panel.
4. Using the 9/32" nut driver, remove the nuts on the GPIB connector (1).
5. Using the 3/16" nut driver, remove the nuts on the RS-232 connector (2).
6. Using the 5/8" hex-nut driver, remove the nuts and washers from the 10 MHz OUT, 10 MHz IN, TRIG IN, and TRIG OUT (3).

---

**NOTE** Do not remove the screw that secures the chain and cap for the COH CARRIER.

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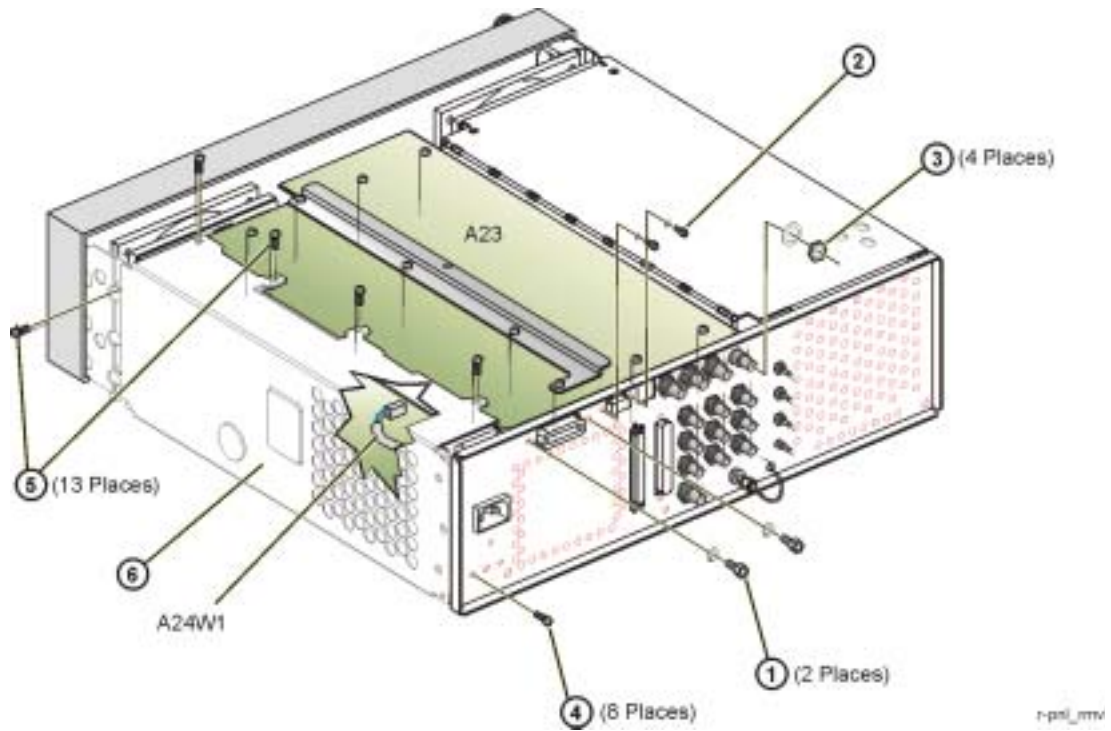
7. Using the T-10 driver, remove the eight screws (4) from the Rear Panel.
8. Using the T-10 driver, remove the 13 screws (5) from the A6 Power Supply's shielding.
9. Disconnect the A24W1 (6) from the A6 Power Supply.
10. Remove the Rear Panel from the chassis.

### Replacement Procedure

1. Reverse the order of the removal procedure.
2. Torque the GPIB hex screw to 9 in-lb.
3. Torque the RS-232 hex screw to 6 in-lb.
4. Torque all T-10 screws to 9 in-lbs.
5. Torque all BNC nuts to 21 in-lbs.
6. Perform the post-repair adjustments and performance tests that pertain to this removal procedure.



Figure 2-33 Rear Panel



## A25 Rear Panel Board

### Tools Required

- T-10 driver
- 3/16" hex-nut driver
- 9/16" open-ended wrench
- 5 mm nut driver
- small phillips screw driver

### Removal Procedure

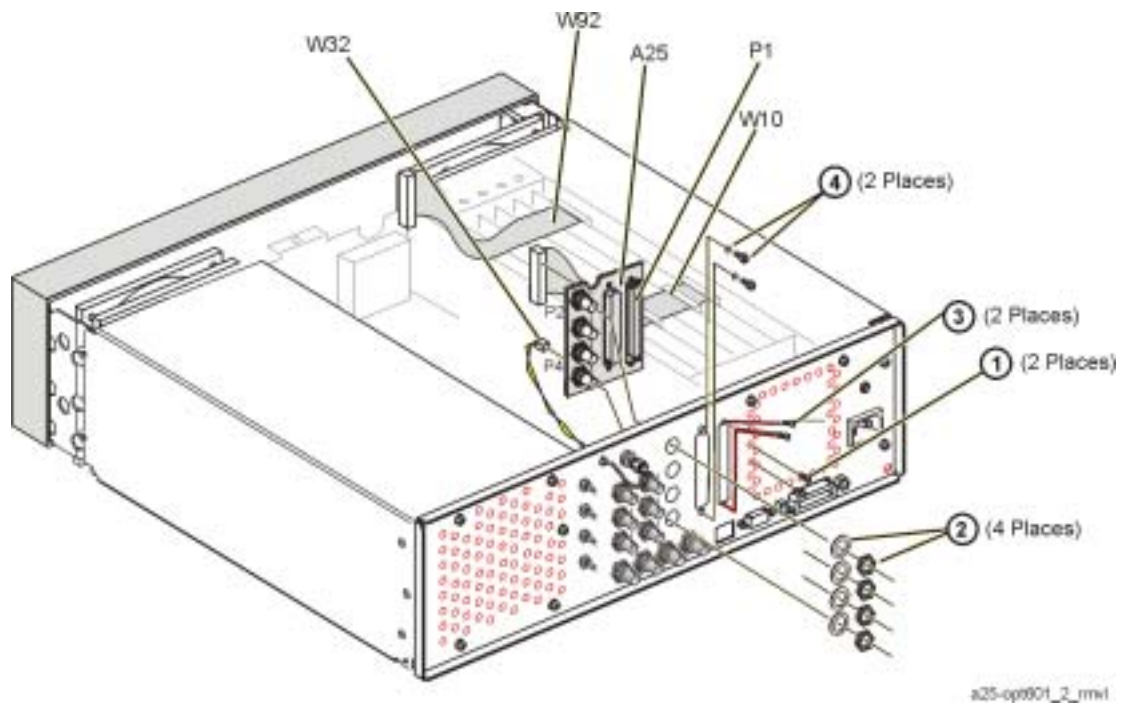
Refer to [Figure 2-34](#) for this procedure.

1. Disconnect the power cord.
2. Remove the outer cover. Refer to [“Outer and Inner Instrument Covers” on page 2-4](#).
3. Disconnect W10 from the A25 Rear Panel Board.
4. Disconnect W32 from the A25 Rear Panel Board.
5. Using the T-10 driver, remove the two screws (1) from the Digital Bust Retainer.
6. Disconnect W92 from P1 on the A25 Rear Panel Board.
7. Using the 9/16" open-ended wrench, remove the nuts on the EVENT 1, EVENT 2, PATT TRIG IN, BURST GATE IN, connectors (2).
8. Using the 5 mm nut driver, remove the shoulder nuts and washers from the AUX I/O connector (3).
9. Using a small phillips screw driver, remove the two screws from the DIG I/Q I/O connector (4).
10. Remove the A25 Rear Panel Board from the chassis.

### Replacement Procedure

1. Reverse the order of the removal procedure.
2. Torque all T-10 screws to 9 in-lbs.
3. Torque all BNC nuts to 21 in-lbs.
4. Perform the post-repair adjustments and performance tests that pertain to this removal procedure.

Figure 2-34 A25 Rear Panel Board



## A26 Rear Panel LVDS Board (Option 1EM) and A27 Rear Panel SMB Board (Option 1EM)

### Tools Required

- T-10 driver
- 5/8" hex-nut driver
- 3/16" hex-nut driver
- 9/16" open-ended wrench
- 5 mm nut driver
- needle-nose pliers

### Removal Procedure

Refer to [Figure 2-35](#) for this procedure.

1. Disconnect the power cord.
2. Remove the outer cover. Refer to [“Outer and Inner Instrument Covers”](#) on page 2-4.
3. Disconnect all cables connected to the A26 Rear Panel LVDS Board (Option 1EM) and A27 Rear Panel SMB Board (Option 1EM).
4. Using the 9/16" open-ended wrench, remove the nuts (1) on the A27 Rear Panel SMB Board (Option 1EM).
5. Using a small phillips screw driver, remove the two screws (2) from the A26 Rear Panel LVDS Board (Option 1EM).
6. Using the 5 mm nut driver, remove the shoulder screws (3) from the A26 Rear Panel LVDS Board (Option 1EM).
7. Remove the A26 Rear Panel LVDS Board (Option 1EM) and A27 Rear Panel SMB Board (Option 1EM) from the chassis.

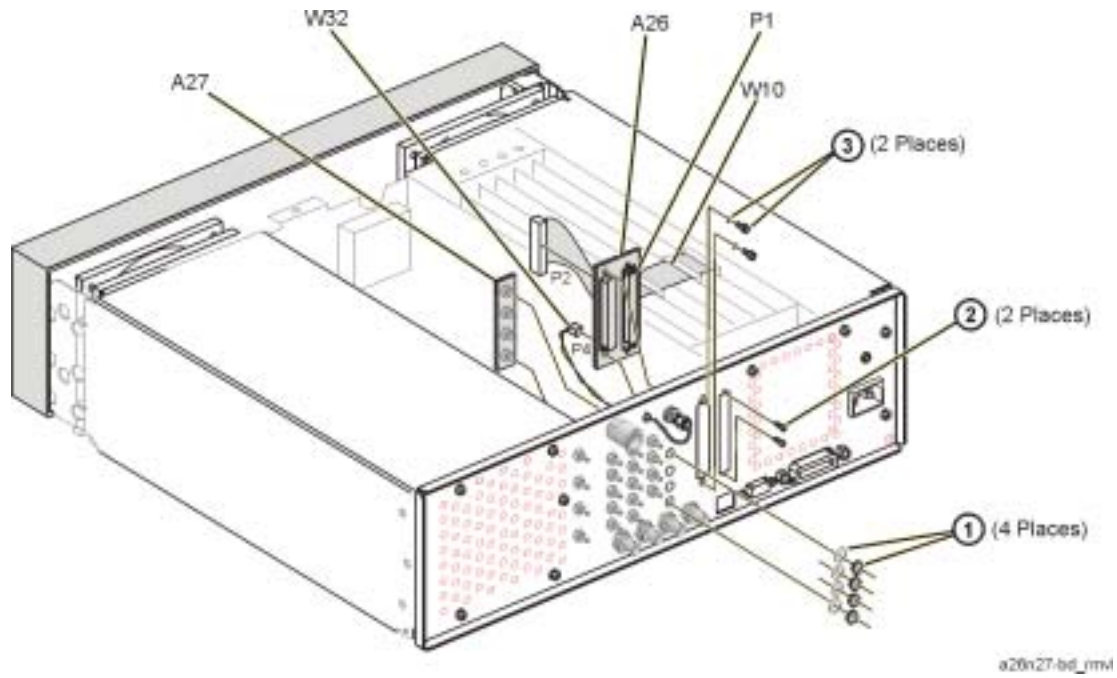
### Replacement Procedure

1. Reverse the order of the removal procedure.
2. Torque the GPIB hex screw to 9 in-lb.
3. Torque the RS-232 hex screw to 6 in-lb.
4. Torque all T-10 screws to 9 in-lbs.
5. Torque all BNC nuts to 21 in-lbs.
6. Perform the post-repair adjustments and performance tests that pertain to this removal procedure.

**A26 Rear Panel LVDS Board (Option 1EM) and A27 Rear Panel SMB Board (Option 1EM)**

Figure 2-35

**A26 Rear Panel LVDS Board (Option 1EM)  
and A27 Rear Panel SMB Board (Option 1EM)**



## **AT1 High-Power Mechanical Attenuator (Option UNB) with A28 Reverse Power Protection (Option 501, 502, 503, 504 or Option UNJ)**

### **Tools Required**

- T-10 driver
- 5/16" open ended wrench

### **Removal Procedure**

Refer to [Figure 2-36](#) for this procedure.

1. Disconnect the power cord.
2. Remove the outer and top inner cover. Refer to [“Outer and Inner Instrument Covers”](#) on page 2-4.
3. Using the 5/16" open ended wrench:
  - disconnect W47 and W48
  - disconnect W49

On signal generators with Option 1EM, disconnect W86. (Refer to [“Option 501, 502, 503, 504 - Option 1EM Semi-Rigid Cables includes the AT1 Electronic Attenuator \(Option 501, 502, 503, 504 or Option UNJ\) or the AT1 High-Power Mechanical Attenuator \(Option UNB\) with the A28 Reverse Power Protection \(Option 501, 502, 503, 504 or Option UNJ\)”](#) on page 3-33.)

4. Disconnect W57 from A23J25.
5. Disconnect W53 from A23J22.
6. Using the T-10 driver, remove the two screws (1) from the AT1 bracket that secures the AT1 High-Power Mechanical Attenuator (Option UNB) and the A28 Reverse Power Protection (Option 501, 502, 503, 504 or Option UNJ) to the chassis.
7. Tilt the AT1 bracket away from the chassis and slide out the tabs from the slots.

There are two hinged tabs at the bottom of the AT1 bracket that fit into slots in the signal generator's chassis.

8. Using the T-10 driver remove the four screws (2) that attach the RPP bracket to the AT1 bracket.
9. Using the T-10 driver remove the four screws (3) that attach the AT1 High-Power Mechanical Attenuator (Option UNB) to the AT1 bracket.
10. Using the T-10 driver remove the three screws (4) that attach the A28 Reverse Power Protection (Option 501, 502, 503, 504 or Option UNJ) to the RPP bracket.
11. Remove the A28 Reverse Power Protection (Option 501, 502, 503, 504 or Option UNJ).

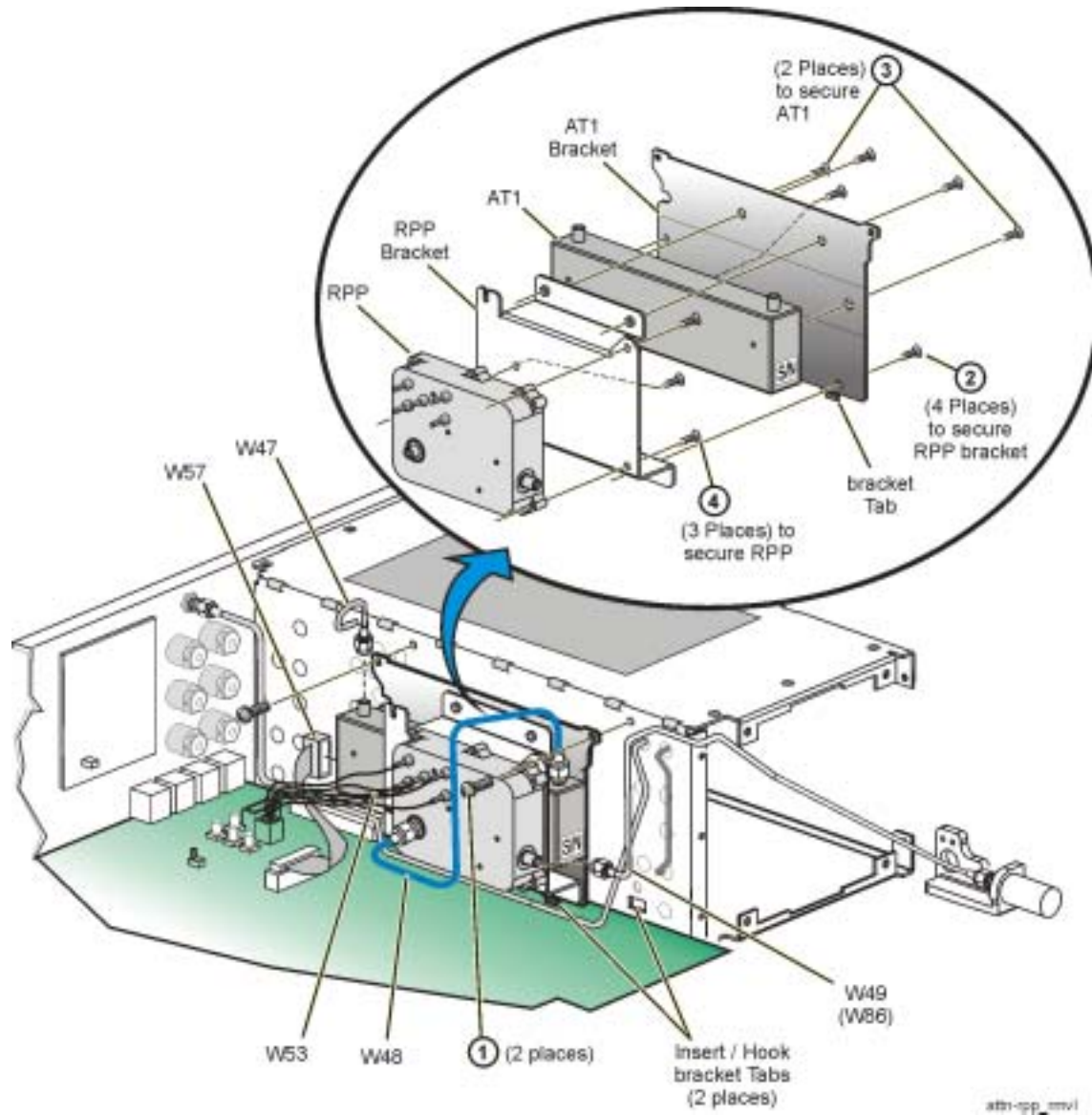
### **Replacement Procedure**

1. Reverse the order of the removal procedure.

**AT1 High-Power Mechanical Attenuator (Option UNB) with A28 Reverse Power Protection (Option 501, 502, 503, 504 or Option UNJ)**

2. Torque all T-10 screws to 9 in-lbs.
3. Torque all RF cables to 9 in-lbs.
4. Perform the post-repair adjustments and performance tests that pertain to this removal procedure.

**Figure 2-36 AT1 High-Power Mechanical Attenuator (Option UNB) with A28 Reverse Power Protection (Option 501, 502, 503, 504 or Option UNJ)**



## **AT1 High-Power Mechanical Attenuator (Option UNB)**

---

**NOTE** On signal generators equipped with an RPP, refer to “[AT1 High-Power Mechanical Attenuator \(Option UNB\) with A28 Reverse Power Protection \(Option 501, 502, 503, 504 or Option UNJ\)](#)” on page 2-68 for removal instructions. The following procedure is primarily for Option 506.

---

### **Tools Required**

- T-10 driver
- 5/16” open ended wrench

### **Removal Procedure**

Refer to [Figure 2-37](#) for this procedure.

1. Disconnect the power cord.
2. Remove the outer and inner covers. Refer to “[Outer and Inner Instrument Covers](#)” on page 2-4.
3. On signal generators equipped with an RPP, refer to “[AT1 High-Power Mechanical Attenuator \(Option UNB\) with A28 Reverse Power Protection \(Option 501, 502, 503, 504 or Option UNJ\)](#)” on page 2-68 for removal instructions.
4. Using the 5/16” open ended wrench:
  - disconnect W50
  - disconnect W59

On signal generators with Option 1EM, disconnect W91. (Refer to “[Option 506 - Option 1EM Semi-Rigid Cables includes the A21 YTO Driver \(Option UNJ or Option 506\) and the AT1 High-Power Mechanical Attenuator \(Option UNB\) with an A29 DC Blocking Capacitor \(Option 506\)](#)” on page 3-58.)

5. Disconnect W57 from A23J25.
6. Using the T-10 driver, remove the two screws (1) that attach the AT1 High-Power Mechanical Attenuator (Option UNB) to the chassis.
7. Tilt the AT1 bracket away from the chassis and slide out the tabs from the slots.

There are two hinged tabs at the bottom of the AT1 bracket that fit into slots in the signal generator’s chassis.
8. Using the T-10 driver, remove the two screws (2) that attach the AT1 bracket to the AT1 High-Power Mechanical Attenuator (Option UNB).
9. Remove the AT1 High-Power Mechanical Attenuator (Option UNB).

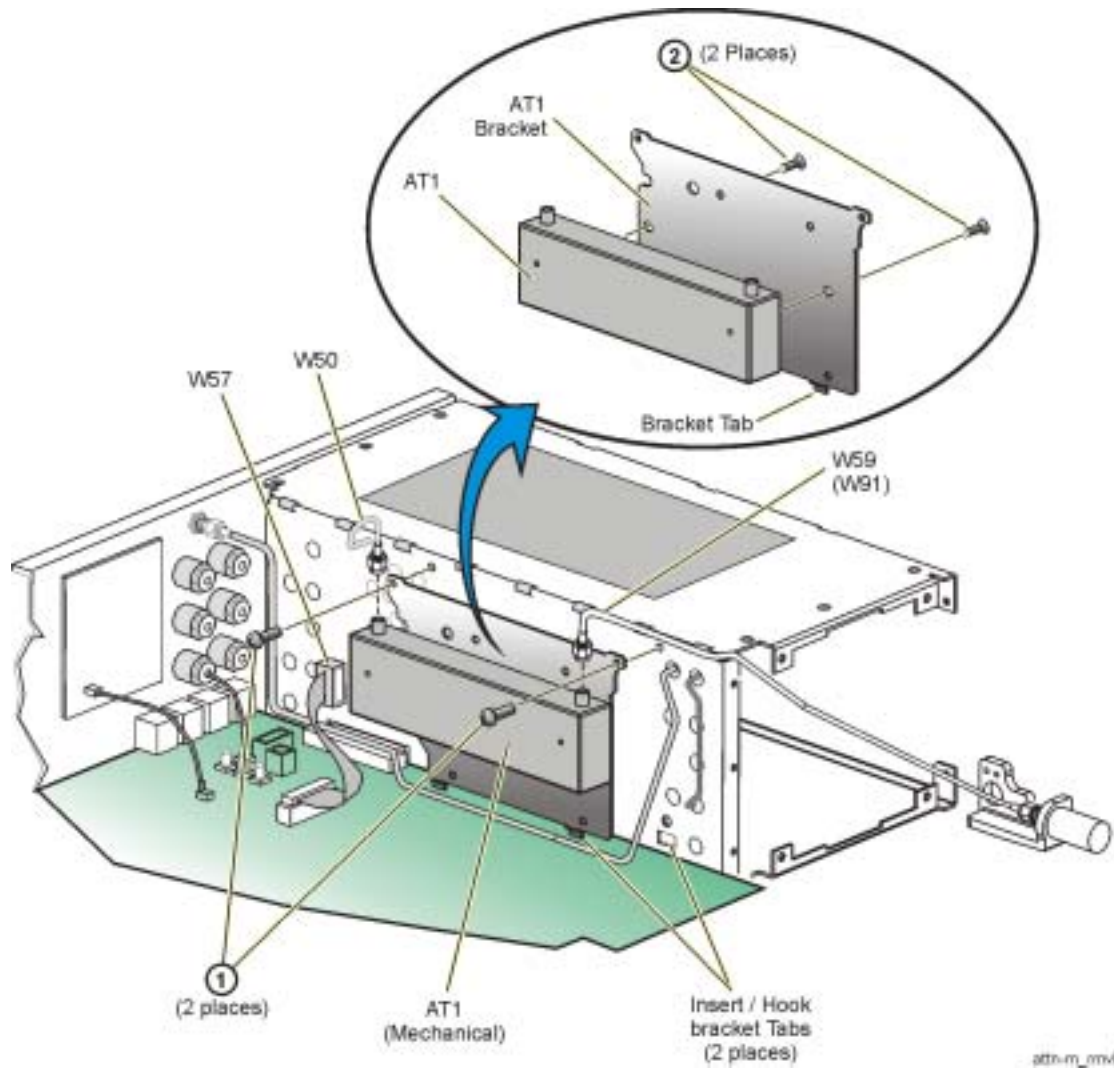
### **Replacement Procedure**

1. Reverse the order of the removal procedure.
2. Torque all T-10 screws to 9 in-lbs.
3. Torque all RF connectors to 8 in-lbs.



4. Perform the post-repair adjustments and performance tests that pertain to this removal procedure.

Figure 2-37 AT1 High-Power Mechanical Attenuator (Option UNB)



atn-m\_rml

## **AT1 Electronic Attenuator (Option 501, 502, 503, 504 or Option UNJ)**

### **Tools Required**

- T-10 driver
- 5/16" open ended wrench

### **Removal Procedure**

Refer to [Figure 2-38](#) for this procedure.

1. Disconnect the power cord.
2. Remove the outer and inner covers. Refer to [“Outer and Inner Instrument Covers” on page 2-4](#).
3. Using the 5/16" open ended wrench:
  - disconnect W43
  - disconnect W9

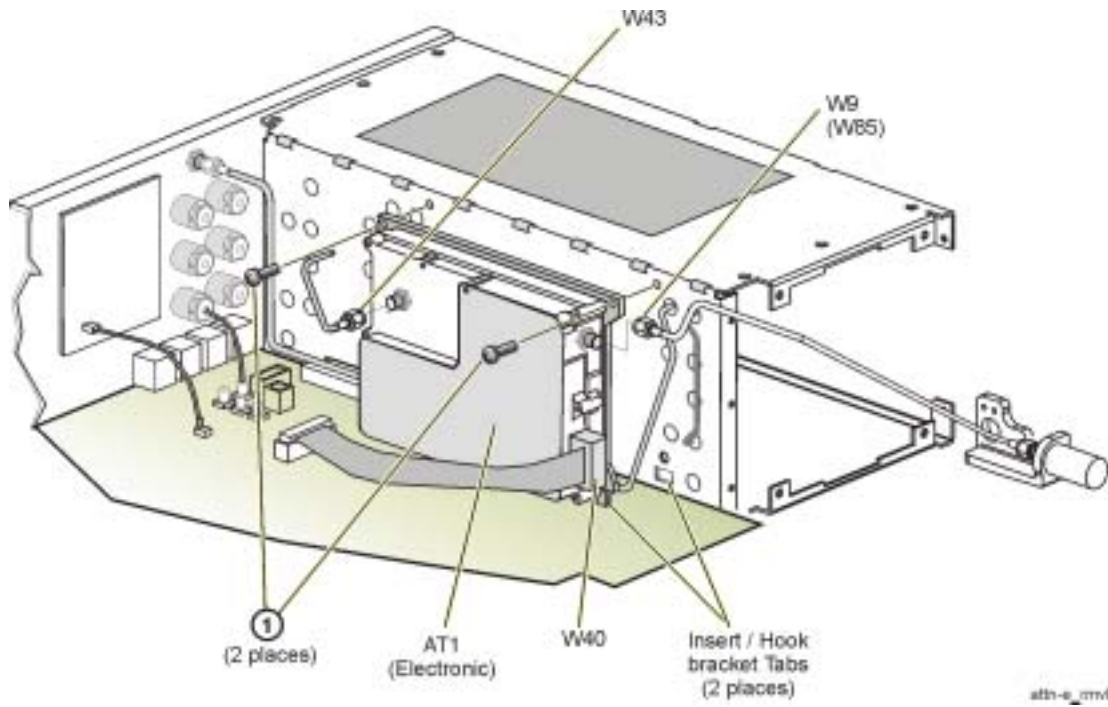
On signal generators with Option 1EM, disconnect W85. (Refer to [“Option 501, 502, 503, 504 - Option 1EM Semi-Rigid Cables includes the AT1 Electronic Attenuator \(Option 501, 502, 503, 504 or Option UNJ\) or the AT1 High-Power Mechanical Attenuator \(Option UNB\) with the A28 Reverse Power Protection \(Option 501, 502, 503, 504 or Option UNJ\)” on page 3-33](#).)

4. Using the T-10 driver, remove the two screws (1) that attach the AT1 Electronic Attenuator (Option 501, 502, 503, 504 or Option UNJ) to the chassis.
5. Disconnect W40 from A23J25.
6. Remove the AT1 Electronic Attenuator (Option 501, 502, 503, 504 or Option UNJ) by leaning it forward and guiding out the hinged tabs located at the bottom of the bracket.
7. Using the T-10 driver, remove the three screws that attach the sheet metal to the AT1 Electronic Attenuator (Option 501, 502, 503, 504 or Option UNJ).

### **Replacement Procedure**

1. Reverse the order of the removal procedure.
2. Torque all T-10 screws to 9 in-lbs.
3. Torque all RF connectors to 8 in-lbs.
4. Perform the post-repair adjustments and performance tests that pertain to this removal procedure.

Figure 2-38 AT1 Electronic Attenuator (Option 501, 502, 503, 504 or Option UNJ)



## **A29 DC Blocking Capacitor (Option 506)**

### **Tools Required**

- 5/16" open ended wrench

### **Removal Procedure**

Refer to [Figure 2-39](#) for this procedure.

1. Disconnect the power cord.
2. Remove the outer and top inner cover. Refer to [“Outer and Inner Instrument Covers” on page 2-4](#).
3. Remove the front panel. Refer to [“Front Panel” on page 2-6](#).
4. Using the 5/16" open ended wrench, disconnect W59 from the A29 DC Blocking Capacitor (Option 506).
5. Using the 5/16" open ended wrench, disconnect A29 DC Blocking Capacitor (Option 506) from W60.
6. Remove the A29 DC Blocking Capacitor (Option 506).

### **Removal Procedure for Option 1EM**

Refer to [Figure 2-40](#) for this procedure.

1. Disconnect the power cord.
2. Remove the outer and top inner cover. Refer to [“Outer and Inner Instrument Covers” on page 2-4](#).
3. Using the 5/16" open ended wrench, disconnect W91 from the A29 DC Blocking Capacitor (Option 506).
4. Using the 5/16" open ended wrench, disconnect A29 DC Blocking Capacitor (Option 506) from the RF Output connector.
5. Remove the A29 DC Blocking Capacitor (Option 506).

### **Replacement Procedure**

1. Reverse the order of the removal procedure.
2. Torque all RF cables to 9 in-lbs.
3. Perform the post-repair adjustments and performance tests that pertain to this removal procedure.

Figure 2-39 A29 DC Blocking Capacitor (Option 506)

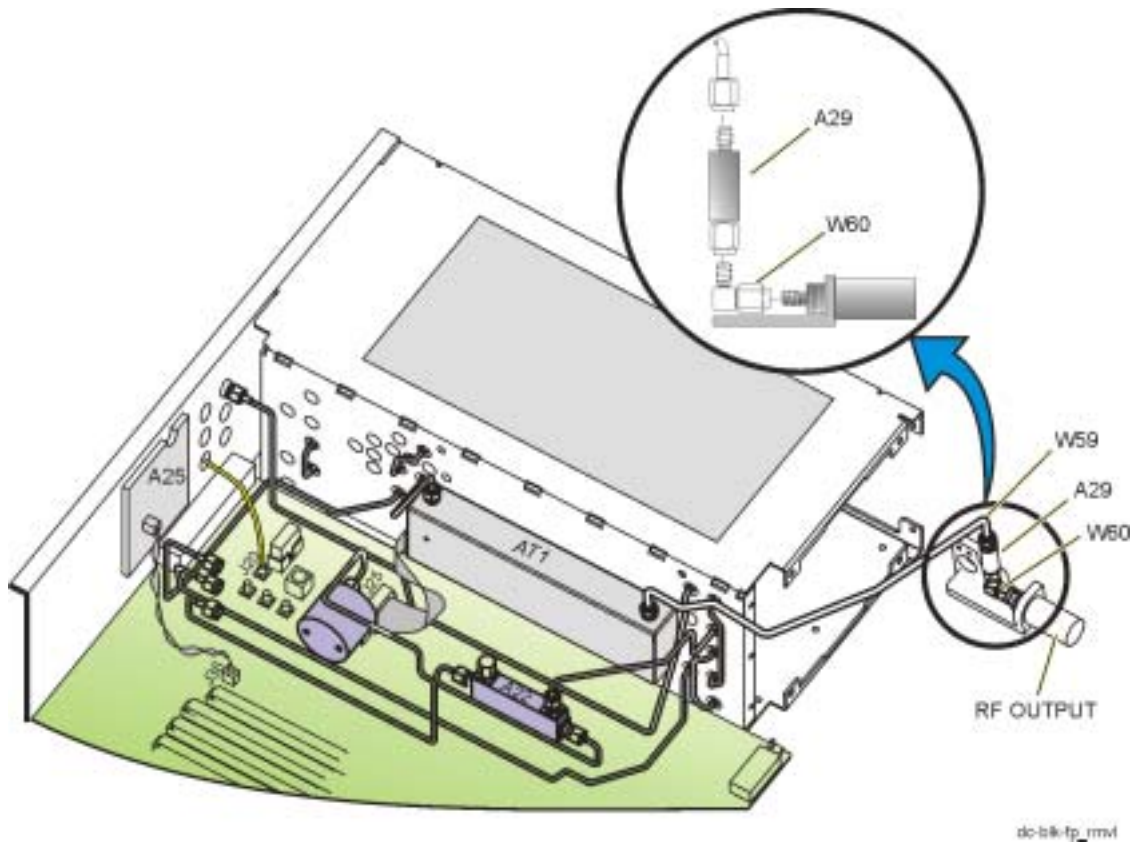
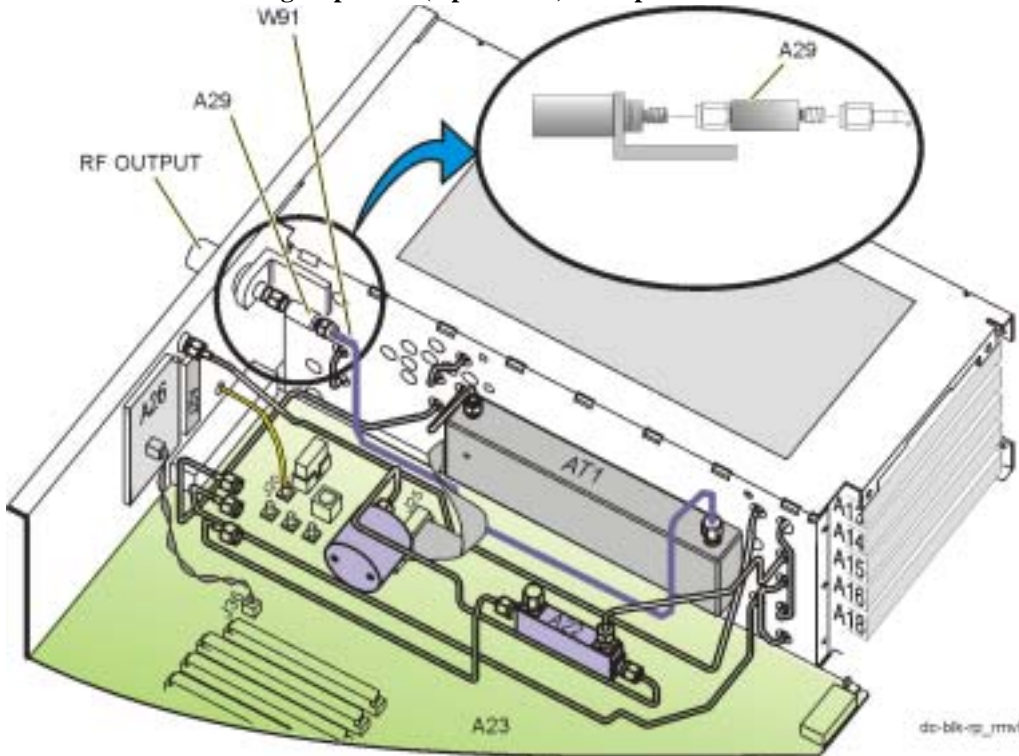


Figure 2-40 A29 DC Blocking Capacitor (Option 506) for Option 1EM



## **B1 Power Supply Fan**

### **Tools Required**

- T-10 driver

### **Removal Procedure**

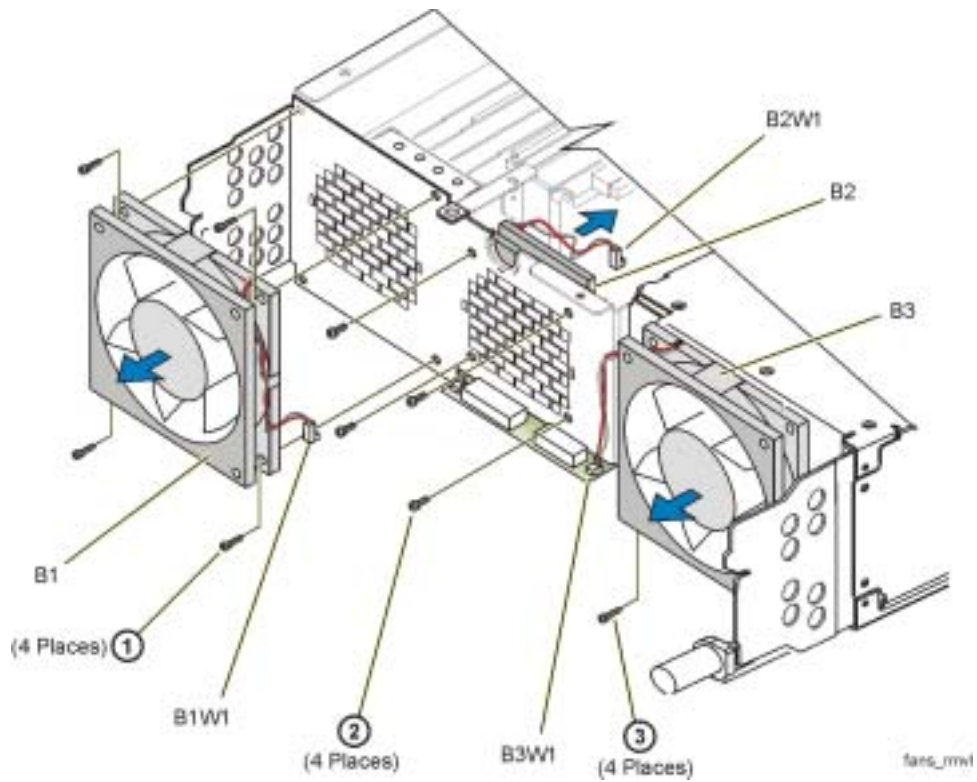
Refer to [Figure 2-41](#) for this procedure.

1. Disconnect the power cord.
2. Remove the outer cover. Refer to [“Outer and Inner Instrument Covers” on page 2-4](#).
3. Remove the front panel. Refer to [“Front Panel” on page 2-6](#).
4. Remove the four screws (1) that attach the B1 Power Supply Fan to the chassis using a T-10 driver.
5. Disconnect the B1W1 fan cable from the A23J33.
6. Remove the B1 Power Supply Fan.

### **Replacement Procedure**

1. Reverse the order of the removal procedure.
2. Torque all T-10 screws to 9 in-lbs.
3. Perform the post-repair adjustments and performance tests that pertain to this removal procedure.

Figure 2-41 B1 Power Supply Fan



## **B2 Small Fan**

### **Tools Required**

- T-10 driver

### **Removal Procedure**

Refer to [Figure 2-42](#) for this procedure.

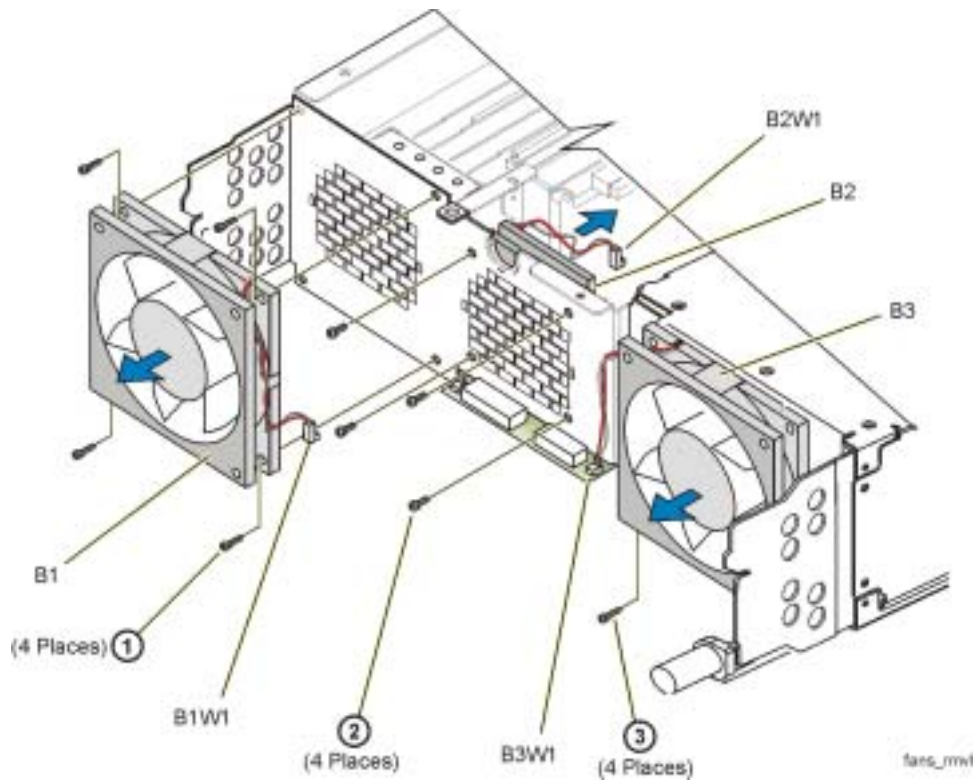
1. Disconnect the power cord.
2. Remove the outer and inner covers. Refer to [“Outer and Inner Instrument Covers”](#) on page 2-4.
3. Remove the front panel. Refer to [“Front Panel”](#) on page 2-6.
4. Remove the four screws (2) that attach the B2 Small Fan to the chassis using a T-10 driver.
5. Disconnect the B2W1 fan cable from the A23J27.
6. Remove the B2 Small Fan.

### **Replacement Procedure**

1. Reverse the order of the removal procedure.
2. Torque all T-10 screws to 9 in-lbs.
3. Perform the post-repair adjustments and performance tests that pertain to this removal procedure.



Figure 2-42 B2 Small Fan



## **B3 Daughterboard/Card Cage Fan**

### **Tools Required**

- T-10 driver

### **Removal Procedure**

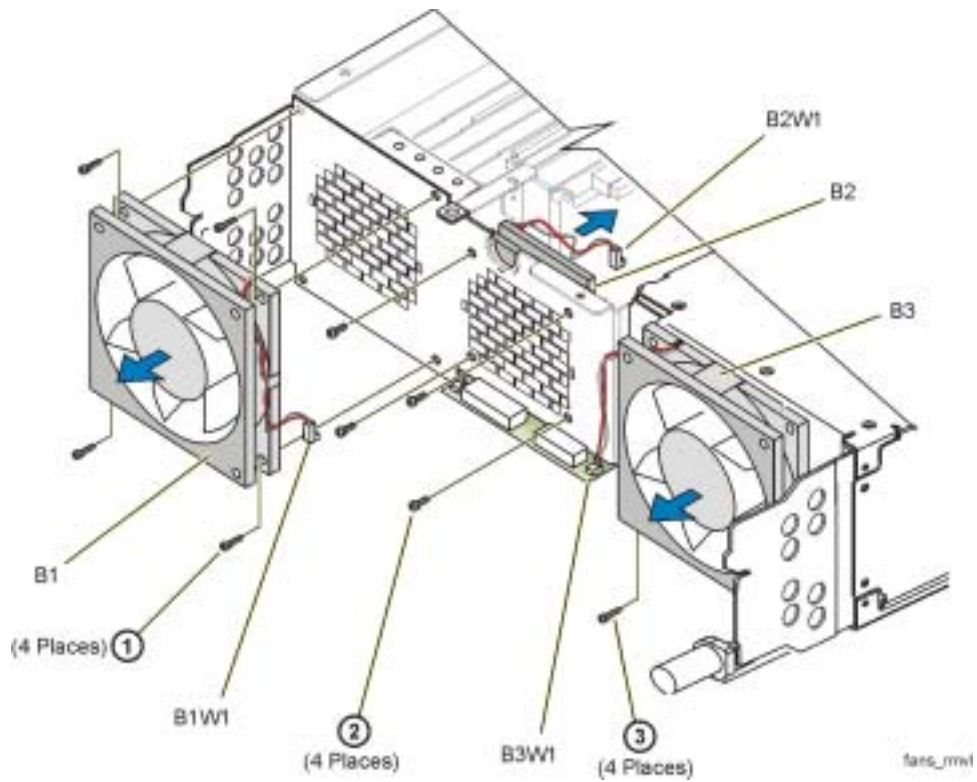
Refer to [Figure 2-43](#) for this procedure.

1. Disconnect the power cord.
2. Remove the outer and inner covers. Refer to [“Outer and Inner Instrument Covers”](#) on page 2-4.
3. Remove the front panel. Refer to [“Front Panel”](#) on page 2-6.
4. Remove the four screws (3) that attached the B3 Daughterboard/Card Cage Fan to the chassis using a T-10 driver.
5. Disconnect the B3W1 fan cable from the A23J29.
6. Remove the B3 Daughterboard/Card Cage Fan.

### **Replacement Procedure**

1. Reverse the order of the removal procedure.
2. Torque all T-10 screws to 9 in-lbs.
3. Perform the post-repair adjustments and performance tests that pertain to this removal procedure.

Figure 2-43 B3 Daughterboard/Card Cage Fan



## RF Output Connector

### Tools Required

- 5/16" open ended wrench
- 9/16" open ended wrench
- T-10 driver

### Removal Procedure

Refer to [Figure 2-44](#) for this procedure.

1. Disconnect the power cord.
2. Remove the outer and top inner cover. Refer to [“Outer and Inner Instrument Covers” on page 2-4](#).
3. Remove the front panel. Refer to [“Front Panel” on page 2-6](#).
4. On signal generators:
  - with Option 501, 502, 503, 504 or Option UNJ, using the 5/16" open ended wrench:
    - disconnect W9 from the RF Output Connector
  - with Option 506, using the 5/16" open ended wrench:
    - disconnect W59 from the A29 DC Blocking Capacitor (Option 506)
    - disconnect A29 DC Blocking Capacitor (Option 506) from W60
    - disconnect W60 from the RF Output Connector
5. Using the T-10 driver, remove the two screws (1) that hold the RF Output Connector and bracket (2) to the chassis; they are replaced as one assembly.

### Removal Procedure for Option 1EM

Refer to [Figure 2-45](#) for this procedure.

1. Disconnect the power cord.
2. Remove the outer and top inner cover. Refer to [“Outer and Inner Instrument Covers” on page 2-4](#).
3. Remove the rear-panel chassis-support bracket. (From a top view, this covers the RF Output Connector.)
4. On signal generators:
  - with Option 501, 502, 503, 504 or Option UNJ, using the 5/16" open ended wrench:
    - disconnect W85 from the RF Output Connector
  - with Option 506, using the 5/16" open ended wrench:
    - disconnect W91 from the A29 DC Blocking Capacitor (Option 506)
    - disconnect A29 DC Blocking Capacitor (Option 506) from the RF Output Connector

5. Using the T-10 driver, remove the two screws (1) that hold the RF Output Connector and bracket to the chassis.
6. Using the 9/16" open ended wrench, remove the nut and washer holding the RF Output Connector to the bracket (2); the bracket does not need to be replaced.

## Replacement Procedure

1. Reverse the order of the removal procedure.
2. Torque all RF cables to 9 in-lbs.
3. Perform the post-repair adjustments and performance tests that pertain to this removal procedure.

Figure 2-44 RF Output Connector

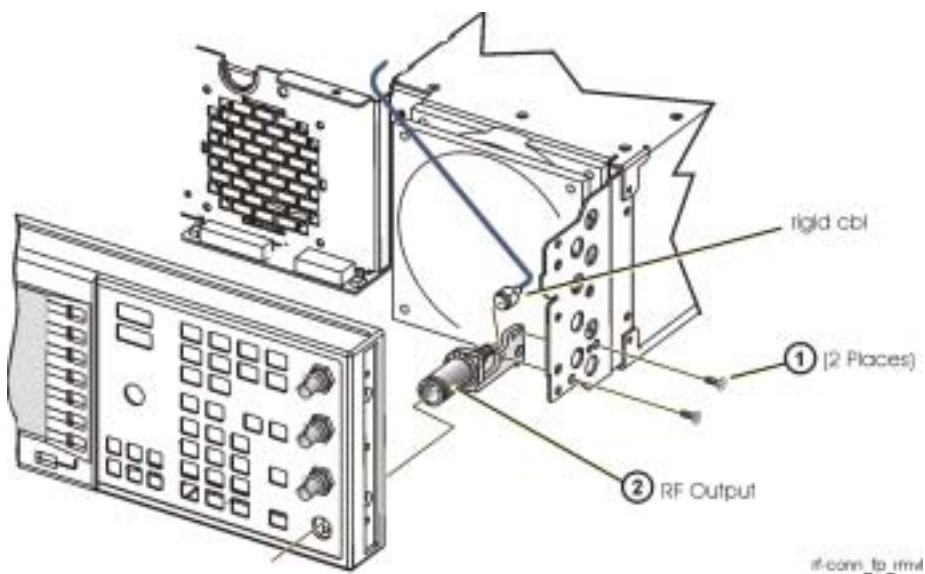
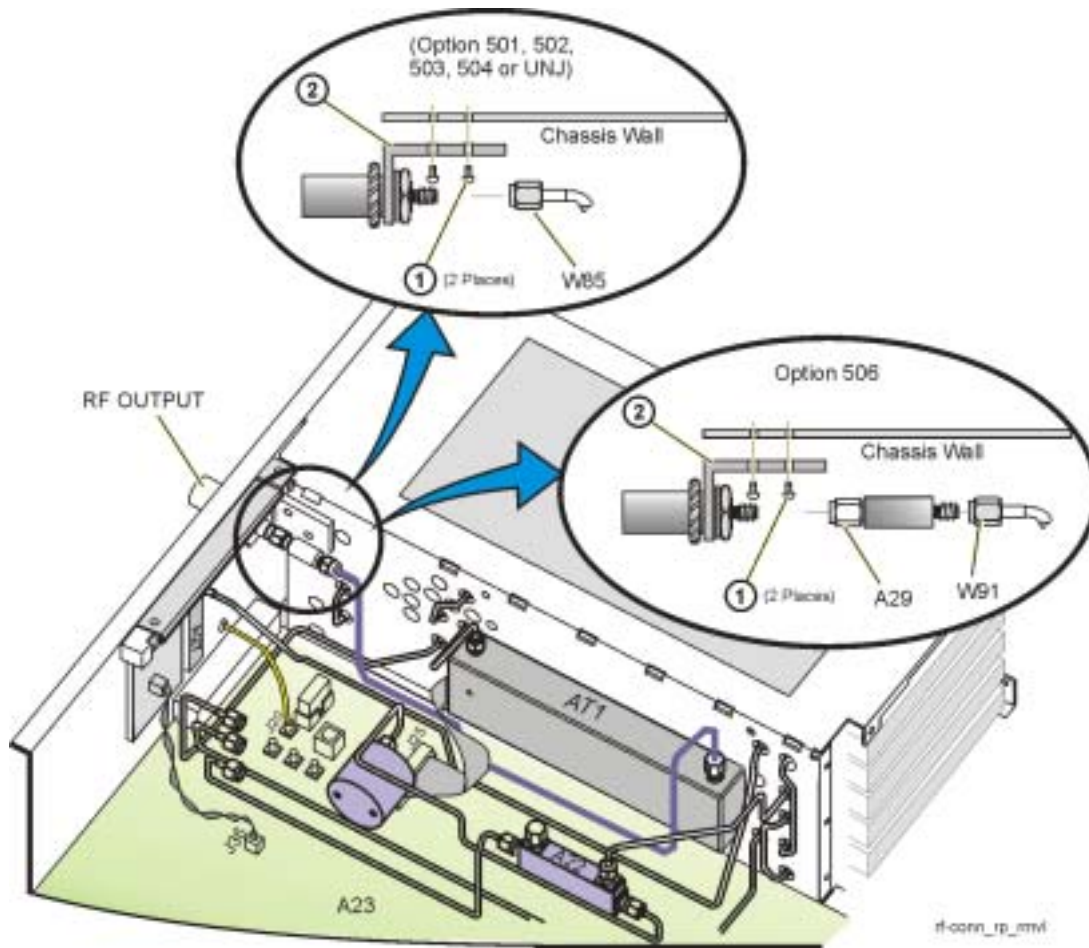


Figure 2-45 RF Output Connector for Option 1EM



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## **3 Replaceable Parts**

## Ordering Information

To order a part, do the following:

1. Determine the part number.
2. Determine the quantity required.
3. Mail this information to the nearest Agilent Technologies office or, in the U.S., call the hot-line number listed in the following section.

To order a part not listed in the replaceable parts lists, mail the following information to the nearest Agilent Technologies office or, in the United States, call the hot-line number listed in the following section.

- the instrument model number
- the serial number and options, if any (see rear panel)
- a description of the part
- a description of the part's function
- the quantity required

### **Call (800) 227-8164 to Order Parts Fast (U.S. Only)**

When you have gathered the information required to place an order, contact Agilent Technologies' direct ordering team by calling the toll-free hot-line number shown above. Orders may be placed Monday through Friday, 6 AM to 5 PM (Pacific Standard Time).

The parts specialists have direct on-line access to replacement parts inventory corresponding to the replaceable parts lists in this manual. Four day delivery time is standard; there is a charge for hot-line one-day delivery.

This information applies to the United States only. Outside the United States, you must contact the nearest Agilent Technologies sales and service office.

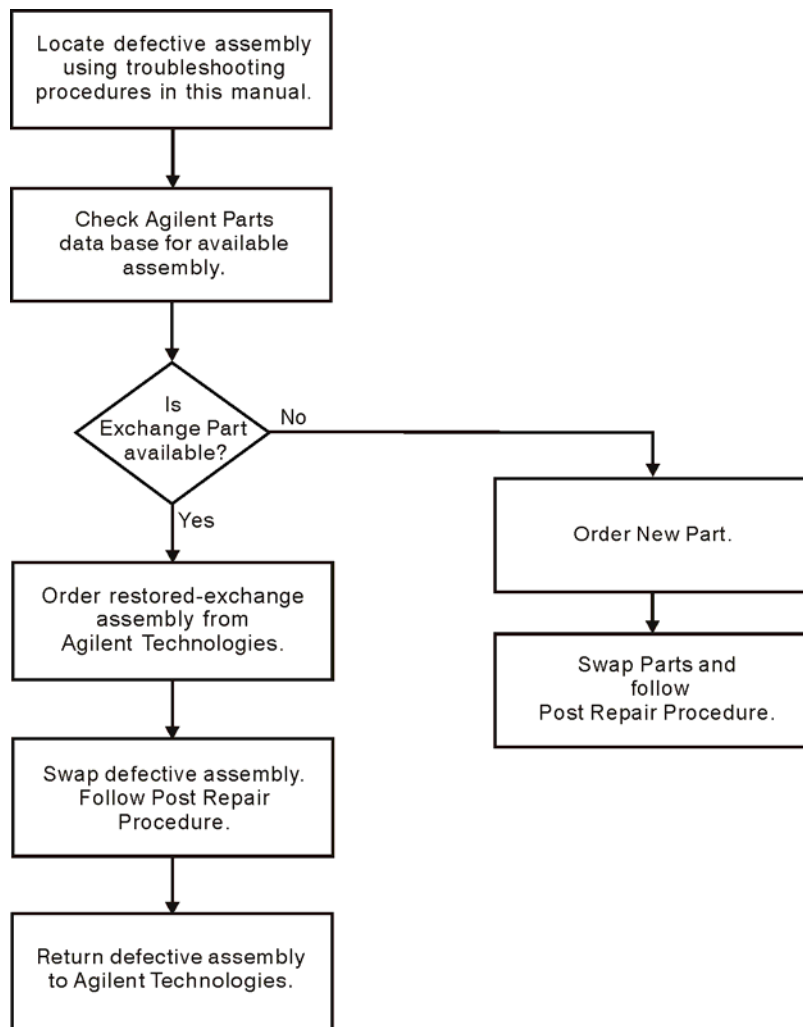


## Save Money with Rebuilt-Exchange Assemblies

Under the rebuilt-exchange assembly program, certain factory-repaired and tested assemblies are available on a trade-in basis. These assemblies cost less than a new assembly and meet all factory specifications required of a new assembly.

The defective assembly must be returned for credit under the terms of the rebuilt-exchange assembly program. The figure below illustrates the assembly exchange procedure in flowchart format.

**Figure 3-1** Assembly Exchange Procedure



sk770a

## Shipping a Defective Assembly Back to Agilent Technologies

1. When you receive the rebuilt assembly, be careful not to damage the box in which it was shipped. You will use that box to return the defective assembly. The box you receive should contain the following:
  - the rebuilt assembly
  - an exchange assembly failure report
  - a return address label
2. Complete the failure report.
3. Place the failure report and the defective assembly in the box. Be sure to remove the enclosed return address label.
4. Seal the box with tape.

If you are inside the United States, stick the pre-printed return address label over the label that is already on the box and return the box to Agilent Technologies. (Agilent Technologies pays postage on boxes mailed within the United States.)

If you are outside the United States, do not use the return address label; instead, address the box to the nearest Agilent Technologies sales and service office.

## Frequency, Hardware, and Personality Options

This section lists the options available with the Agilent E4438C ESG Vector Signal Generator.

<b>Frequency Options</b>	
501	250 kHz to 1 GHz frequency range
502	250 kHz to 2 GHz frequency range
503	250 kHz to 3 GHz frequency range
504	250 kHz to 4 GHz frequency range
506	250 kHz to 6 GHz frequency range (Requires Option UNJ)

<b>Hardware Options</b>	
UNB	High output power with mechanical attenuator (not compatible with Option 506)
UNJ	Enhanced phase noise performance (includes high stability timebase, Option 1E5)
UN7	Internal bit-error-rate analyzer
1E5	High stability timebase
1EM	Moves all front panel connectors to rear panel
001/601	Internal baseband generator with 8 Mega sample memory
002	Internal baseband generator with 32 Mega sample memory
003	Enables ESG digital output connectivity with N5102A (Option 601 or 602 required)
005	10 GB hard drive (Option 001/601 or 002/602 required)
300	GSM/EDGE basestation loopback BER test capability (Requires Options UN7, 001/601 or 002/602, and 402)
602	Internal baseband generator with 64 Mega sample memory

<b>Signal Generator Personalities</b>	
400	3GPP W-CDMA-FDD personalities
401	cdma2000 and IS95A personalities
402	TDMA personalities (includes GSM, EDGE, NADC, and others)
403	Calibrated noise personalities
409	Real-Time GPS (Option 001/601 or 002/602 required)
416	Enhanced observed time difference (E-OTD, Option 001/601/ or 002/602 and 402 required)

<b>Software Personalities</b>	
404	cdma2000-1xEV-DO Signal Studio personality

Replaceable Parts

Hardware and Other Instrument Parts

405	Wireless LAN (802.11a) Signal Studio personality
406	Bluetooth Signal Studio personality
410	Wireless LAN (802.11b) Signal Studio personality

## Assemblies and Hardware Options

Assembly	Option 501, 502, 503, 504 and Option 300	Option UNJ	Option 506
A1 Keyboard <a href="#">page 3-16</a>	E4400-60502	E4400-60502	E4400-60502
A2 Display <a href="#">page 3-16</a>	2090-0375	2090-0375	2090-0375
A2DS1 Display Backlight <a href="#">page 3-16</a>	1513-5204	1513-5204	1513-5204
A3 Front Panel Auxiliary Board <a href="#">page 3-16</a>	E4400-60503	E4400-60503	E4400-60503
A4 Inverter <a href="#">page 3-16</a>	0950-3757	0950-3757	0950-3757
A5 Hard Drive (Option 005) <a href="#">page 3-16</a>	E4400-60253	E4400-60253	E4400-60253
A6 Power Supply <a href="#">page 3-10</a>	0950-4031	0950-4031	0950-4031
<b>Slot 1<sup>a</sup></b> A7 Baseband Generator (Option 001) <a href="#">page 3-10</a> or A7 Baseband Generator (Option 002) <a href="#">page 3-10</a>	E4400-60539	E4400-60539	E4400-60539
	E4400-60501	E4400-60501	E4400-60501
<b>Slot 1</b> (for instruments with serial prefixes <US4426/MY4426) A7 Baseband Generator (Option 601) <a href="#">page 3-10</a> or A7 Baseband Generator (Option 602) <a href="#">page 3-10</a>	E4400-60652	E4400-60652	E4400-60652
	E4400-60654	E4400-60654	E4400-60654
<b>Slot 1</b> (for instruments with serial prefixes ≥US4426/MY4426) A7 Baseband Generator (Option 601/602) <a href="#">page 3-10</a>	E4400-60744	E4400-60744	E4400-60744
<b>Slot 2</b> A9 Digital Demodulator (Option 501, 502, 503, 504 with Option 300) <a href="#">page 3-10</a>	E4400-60510	N/A	N/A
<b>Slot 3</b> A10 I/Q Multiplexer (Option 501, 502, 503, 504 or Option UNJ) <a href="#">page 3-10</a> or A10 I/Q Multiplexer (Option 506) <a href="#">page 3-10</a>	E4400-60191	E4400-60191	N/A
	N/A	N/A	E8251-60063
<b>Slot 4</b> A11 Internal Bit Error Rate Analyzer (Option UN7) <sup>b</sup> <a href="#">page 3-10</a>	E4400-60519	E4400-60519	E4400-60519
<b>Slot 5</b> A12 CPU <a href="#">page 3-14</a>	E4400-60500	E4400-60500	E4400-60500

Assembly	Option 501, 502, 503, 504 and Option 300	Option UNJ	Option 506
<b>Side-SLOT 1<sup>c</sup></b> A13 Output (Option 501, 502, 503, 504 or Option UNJ) <a href="#">page 3-14</a> or A13 Output (Option UNB) <a href="#">page 3-14</a>	E4400-60659	E4400-60659	N/A
	E4400-60659	E4400-60659	E4400-60659 used with <b>Side-SLOT 2</b>
<b>Side-SLOT 2</b> A14 Extended Frequency Output (Option 506) <sup>d</sup> <a href="#">page 3-14</a>	N/A	N/A	E4400-60718
<b>Side-SLOT 3</b> A15 Sampler (Option UNJ or Option 506) <a href="#">page 3-14</a>	N/A	E8251-60043	E8251-60043
<b>Side-SLOT 4</b> A16 Frac-N (Option UNJ or Option 506) <a href="#">page 3-14</a> or A17 Synthesizer (Option 501, 502, 503, 504) <a href="#">page 3-14</a>	N/A	E8251-60044	E8251-60044
	E4400-60520	N/A	N/A
<b>Side-SLOT 5</b> A18 Reference (Option 501, 502, 503, 504) <a href="#">page 3-14</a> or A18 Reference (Option 501, 502, 503, 504 with Option 1E5) <a href="#">page 3-14</a> or A18 Reference (Option UNJ or Option 506) <a href="#">page 3-14</a>	E4400-60512	N/A	N/A
	E4400-60513	N/A	N/A
	N/A	E4423-60500	E4423-60500
A19 Daughterboard <a href="#">page 3-14</a>	E4400-60719	E4400-60719	E4400-60719
A20 Downconverter (Option 501, 502, 503, 504 with Option 300) <a href="#">page 3-10</a>	E4400-60544	N/A	N/A
A21 YTO Driver (Option UNJ or Option 506) <a href="#">page 3-10</a>	N/A	E4423-60510	E4423-60510
A22 Coupler (Option 506) <a href="#">page 3-10</a>	N/A	N/A	0950-0098
A23 Motherboard <a href="#">page 3-10</a>	E4400-60504	E4400-60504	E4400-60504
A23BT1 Battery <a href="#">page 3-21</a>	1420-0356	1420-0356	1420-0356
A24 Line Module <a href="#">page 3-19</a>	E4400-60537	E4400-60537	E4400-60537
A25 Rear Panel Board <a href="#">page 3-19</a> or A26 Rear Panel LVDS Board (Option 1EM) and A27 Rear Panel SMB Board (Option 1EM) <a href="#">page 3-19</a>	E4400-60655	E4400-60655	E4400-60655
	E4400-60524 E4400-60525	E4400-60524 E4400-60525	E4400-60524 E4400-60525

Assembly	Option 501, 502, 503, 504 and Option 300	Option UNJ	Option 506
AT1 Electronic Attenuator (Option 501, 502, 503, 504 or Option UNJ) <a href="#">page 3-10</a> or AT1 High-Power Mechanical Attenuator (Option UNB) <sup>e</sup> and A28 Reverse Power Protection (Option 501, 502, 503, 504 or Option UNJ) <a href="#">page 3-10</a> A29 DC Blocking Capacitor (Option 506)	E4400-60515	E4400-60515	N/A
	33322-60014 08648-60025	33322-60014 08648-60025	33322-60020 used with A29 0955-1131
	N/A	N/A	
B1 Power Supply Fan <a href="#">page 3-10</a>	3160-4133	3160-4133	3160-4133
B2 Small Fan <a href="#">page 3-10</a>	3160-4135	3160-4135	3160-4135
B3 Daughterboard/Card Cage Fan <a href="#">page 3-10</a>	3160-4133	3160-4133	3160-4133
RF Output Connector <a href="#">page 3-16</a>	E4400-60536	E4400-60536	E4400-60536

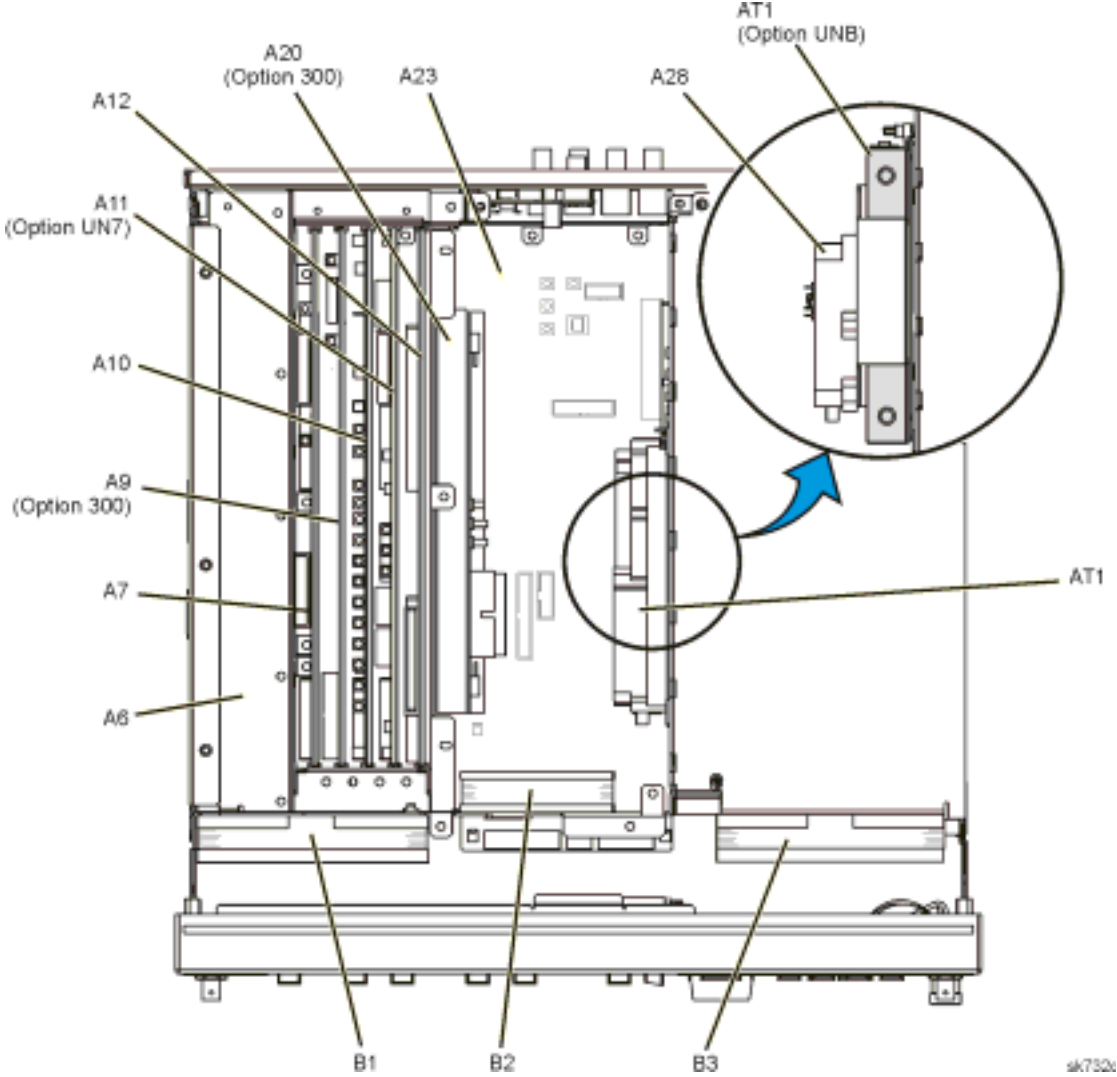
- a. Slot numbers one through five (where one is on the left) refer to the position of an assembly in the top card-cage of the signal generator when viewed from the top.
- b. A11 Internal Bit Error Rate Analyzer (Option UN7) is used with the A9 Digital Demodulator (Option 501, 502, 503, 504 with Option 300) and A20 Downconverter (Option 501, 502, 503, 504 with Option 300) for GSM/EDGE basestation loopback BER test capability (it also requires Option 001/601 or 002/602 and Option 402).
- c. Side-SLOT numbers one through five (where one is on top) refer to the position of an assembly in the right-hand side card-cage of the signal generator.
- d. A13 Output (Option UNB) is used together with A14 Extended Frequency Output (Option 506).
- e. AT1 High-Power Mechanical Attenuator (Option UNB) is used together with either an A28 Reverse Power Protection (Option 501, 502, 503, 504 or Option UNJ) or with an A29 DC Blocking Capacitor (Option 506).

## Assemblies from a Top View (Option 501, 502, 503, 504, Option 300, Option UNJ, or Option 506)

Assembly	Option 501, 502, 503, 504 and Option 300	Option UNJ	Option 506
A6 Power Supply	0950-4031	0950-4031	0950-4031
<b>Slot 1</b> A7 Baseband Generator (Option 001/601) or A7 Baseband Generator (Option 002)	E4400-60539	E4400-60539	E4400-60539
	E4400-60501	E4400-60501	E4400-60501
<b>Slot 2</b> A9 Digital Demodulator (Option 501, 502, 503, 504 with Option 300)	E4400-60510	N/A	N/A
<b>Slot 3</b> A10 I/Q Multiplexer (Option 501, 502, 503, 504 or Option UNJ) or A10 I/Q Multiplexer (Option 506)	E4400-60191	E4400-60191	N/A
	N/A	N/A	E8251-60063
<b>Slot 4</b> A11 Internal Bit Error Rate Analyzer (Option UN7)	E4400-60519	E4400-60519	E4400-60519
<b>Slot 5</b> A12 CPU	E4400-60500	E4400-60500	E4400-60500
A20 Downconverter (Option 501, 502, 503, 504 with Option 300)	E4400-60544	N/A	N/A
A21 YTO Driver (Option UNJ or Option 506)	N/A	E4423-60510	E4423-60510
A22 Coupler (Option 506)	N/A	N/A	0950-0098
A23 Motherboard	E4400-60504	E4400-60504	E4400-60504
AT1 Electronic Attenuator (Option 501, 502, 503, 504 or Option UNJ) or AT1 High-Power Mechanical Attenuator (Option UNB) and A28 Reverse Power Protection (Option 501, 502, 503, 504 or Option UNJ)	E4400-60515	E4400-60515	N/A
	33322-60020 08648-60025	33322-60020 08648-60025	33322-60020 used with A29 0955-1131
A29 DC Blocking Capacitor (Option 506)	N/A	N/A	
B1 Power Supply Fan or B3 Daughterboard/Card Cage Fan	3160-4133	3160-4133	3160-4133
B2 Small Fan	3160-4135	3160-4135	3160-4135

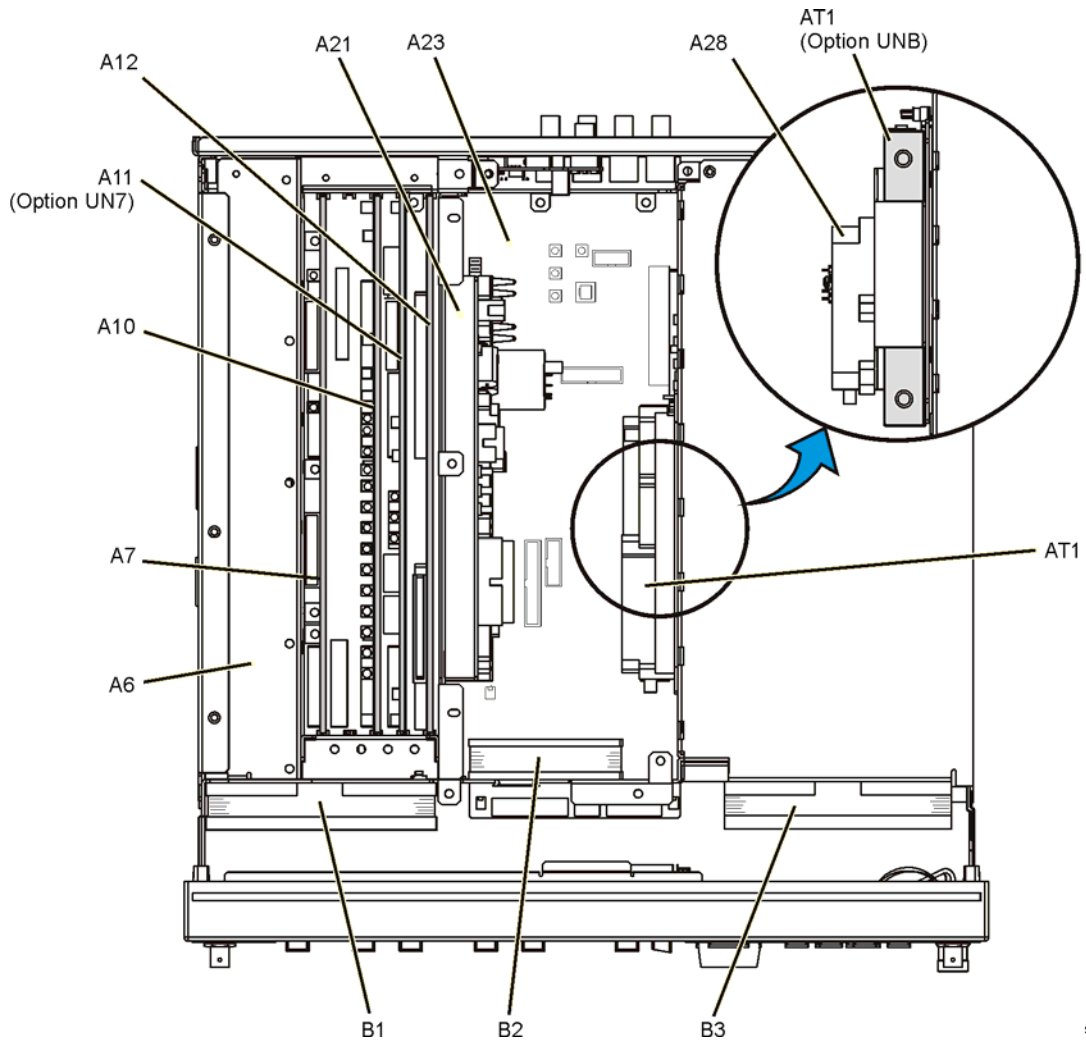


**Figure 3-2 Assemblies from a Top View (Option 501, 502, 503, 504 or Option 300)**



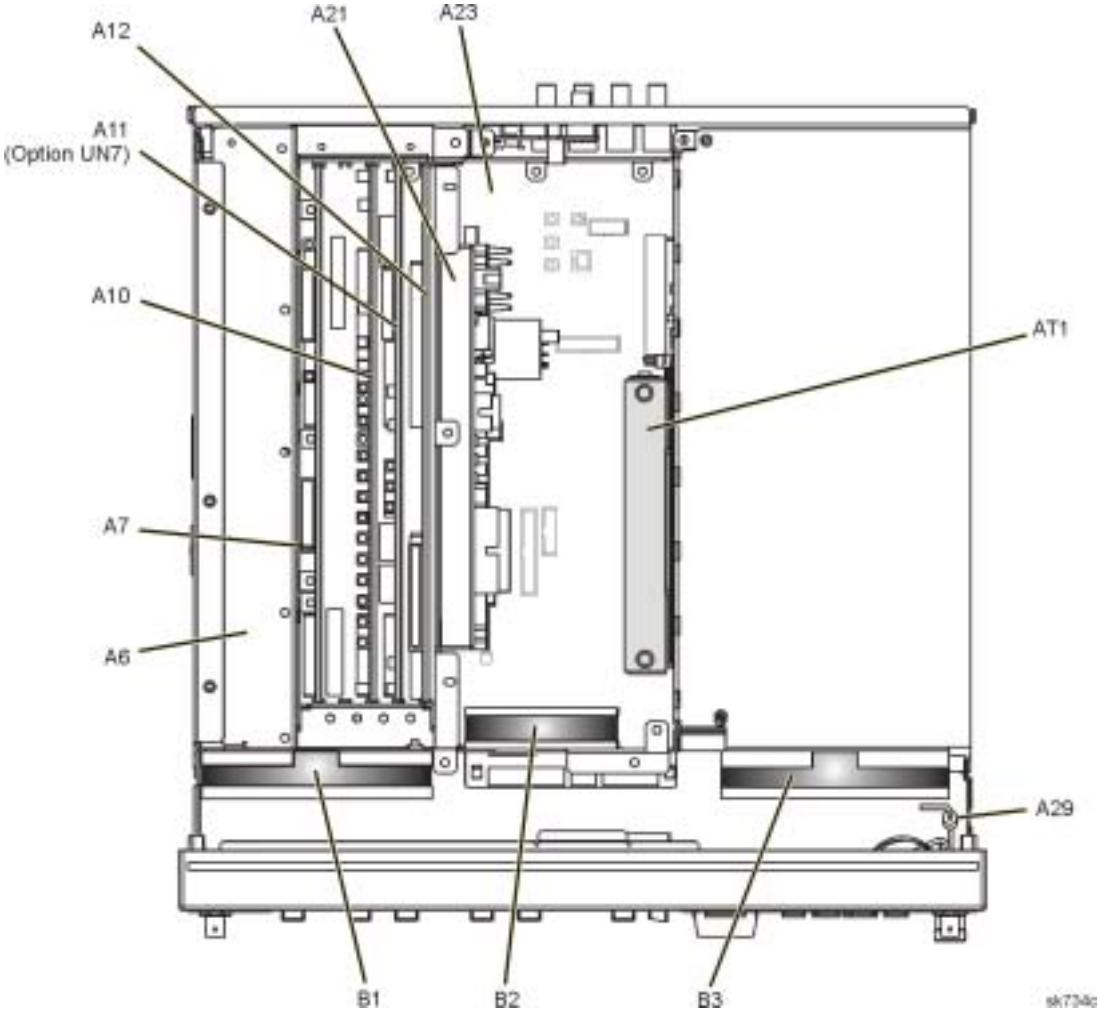
sk732i

**Figure 3-3 Assemblies from a Top View (Option UNJ)**



sk733c

**Figure 3-4 Assemblies from a Top View (Option 506)**



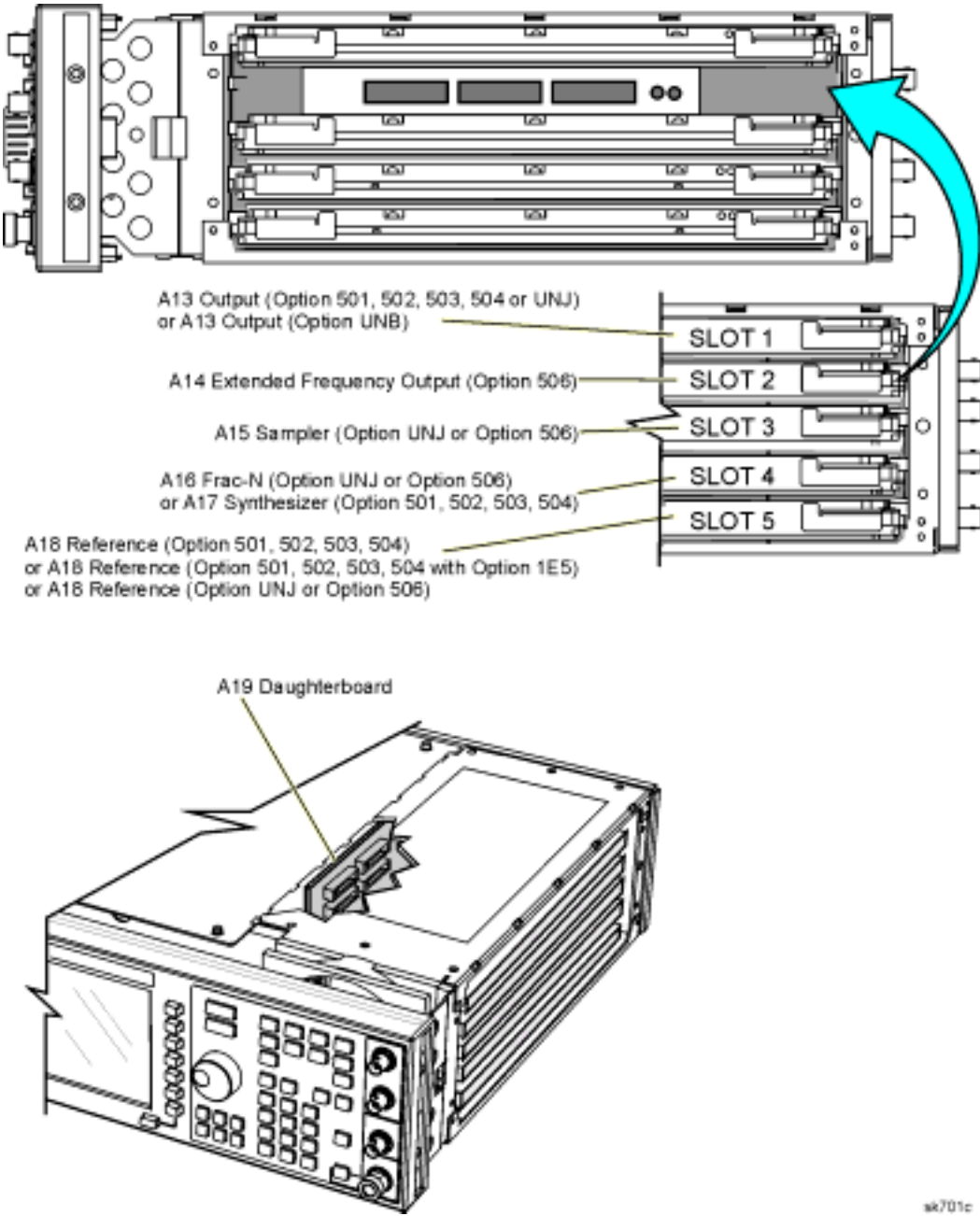
sk734c

## Assemblies from a Right-Side View (Option 501, 502, 503, 504, Option 300, Option UNJ, or Option 506)

Assembly	Option 501, 502, 503, 504 and Option 300	Option UNJ	Option 506
<b>Side-SLOT 1<sup>a</sup></b> A13 Output (Option 501, 502, 503, 504 or Option UNJ) or A13 Output (Option UNB)	E4400-60659	E4400-60659	N/A
	E4400-60659	E4400-60659	E4400-60659 also requires <b>Side-SLOT 2</b>
<b>Side-SLOT 2</b> A14 Extended Frequency Output (Option 506) <sup>b</sup>	N/A	N/A	E4400-60718
<b>Side-SLOT 3</b> A15 Sampler (Option UNJ or Option 506)	N/A	E8251-60043	E8251-60043
<b>Side-SLOT 4</b> A16 Frac-N (Option UNJ or Option 506) or A17 Synthesizer (Option 501, 502, 503, 504)	N/A	E8251-60044	E8251-60044
	E4400-60520	N/A	N/A
<b>Side-SLOT 5</b> A18 Reference (Option 501, 502, 503, 504) or A18 Reference (Option 501, 502, 503, 504 with Option 1E5) or A18 Reference (Option UNJ or Option 506)	E4400-60512	N/A	N/A
	E4400-60513	N/A	N/A
	N/A	E4423-60500	E4423-60500
A19 Daughterboard	E4400-60719	E4400-60719	E4400-60719

- a. Side-SLOT numbers one through five (where one is on top) refer to the position of an assembly in the right-hand side card-cage of the signal generator.
- b. A13 Output (Option UNB) is used together with A14 Extended Frequency Output (Option 506).

**Figure 3-5 Assemblies from a Right-Side View  
(Option 501, 502, 503, 504, Option 300, Option UNJ, or Option 506)**



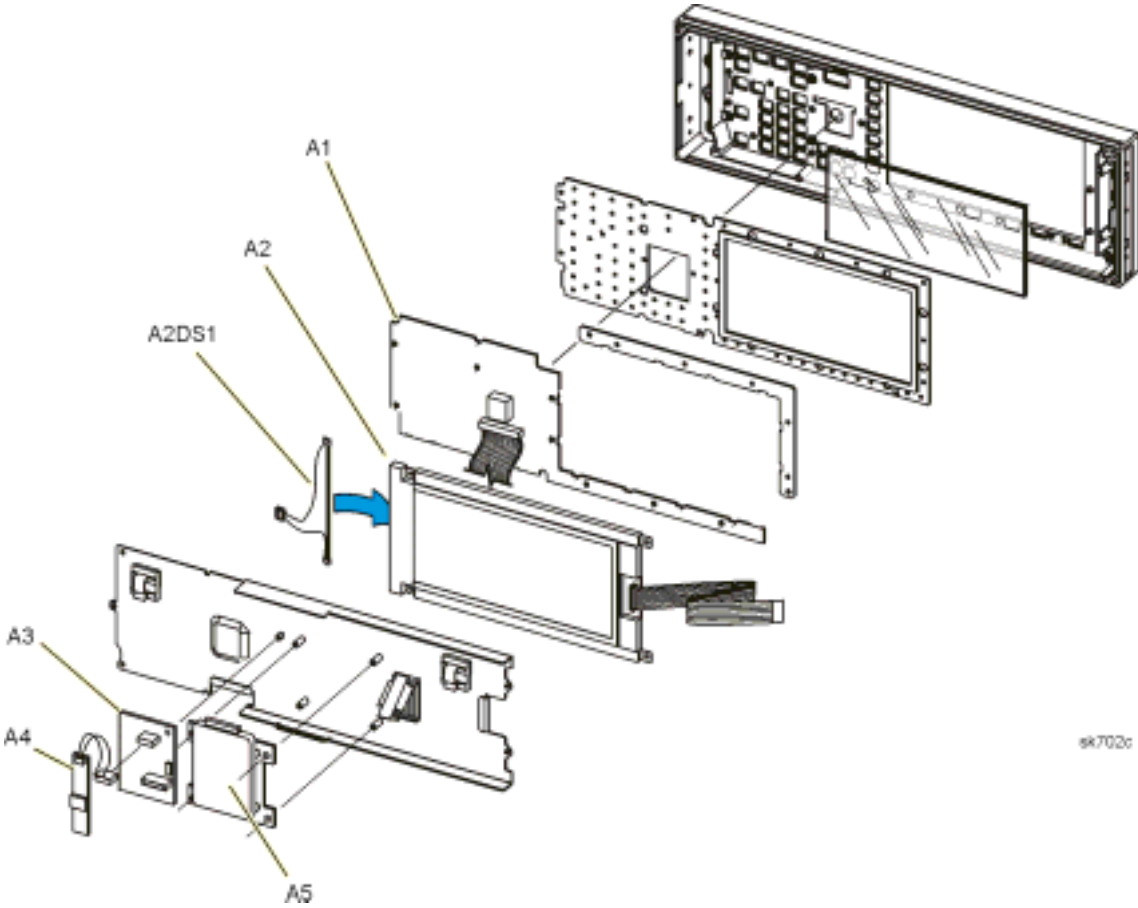
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## **Assemblies of the Front Panel (Option 501, 502, 503, 504, Option 300, Option UNJ, or Option 506)**

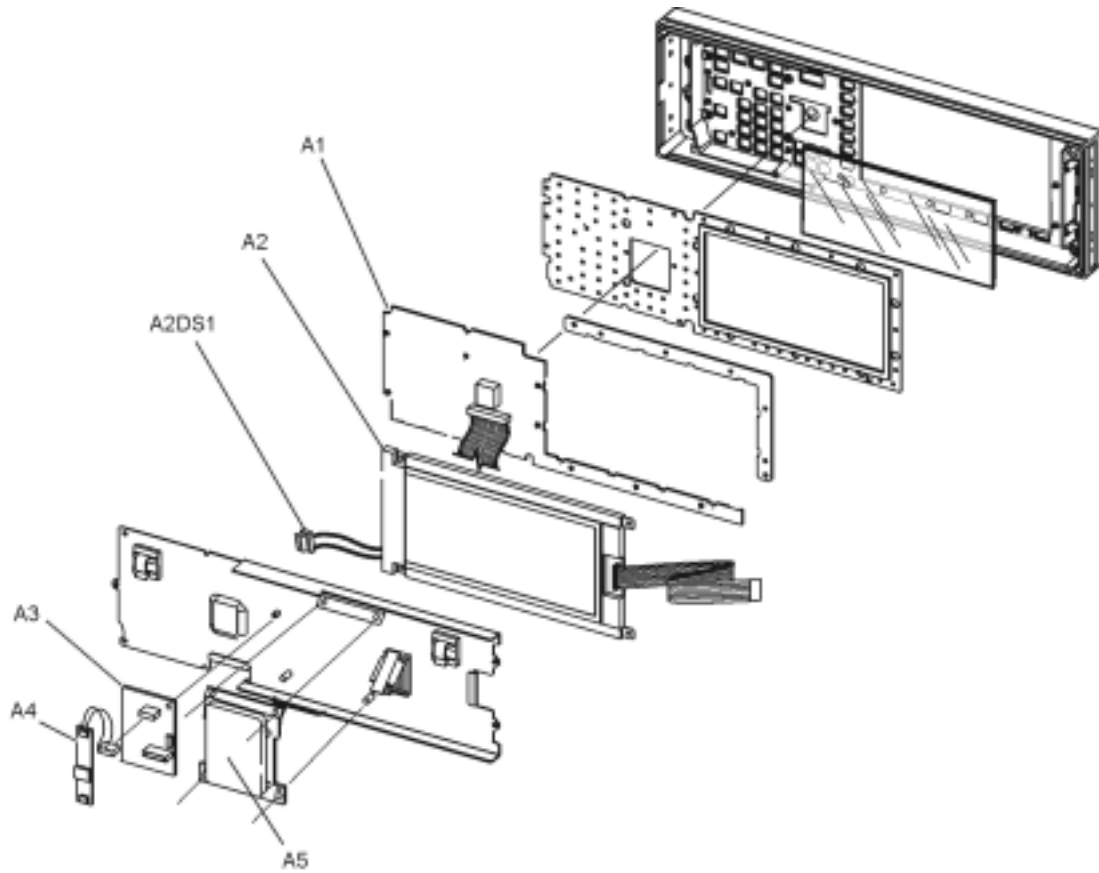
**Table 3-1**

<b>Assembly</b>	<b>Option 501, 502, 503, 504 and Option 300</b>	<b>Option UNJ</b>	<b>Option 506</b>
A1 Keyboard	E4400-60502	E4400-60502	E4400-60502
A2 Display	2090-0375	2090-0375	2090-0375
A2DS1 Display Backlight	1513-5204	1513-5204	1513-5204
A3 Front Panel Auxiliary Board	E4400-60503	E4400-60503	E4400-60503
A4 Inverter	0950-3757	0950-3757	0950-3757
A5 Hard Drive (Option 005)	E4400-60253	E4400-60253	E4400-60253

**Figure 3-6** Assemblies of the Front Panel, for instruments with serial prefixes <US4426/MY4426 (Option 501, 502, 503, 504, Option 300, Option UNJ, or Option 506)



**Figure 3-7** Assemblies of the Front Panel, for instruments with serial prefix  $\geq$ US4426/MY4426 and greater (Option 501, 502, 503, 504, Option 300, Option UNJ, or Option 506)





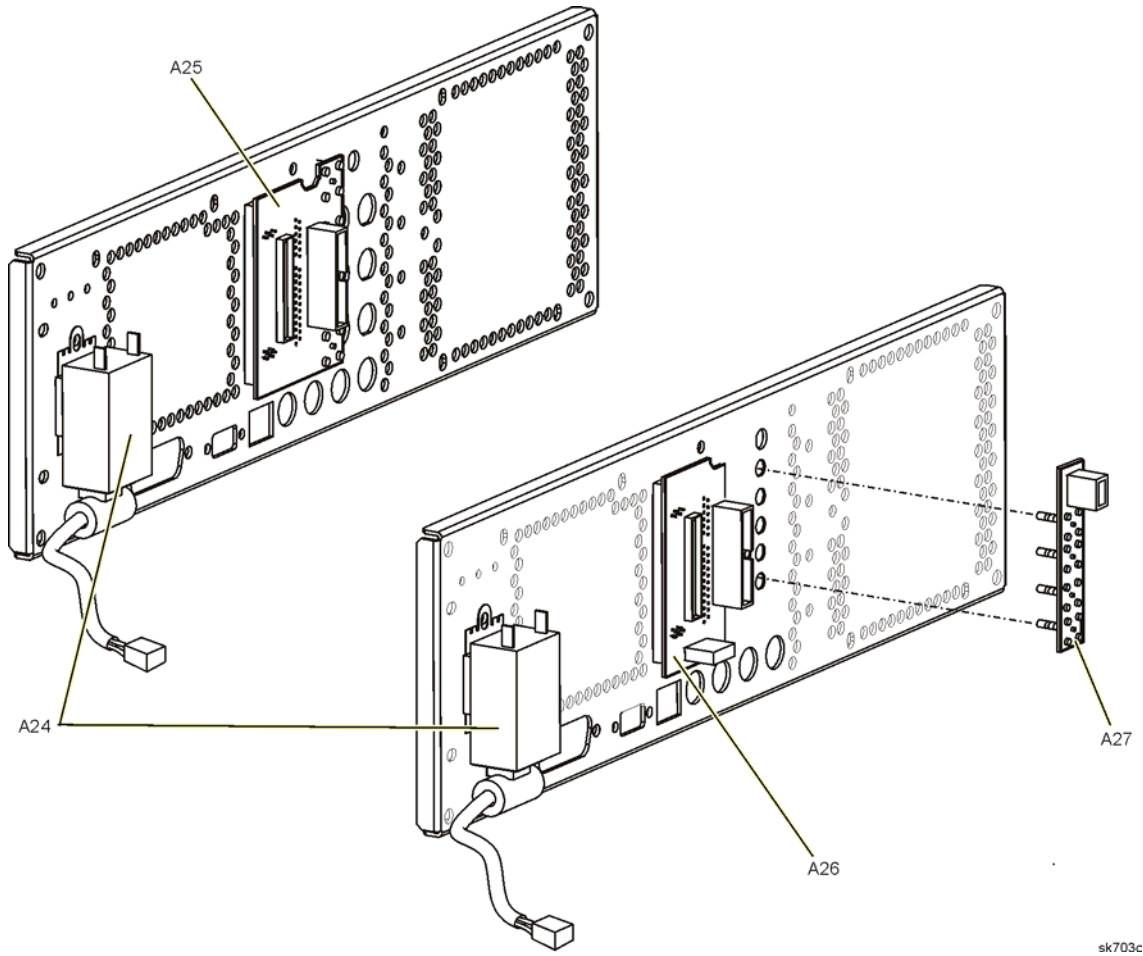
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**Assemblies of the Inside Rear Panel  
(Option 501, 502, 503, 504, Option 300, Option UNJ, Option 506,  
or Option 1EM)**

**Table 3-2**

<b>Assembly</b>	<b>Option 501, 502, 503, 504 and Option 300</b>	<b>Option UNJ</b>	<b>Option 506</b>
A24 Line Module	E4400-60537	E4400-60537	E4400-60537
A25 Rear Panel Board or A26 Rear Panel LVDS Board (Option 1EM) and A27 Rear Panel SMB Board (Option 1EM)	E4400-60655	E4400-60655	E4400-60655
	E4400-60524	E4400-60524	E4400-60524
	E4400-60525	E4400-60525	E4400-60525

**Figure 3-8**      **Assemblies of the Inside Rear Panel**  
**(Option 501, 502, 503, 504, Option 300, Option UNJ, Option 506, or Option 1EM)**

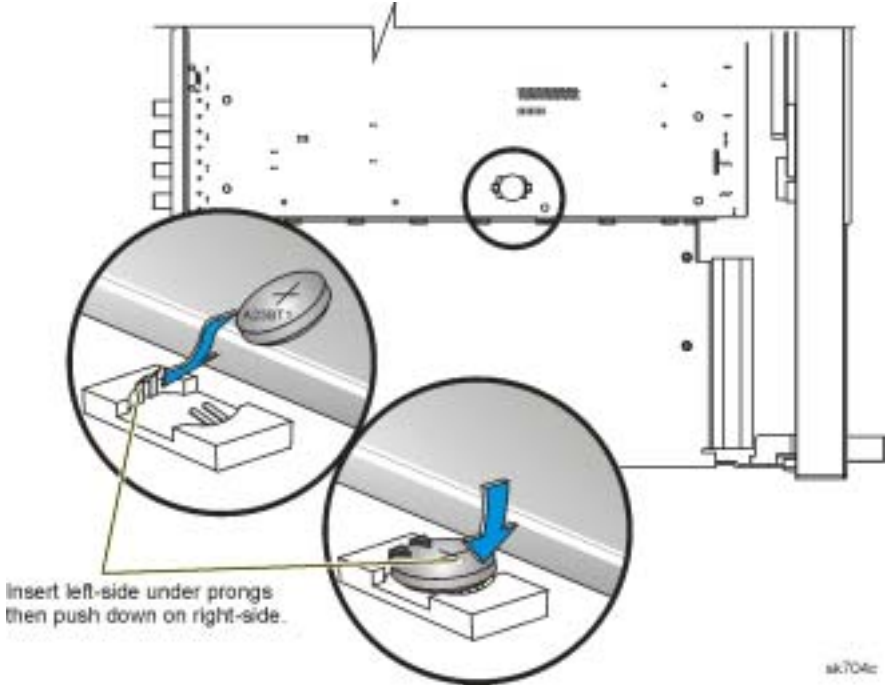


# A23BT1 Battery (Option 501, 502, 503, 504, Option 300, Option UNJ, or Option 506)

Table 3-3

Assembly	Option 501, 502, 503, 504 and Option 300	Option UNJ	Option 506
A23BT1 Battery	1420-0356	1420-0356	1420-0356

Figure 3-9 A23BT1 Battery  
(Option 501, 502, 503, 504, Option 300, Option UNJ, or Option 506)



## Cables

This section lists part numbers for the cables in your signal generator, in the order shown.

### **“Cables for the Inside Front Panel and Fans (B1, B2, and B3)” on page 3-24**

#### **Option 501, 502, 503, 504 Cables**

- “Option 501, 502, 503, 504 - Overall Cables (also includes cables for Option UN7, 300, 005, and 001/601 or 002/602)” on page 3-25
- “Option 501, 502, 503, 504 - Front Panel Cables” on page 3-27
- “Option 501, 502, 503, 504 - Standard Cables (does not include cables for the front panel, Option UN7, Option 300, or Option 005)” on page 3-28
- “Option 501, 502, 503, 504 - Only Option UN7, 300, 005, and 001/601 or 002/602 Cables” on page 3-29
- “Option 501, 502, 503, 504 - Semi-Rigid Cables includes the AT1 Electronic Attenuator (Option 501, 502, 503, 504 or Option UNJ) or the AT1 High-Power Mechanical Attenuator (Option UNB) with the A28 Reverse Power Protection (Option 501, 502, 503, 504 or Option UNJ)” on page 3-30
- “Option 501, 502, 503, 504 - Option 1EM Overall Cables (also includes cables for Option UN7, 300, 005, and 001/601 or 002/602)” on page 3-31
- “Option 501, 502, 503, 504 - Option 1EM Semi-Rigid Cables includes the AT1 Electronic Attenuator (Option 501, 502, 503, 504 or Option UNJ) or the AT1 High-Power Mechanical Attenuator (Option UNB) with the A28 Reverse Power Protection (Option 501, 502, 503, 504 or Option UNJ)” on page 3-33

#### **Option UNJ Cables**

- “Option UNJ - Overall Cables (also includes cables for Option UN7, 005, and 001/601 or 002/602)” on page 3-34
- “Option UNJ - Front Panel Cables” on page 3-36
- “Option UNJ - Standard Cables (does not include cables for the front panel, Option UN7, or Option 005)” on page 3-37
- “Option UNJ - Only Option UN7, 005, and 001/601 or 002/602 Cables” on page 3-38
- “Option UNJ - Semi-Rigid Cables includes the A21 YTO Driver (Option UNJ or Option 506) and the AT1 Electronic Attenuator (Option 501, 502, 503, 504 or Option UNJ) or the AT1 High-Power Mechanical Attenuator (Option UNB) with A28 Reverse Power Protection (Option 501, 502, 503, 504 or Option UNJ)” on page 3-39
- “Option UNJ - Option 1EM Overall Cables (also includes cables for Option UN7, 005, and 001/601 or 002/602)” on page 3-41
- “Option UNJ - Option 1EM Semi-Rigid Cables includes the A21 YTO Driver (Option UNJ or Option 506) and the AT1 Electronic Attenuator (Option 501, 502, 503, 504 or Option UNJ) or the AT1 High-Power Mechanical Attenuator (Option UNB) with A28 Reverse Power Protection (Option 501, 502, 503, 504 or Option UNJ)” on page 3-44

## Option 506 Cables

- “Option 506 - Overall Cables (also includes cables for Option UN7, 005, and 001/601 or 002/602)” on page 3-46
- “Option 506 - Front Panel Cables” on page 3-49
- “Option 506 - Standard Cables (does not include cables for the front panel, Option UN7, or Option 005)” on page 3-50
- “Option 506 - Only Option UN7, 005, and 001/601 or 002/602 Cables” on page 3-52
- “Option 506 - Semi-Rigid Cables includes the A21 YTO Driver (Option UNJ or Option 506) and the AT1 High-Power Mechanical Attenuator (Option UNB) with an A29 DC Blocking Capacitor (Option 506)” on page 3-53
- “Option 506 - Option 1EM Overall Cables (also includes cables for Option UN7, 005, and 001/601 or 002/602)” on page 3-55
- “Option 506 - Option 1EM Semi-Rigid Cables includes the A21 YTO Driver (Option UNJ or Option 506) and the AT1 High-Power Mechanical Attenuator (Option UNB) with an A29 DC Blocking Capacitor (Option 506)” on page 3-58

## Cables for the Inside Front Panel and Fans (B1, B2, and B3)

**Table 3-4**

Reference Designator	Input	Output	Part Number
W1	I INPUT of front panel	A10J7	8121-0642
W2	Q INPUT of front panel	A10J8	8121-0642
W3	DATA of front panel	A21J21	8121-0661
W4	DATA CLOCK of front panel	A23J23	8121-0661
W5	SYMBOL SYNC of front panel	A23J24	8121-0661
W6 <sup>a</sup>	EXT 1 INPUT of front panel	EXT1 on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0644
W7 <sup>a</sup>	EXT 2 INPUT of front panel	EXT2 on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0644
W8	LF OUTPUT of front panel	LF OUT A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0657
W26 (Option 005)	A5 10 GB Hard Drive	A12P3	8121-0685
W33	A2 Display	A3J1	8121-0689
W34	CN1 (top-side) of A4 Inverter	A3P2	8121-0687
W35	A3 Front Panel Auxilary Board	A23J3	8121-0686
W36 <sup>b</sup>	A1 Keyboard	A28J32	part of A1
A2DS1W1 <sup>b</sup>	wire cable, A2DS1 Display Backlight	CN2 (bottom-side) of the A4 Inverter	part of A2
B1W1 <sup>b</sup>	wire cable, B1 Power Supply Fan	A23J33	part of B1
B2W1 <sup>b</sup>	wire cable, B2 Small Fan	A23J27	part of B2
B3W1 <sup>b</sup>	wire cable, B3 Daughterboard/Card Cage Fan	A23J29	part of B3

- a. When comparing cables W6 and W7 between a model with Option 506 and a model with either Option 501, 502, 503, 504, or UNJ, notice that cables W6 and W7 are connected to different (swapped) ports of the A19 Daughterboard.
- b. This cable is not replaceable by itself; it is part of an assembly.

## Cables for Option 501, 502, 503, 504 Signal Generators

### Option 501, 502, 503, 504 - Overall Cables (also includes cables for Option UN7, 300, 005, and 001/601 or 002/602)

Table 3-5

Reference Designator	Input	Output	Part Number
W1	I INPUT of front panel	A10J7	8121-0642
W2	Q INPUT of front panel	A10J8	8121-0642
W3	DATA of front panel	A21J21	8121-0661
W4	DATA CLOCK of front panel	A23J23	8121-0661
W5	SYMBOL SYNC of front panel	A23J24	8121-0661
W6 <sup>a</sup>	EXT 1 INPUT of front panel	EXT1 on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0644
W7 <sup>a</sup>	EXT 2 INPUT of front panel	EXT2 on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0644
W8	LF OUTPUT of front panel	LF OUT A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0657
W10 (Option 001/601 or 002/602)	A7J13	A9J3 (Option 501, 502, 503, 504 with Option 300) or A11J3 (Option UN7) or A25P2	8121-0679
W12 (Option 001/601 or 002/602)	A7J800	BASEBAND GEN REF IN of the rear panel	8121-0643
W13 (Option 300)	A9J4	A20 Downconverter	8121-0651
W14 (Option 300)	A9J15	A20 Downconverter	8121-0651
W15	A10J14	Q bar OUT of the rear panel	8121-0640
W16	A10J13	I bar OUT of the rear panel	8121-0640
W17	A10J12	Q OUT of the rear panel	8121-0640
W18	A10J11	I OUT of the rear panel	8121-0640

**Table 3-5**

<b>Reference Designator</b>	<b>Input</b>	<b>Output</b>	<b>Part Number</b>
W19	A10J10	Q INT LOW on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0641
W20	A10J9	I INT on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0641
W23 (Option UN7)	A11J4	BER DATA IN of the rear panel	8121-0659
W24 (Option UN7)	A11J5	BER CLK IN of the rear panel	8121-0659
W25 (Option UN7)	A11J6	BER GATE IN of the rear panel	8121-0659
W26 (Option 005)	ribbon cable, A5 10 GB Hard Drive	A12P3	8121-0685
W31	A23J20	SWEEP OUT of the rear panel	8121-0658
W32	A23J36	A25P4	8121-0678
W40	ribbon cable, AT1 Electronic Attenuator	A23J25	8121-6837
W43	semi-rigid cable, output of A13 Output	input of AT1 Electronic Attenuator - passes through the A19 Daughterboard	E4400-60625
W63 (Option 300)	ribbon cable, A20 Downconverter	A23J26	8121-0682
W64 (Option 300)	flexible cable, A20 Downconverter	321.4 IN of the rear panel	8121-0650
W92 (Option 001/601 or 002/602)	A7J21	A25P1	8121-0690

- a. When comparing cables W6 and W7 between a model with Option 506 and a model with either Option 501, 502, 503, 504, or UNJ, notice that cables W6 and W7 are connected to different (swapped) ports of the A19 Daughterboard.



## Option 501, 502, 503, 504 - Front Panel Cables

**Table 3-6**

<b>Reference Designator</b>	<b>Input</b>	<b>Output</b>	<b>Part Number</b>
W1	I INPUT of front panel	A10J7	8121-0642
W2	Q INPUT of front panel	A10J8	8121-0642
W3	DATA of front panel	A21J21	8121-0661
W4	DATA CLOCK of front panel	A23J23	8121-0661
W5	SYMBOL SYNC of front panel	A23J24	8121-0661
W6 <sup>a</sup>	EXT 1 INPUT of front panel	EXT1 on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0644
W7 <sup>a</sup>	EXT 2 INPUT of front panel	EXT2 on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0644
W8	LF OUTPUT of front panel	LF OUT A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0657

- a. When comparing cables W6 and W7 between a model with Option 506 and a model with either Option 501, 502, 503, 504, or UNJ, notice that cables W6 and W7 are connected to different (swapped) ports of the A19 Daughterboard.

**Option 501, 502, 503, 504 - Standard Cables  
(does not include cables for the front panel,  
Option UN7, Option 300, or Option 005)**

**Table 3-7**

<b>Reference Designator</b>	<b>Input</b>	<b>Output</b>	<b>Part Number</b>
W10 (Option 001/601 or 002/602)	A7J13	A9J3 (Option 501, 502, 503, 504 with Option 300) or A11J3 (Option UN7) or A25P2	8121-0679
W12 (Option 001/601 or 002/602)	A7J800	BASEBAND GEN REF IN of the rear panel	8121-0643
W15	A10J14	Q bar OUT of the rear panel	8121-0640
W16	A10J13	I bar OUT of the rear panel	8121-0640
W17	A10J12	Q OUT of the rear panel	8121-0640
W18	A10J11	I OUT of the rear panel	8121-0640
W19	A10J10	Q INT LOW on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0641
W20	A10J9	I INT on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0641
W31	A23J20	SWEEP OUT of the rear panel	8121-0658
W32	A23J36	A25P4	8121-0678
W40	ribbon cable, AT1 Electronic Attenuator	A23J25	8121-6837
W43	semi-rigid cable, output of A13 Output	input of AT1 Electronic Attenuator - passes through the A19 Daughterboard	E4400-60625
W92 (Option 001/601 or 002/602)	A7J21	A25P1	8121-0690

**Option 501, 502, 503, 504 - Only Option UN7, 300, 005, and 001/601 or 002/602 Cables**

**Table 3-8**

<b>Reference Designator</b>	<b>Input</b>	<b>Output</b>	<b>Part Number</b>
W10 (Option 001/601 or 002/602)	A7J13	A9J3 (Option 501, 502, 503, 504 with Option 300) or A11J3 (Option UN7) or A25P2	8121-0679
W12 (Option 001/601 or 002/602)	A7J800	BASEBAND GEN REF IN of the rear panel	8121-0643
W13 (Option 300)	A9J4	A20 Downconverter	8121-0651
W14 (Option 300)	A9J15	A20 Downconverter	8121-0651
W23 (Option UN7)	A11J4	BER DATA IN of the rear panel	8121-0659
W24 (Option UN7)	A11J5	BER CLK IN of the rear panel	8121-0659
W25 (Option UN7)	A11J6	BER GATE IN of the rear panel	8121-0659
W26 (Option 005)	ribbon cable, A5 10 GB Hard Drive	A12P3	8121-0685
W63 (Option 300)	ribbon cable, A20 Downconverter	A23J26	8121-0682
W64 (Option 300)	flexible cable, A20 Downconverter	321.4 IN of the rear panel	8121-0650
W92 (Option 001/601 or 002/602)	A7J21	A25P1	8121-0690

**Option 501, 502, 503, 504 - Semi-Rigid Cables**  
**includes the AT1 Electronic Attenuator (Option 501, 502, 503, 504 or Option UNJ)**  
**or the AT1 High-Power Mechanical Attenuator (Option UNB)**  
**with the A28 Reverse Power Protection (Option 501, 502, 503, 504 or Option UNJ)**

**Table 3-9**

Reference Designator	Input	Output	Part Number
W9	semi-rigid cable, output of AT1 Electronic Attenuator	RF OUTPUT of the front panel	E4400-20554
W31	A23J20	SWEEP OUT of the rear panel	8121-0658
W32	A23J36	A25P4	8121-0678
W40	ribbon cable, AT1 Electronic Attenuator	A23J25	8121-6837
W41	semi-rigid cable, output of A17 Synthesizer	A13 Output - passes through the A19 Daughterboard	E4400-60623
W42	semi-rigid cable, output of A13 Output	COH CARRIER of the rear panel - passes through A19 Daughterboard	E4400-60624
W43	semi-rigid cable, output of A13 Output	input of AT1 Electronic Attenuator - passes through A19 Daughterboard	E4400-60625
W47 (Option UNB & A28)	semi-rigid cable, output of A13 Output	input of AT1 High-Power Mechanical Attenuator - passes through A19 Daughterboard	E4400-60627
W48 (Option UNB & A28)	semi-rigid cable, AT1 High-Power Mechanical Attenuator	input of A28 Reverse Power Protection	E4400-20550
W49 (Option UNB & A28)	semi -rigid cable, A28 Reverse Power Protection	RF OUTPUT of the rear panel	E4400-20551
W53 (Option UNB & A28)	wire-harness, control lines of the A28 Reverse Power Protection	A23J22	08647-61028
W57 (Option UNB & A28)	ribbon cable, AT1 High-Power Mechanical Attenuator	A23J25	E4400-20132

**Option 501, 502, 503, 504 - Option 1EM Overall Cables  
(also includes cables for Option UN7, 300, 005, and 001/601 or 002/602)**

**Table 3-10**

<b>Reference Designator</b>	<b>Input</b>	<b>Output</b>	<b>Part Number</b>
W10 (Option 001/601 or 002/602)	A7J13	A9J3 (Option 501, 502, 503, 504 with Option 300) or A11J3 (Option UN7) or A25P2	8121-0679
W13 (Option 300)	A9J4	A20 Downconverter	8121-0651
W14 (Option 300)	A9J15	A20 Downconverter	8121-0651
W19	A10J10	Q INT LOW on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0641
W20	A10J9	I INT on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0641
W23 (Option UN7)	A11J4	BER DATA IN of the rear panel	8121-0659
W24 (Option UN7)	A11J5	BER CLK IN of the rear panel	8121-0659
W25 (Option UN7)	A11J6	BER GATE IN of the rear panel	8121-0659
W26 (Option 005)	ribbon cable, A5 10 GB Hard Drive	A12P3	8121-0685
W32	A23J36	A25P4	8121-0678
W40	ribbon cable, AT1 Electronic Attenuator	A23J25	8121-6837
W43	semi-rigid cable, output of A13 Output	input of AT1 Electronic Attenuator - passed through the A19 Daughterboard	E4400-60625
W63 (Option 300)	ribbon cable, A20 Downconverter	A23J26	8121-0682
W64 (Option 300)	flexible cable, A20 Downconverter	321.4 IN of the rear panel	8121-0650
W70 <sup>a</sup> (Option 1EM)	EXT 1 of the rear panel	EXT 1 of A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0654

**Table 3-10**

<b>Reference Designator</b>	<b>Input</b>	<b>Output</b>	<b>Part Number</b>
W71 <sup>a</sup> (Option 1EM)	EXT 2 of the rear panel	EXT 2 of A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0654
W72 (Option 1EM)	I IN of the rear panel	A10J7	8121-0652
W73 (Option 1EM)	Q IN of the rear panel	A10J8	8121-0652
W74 (Option 1EM)	A10J11	I OUT of the rear panel	8121-0653
W75 (Option 1EM)	A10J12	Q OUT of the rear panel	8121-0653
W76 (Option 1EM)	A10J13	I bar OUT of the rear panel	8121-0653
W77 (Option 1EM)	A10J14	Q bar OUT of the rear panel	8121-0653
W78 (Option 1EM and Option 001/601 or 002/602)	A7J800	BASEBAND GEN REF IN of the rear panel	8121-0655
W79 (Option 1EM)	A23J20	SWP OUT of the rear panel	8121-0660
W80 (Option 1EM)	DATA IN of the rear panel	A23J21	8121-0660
W81 (Option 1EM)	DATA CLK IN of the rear panel	A23J23	8121-0660
W82 (Option 1EM)	SYMBOL SYNC IN of the rear panel	A23J24	8121-0660
W83 (Option 1EM)	flexible cable, A19 Daughterboard	LF OUT of the rear panel	8121-0656
W92 (Option 001/601 or 002/602)	A7J21	A25P1	8121-0690

a. When comparing cables W70 and W71 between a model with Option 506 and a model with either Option 501, 502, 503, 504, or UNJ, notice that cables W70 and W71 are connected to different (swapped) ports of the A19 Daughterboard.

**Option 501, 502, 503, 504 - Option 1EM Semi-Rigid Cables includes the AT1 Electronic Attenuator (Option 501, 502, 503, 504 or Option UNJ) or the AT1 High-Power Mechanical Attenuator (Option UNB) with the A28 Reverse Power Protection (Option 501, 502, 503, 504 or Option UNJ)**

**Table 3-11**

<b>Reference Designator</b>	<b>Input</b>	<b>Output</b>	<b>Part Number</b>
W32	A23J36	A25P4	8121-0678
W40	ribbon cable, AT1 Electronic Attenuator	A23J25	8121-6837
W41	semi-rigid cable, output of A17 Synthesizer	A13 Output - passes through the A19 Daughterboard	E4400-60623
W43	semi-rigid cable, output of A13 Output	input of AT1 Electronic Attenuator - passes through the A19 Daughterboard	E4400-60625
W79 (Option 1EM)	A23J20	SWP OUT of the rear panel	8121-0660
W84 (Option 1EM)	semi-rigid cable, output of A13 Output	COH CARRIER of the rear panel - passes through A19 Daughterboard	E4400-60631
W85 (Option 1EM)	semi-rigid cable, output of AT1 Electronic Attenuator	RF OUTPUT of the rear panel	E4400-20555
W47 (Option UNB & A28)	semi-rigid cable, output of A13 Output	input of AT1 High-Power Mechanical Attenuator - passes through A19 Daughterboard	E4400-60627
W48 (Option UNB & A28)	semi-rigid cable, AT1 High-Power Mechanical Attenuator	input of A28 Reverse Power Protection	E4400-20550
W53 (Option UNB & A28)	wire-harness, control lines of the A28 Reverse Power Protection	A23J22	08647-61028
W57 (Option UNB & A28)	ribbon cable, AT1 High-Power Mechanical Attenuator	A23J25	E4400-20132
W84 (Option 1EM, Option UNB & A28)	semi-rigid cable, output of A13 Output	COH CARRIER of the rear panel - passes through A19 Daughterboard	E4400-60631
W46 (Option UNB & A28)	semi-rigid cable, A28 Reverse Power Protection	RF OUTPUT of the rear panel	8121-6837

## Cables for Option UNJ Signal Generators

### Option UNJ - Overall Cables

(also includes cables for Option UN7, 005, and 001/601 or 002/602)

**Table 3-12**

Reference Designator	Input	Output	Part Number
W1	I INPUT of front panel	A10J7	8121-0642
W2	Q INPUT of front panel	A10J8	8121-0642
W3	DATA of front panel	A21J21	8121-0661
W4	DATA CLOCK of front panel	A23J23	8121-0661
W5	SYMBOL SYNC of front panel	A23J24	8121-0661
W6 <sup>a</sup>	EXT 1 INPUT of front panel	EXT1 on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0644
W7 <sup>a</sup>	EXT 2 INPUT of front panel	EXT2 on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0644
W8	LF OUTPUT of front panel	LF OUT A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0657
W10 (Option 001/601 or 002/602)	A7J13	A9J3 (Option 501, 502, 503, 504 with Option 300) or A11J3 (Option UN7) or A25P2	8121-0679
W11	semi-rigid cable, output of A16 Frac-N	input of A15 Sampler - passes through A19 Daughterboard	E4400-60622
W12 (Option 001/601 or 002/602)	A7J800	BASEBAND GEN REF IN of the rear panel	8121-0643
W15	A10J14	Q bar OUT of the rear panel	8121-0640
W16	A10J13	I bar OUT of the rear panel	8121-0640
W17	A10J12	Q OUT of the rear panel	8121-0640
W18	A10J11	I OUT of the rear panel	8121-0640



**Table 3-12**

<b>Reference Designator</b>	<b>Input</b>	<b>Output</b>	<b>Part Number</b>
W19	A10J10	Q INT LOW on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0641
W20	A10J9	I INT on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0641
W23 (Option UN7)	A11J4	BER DATA IN of the rear panel	8121-0659
W24 (Option UN7)	A11J5	BER CLK IN of the rear panel	8121-0659
W25 (Option UN7)	A11J6	BER GATE IN of the rear panel	8121-0659
W26 (Option 005)	ribbon cable, A5 10 GB Hard Drive	A12P3	8121-0685
W27	A21J2	A23J34	8121-0644
W28	A21J3	YTO FM on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0645
W29	A21J4	PH LK on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0645
W30	A21J5	J1 of YTO Interface board	8121-0683
W31	A23J20	SWEEP OUT of rear panel	8121-0658
W32	A23J36	A25P4	8121-0678
W40	ribbon cable, AT1 Electronic Attenuator	A23J25	8121-6837
W43	semi-rigid cable, output of A13 Output	input of AT1 Electronic Attenuator - passes through the A19 Daughterboard	E4400-60625
W92 (Option 001/601 or 002/602)	A7J21	A25P1	8121-0690

- a. When comparing cables W6 and W7 between a model with Option 506 and a model with either Option 501, 502, 503, 504, or UNJ, notice that cables W6 and W7 are connected to different (swapped) ports of the A19 Daughterboard.

**Option UNJ - Front Panel Cables**

**Table 3-13**

<b>Reference Designator</b>	<b>Input</b>	<b>Output</b>	<b>Part Number</b>
W1	I INPUT of front panel	A10J7	8121-0642
W2	Q INPUT of front panel	A10J8	8121-0642
W3	DATA of front panel	A21J21	8121-0661
W4	DATA CLOCK of front panel	A23J23	8121-0661
W5	SYMBOL SYNC of front panel	A23J24	8121-0661
W6 <sup>a</sup>	EXT 1 INPUT of front panel	EXT1 on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0644
W7 <sup>a</sup>	EXT 2 INPUT of front panel	EXT2 on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0644
W8	LF OUTPUT of front panel	LF OUT A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0657

- a. When comparing cables W6 and W7 between a model with Option 506 and a model with either Option 501, 502, 503, 504, or UNJ, notice that cables W6 and W7 are connected to different (swapped) ports of the A19 Daughterboard.

**Option UNJ - Standard Cables**  
**(does not include cables for the front panel, Option UN7, or Option 005)**

**Table 3-14**

<b>Reference Designator</b>	<b>Input</b>	<b>Output</b>	<b>Part Number</b>
W10 (Option 001/601 or 002/602)	A7J13	A9J3 (Option 501, 502, 503, 504 with Option 300) or A11J3 (Option UN7) or A25P2	8121-0679
W11	semi-rigid cable, output of A16 Frac-N	input of A15 Sampler - passes through A19 Daughterboard	E4400-60622
W12 (Option 001/601 or 002/602)	A7J800	BASEBAND GEN REF IN of the rear panel	8121-0643
W15	A10J14	Q bar OUT of the rear panel	8121-0640
W16	A10J13	I bar OUT of the rear panel	8121-0640
W17	A10J12	Q OUT of the rear panel	8121-0640
W18	A10J11	I OUT of the rear panel	8121-0640
W19	A10J10	Q INT LOW on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0641
W20	A10J9	I INT on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0641
W31	A23J20	SWEEP OUT of rear panel	8121-0658
W32	A23J36	A25P4	8121-0678
W40	ribbon cable, AT1 Electronic Attenuator	A23J25	8121-6837
W43	semi-rigid cable, output of A13 Output	input of AT1 Electronic Attenuator - passes through the A19 Daughterboard	E4400-60625
W92 (Option 001/601 or 002/602)	A7J21	A25P1	8121-0690

**Option UNJ - Only Option UN7, 005, and 001/601 or 002/602 Cables**

**Table 3-15**

<b>Reference Designator</b>	<b>Input</b>	<b>Output</b>	<b>Part Number</b>
W10 (Option 001/601 or 002/602)	A7J13	A9J3 (Option 501, 502, 503, 504 with Option 300) or A11J3 (Option UN7) or A25P2	8121-0679
W12 (Option 001/601 or 002/602)	A7J800	BASEBAND GEN REF IN of the rear panel	8121-0643
W23 (Option UN7)	A11J4	BER DATA IN of the rear panel	8121-0659
W24 (Option UN7)	A11J5	BER CLK IN of the rear panel	8121-0659
W25 (Option UN7)	A11J6	BER GATE IN of the rear panel	8121-0659
W26 (Option 005)	ribbon cable, A5 10 GB Hard Drive	A12P3	8121-0685
W92 (Option 001/601 or 002/602)	A7J21	A25P1	8121-0690

**Option UNJ - Semi-Rigid Cables includes the A21 YTO Driver (Option UNJ or Option 506) and the AT1 Electronic Attenuator (Option 501, 502, 503, 504 or Option UNJ) or the AT1 High-Power Mechanical Attenuator (Option UNB) with A28 Reverse Power Protection (Option 501, 502, 503, 504 or Option UNJ)**

**Table 3-16**

Reference Designator	Input	Output	Part Number
W9	semi-rigid cable, output of AT1 Electronic Attenuator	RF OUPUT of the front panel	E4400-20554
W11	semi-rigid cable, output of the A16 Frac-N	input of the A15 Sampler - passes through the A19 Daughterboard	E4400-60622
W31	A23J20I	SWEEP OUT of the rear panel	8121-0658
W32	A23J36	A25P4	8121-0678
W37	semi-rigid cable, A15 Sampler - passes through the A19 Daughterboard	top connector of A21 YTO Driver	E4423-20510
W38	semi-rigid cable, A16 Frac-N - passes through the A19 Daughterboard	middle connector of A21 YTO Driver	E4423-20514
W39	semi-rigid cable, output of YTO	bottom connector of A21 YTO Driver	E4423-20513
W40	ribbon cable, AT1 Electronic Attenuator	A23J25	8121-6837
W41	semi-rigid cable, output of A16 Frac-N	A13 Output - passes through the A19 Daughterboard	E4400-60623
W42	semi-rigid cable, output of A13 Output	COH CARRIER of the rear panel - passes through the A19 Daughterboard	E4400-60624
W43	semi-rigid cable, output of A13 Output	input of AT1 Electronic Attenuator - passes through the A19 Daughterboard	E4400-60625
W44	semi-rigid cable, output of A18 Reference	A15 Sampler - passes through the A19 Daughterboard	E4400-60626
<b>When using the AT1 High-Power Mechanical Attenuator (Option UNB) with the A28 Reverse Power Protection (Option 501, 502, 503, 504 or Option UNJ)</b>			
W47	semi-rigid cable, output of A13 Output	input of AT1 High-Power Mechanical Attenuator - passes through A19 Daughterboard	E4400-60627
W48	semi-rigid cable, AT1 High-Power Mechanical Attenuator	input of A28 Reverse Power Protection	E4400-20550

**Table 3-16**

<b>Reference Designator</b>	<b>Input</b>	<b>Output</b>	<b>Part Number</b>
W49	semi-rigid cable, A28 Reverse Power Protection	RF OUTPUT of the rear panel	E4400-20551
W53	wire-harness, control lines of the A28 Reverse Power Protection	A23J22	08647-61028
W57	ribbon cable, AT1 High-Power Mechanical Attenuator	A23J25	E4400-20132

**Option UNJ - Option 1EM Overall Cables  
(also includes cables for Option UN7, 005, and 001/601 or 002/602)**

**Table 3-17**

Reference Designator	Input	Output	Part Number
W10 (Option 001/601 or 002/602)	A7J13	A9J3 (Option 501, 502, 503, 504 with Option 300) or A11J3 (Option UN7) or A25P2	8121-0679
W11	semi-rigid cable, output of A16 Frac-N	input of A15 Sampler - passes through A19 Daughterboard	E4400-60622
W19	A10J10	Q INT LOW on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0641
W20	A10J9	I INT on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0641
W23 (Option UN7)	A11J4	BER DATA IN of the rear panel	8121-0659
W24 (Option UN7)	A11J5	BER CLK IN of the rear panel	8121-0659
W25 (Option UN7)	A11J6	BER GATE IN of the rear panel	8121-0659
W26 (Option 005)	ribbon cable, A5 10 GB Hard Drive	A12P3	8121-0685
W27	A21J2	A23J34	8121-0644
W28	A21J3	YTO FM on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0645
W29	A21J4	PH LK on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0645
W30	A21J5	J1 of YTO Interface board	8121-0683
W31	A23J20	SWEEP OUT of rear panel	8121-0658
W32	A23J36	A25P4	8121-0678
W40	ribbon cable, AT1 Electronic Attenuator	A23J25	8121-6837

**Table 3-17**

<b>Reference Designator</b>	<b>Input</b>	<b>Output</b>	<b>Part Number</b>
W43	semi-rigid cable, output of A13 Output	input of AT1 Electronic Attenuator - passes through the A19 Daughterboard	E4400-60625
W70 <sup>a</sup> (Option 1EM)	EXT 1 of the rear panel	EXT 1 of A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0654
W71 <sup>a</sup> (Option 1EM)	EXT 2 of the rear panel	EXT 2 of A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0654
W72 (Option 1EM)	I IN of the rear panel	A10J7	8121-0652
W73 (Option 1EM)	Q IN of the rear panel	A10J8	8121-0652
W74 (Option 1EM)	A10J11	I OUT of the rear panel	8121-0653
W75 (Option 1EM)	A10J12	Q OUT of the rear panel	8121-0653
W76 (Option 1EM)	A10J13	I bar OUT of the rear panel	8121-0653
W77 (Option 1EM)	A10J14	Q bar OUT of the rear panel	8121-0653
W78 (Option 1EM and Option 001/601 or 002/602)	A7J800	BASEBAND GEN REF IN of the rear panel	8121-0655
W79 (Option 1EM)	A23J20	SWP OUT of the rear panel	8121-0660
W80 (Option 1EM)	DATA IN of the rear panel	A23J21	8121-0660
W81 (Option 1EM)	DATA CLK IN of the rear panel	A23J23	8121-0660
W82 (Option 1EM)	SYMBOL SYNC IN of the rear panel	A23J24	8121-0660
W83 (Option 1EM)	flexible cable A19 Daughterboard	LF OUT of the rear panel	8121-0656
W92 (Option 001/601 or 002/602)	A7J21	A25P1	8121-0690



- a. When comparing cables W70 and W71 between a model with Option 506 and a model with either Option 501, 502, 503, 504, or UNJ, notice that cables W70 and W71 are connected to different (swapped) ports of the A19 Daughterboard.

**Option UNJ - Option 1EM Semi-Rigid Cables  
includes the A21 YTO Driver (Option UNJ or Option 506)  
and the AT1 Electronic Attenuator (Option 501, 502, 503, 504 or Option UNJ)  
or the AT1 High-Power Mechanical Attenuator (Option UNB)  
with A28 Reverse Power Protection (Option 501, 502, 503, 504 or Option UNJ)**

**Table 3-18**

Reference Designator	Input	Output	Part Number
W11	semi-rigid cable, output of A16 Frac-N	input of A15 Sampler - passes through A19 Daughterboard	E4400-60622
W32	A23J36	A25P4	8121-0678
W37	semi-rigid cable, A15 Sampler - passes through the A19 Daughterboard	top connector of A21 YTO Driver	E4423-20510
W38	semi-rigid cable, A16 Frac-N - passes through the A19 Daughterboard	middle connector of A21 YTO Driver	E4423-20514
W39	semi-rigid cable, output of YTO	bottom connector of A21 YTO Driver	E4423-20513
W40	ribbon cable, AT1 Electronic Attenuator	A23J25	8121-6837
W41	semi-rigid cable, output of A16 Frac-N	A13 Output - passes through the A19 Daughterboard	E4400-60623
W43	semi-rigid cable, output of A13 Output	input of AT1 Electronic Attenuator - passes through the A19 Daughterboard	E4400-60625
W44	semi-rigid cable, output of A18 Reference	A15 Sampler - passes through the A19 Daughterboard	E4400-60626
W79 (Option 1EM)	A23J20	SWP OUT of the rear panel	8121-0660
W84 (Option 1EM)	semi-rigid cable, output of A13 Output	COH CARRIER of the rear panel - passes through A19 Daughterboard	E4400-60631
W85 (Option 1EM)	semi-rigid cable, output of AT1 Electronic Attenuator	RF OUTPUT of the rear panel	E4400-20555
W47 (Option UNB & A28)	semi-rigid cable, output of A13 Output	input of AT1 High-Power Mechanical Attenuator - passes through the A19 Daughterboard	E4400-60627

**Table 3-18**

<b>Reference Designator</b>	<b>Input</b>	<b>Output</b>	<b>Part Number</b>
W48 (Option UNB & A28)	semi-rigid cable, AT1 High-Power Mechanical Attenuator	input of A28 Reverse Power Protection	E4400-20550
W53 (Option UNB & A28)	wire-harness, control lines of the A28 Reverse Power Protection	A23J22	08647-61028
W57 (Option UNB & A28)	ribbon cable, AT1 High-Power Mechanical Attenuator	A23J25	E4400-20132
W84 (Option 1EM, Option UNB & A28)	semi-rigid cable, output of A13 Output	COH CARRIER of rear panel - passes through the A19 Daughterboard	E4400-60631
W86 (Option 1EM, Option UNB & A28)	semi-rigid cable, A28 Reverse Power Protection	RF OUTPUT of rear panel	E4400-20552

## Cables for Option 506 Signal Generators

### Option 506 - Overall Cables

(also includes cables for Option UN7, 005, and 001/601 or 002/602)

**Table 3-19**

Reference Designator	Input	Output	Part Number
W1	I INPUT of front panel	A10J7	8121-0642
W2	Q INPUT of front panel	A10J8	8121-0642
W3	DATA of front panel	A21J21	8121-0661
W4	DATA CLOCK of front panel	A23J23	8121-0661
W5	SYMBOL SYNC of front panel	A23J24	8121-0661
W6 <sup>a</sup>	EXT 1 INPUT of front panel	EXT1 on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0644
W7 <sup>a</sup>	EXT 2 INPUT of front panel	EXT2 on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0644
W8	LF OUTPUT of front panel	LF OUT A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0657
W10 (Option 001/601 or 002/602)	A7J13	A9J3 (Option 501, 502, 503, 504 with Option 300) or A11J3 (Option UN7) or A25P2	8121-0679
W11	semi-rigid cable, output of A16 Frac-N	input of A15 Sampler - passes through A19 Daughterboard	E4400-60622
W12 (Option 001/601 or 002/602)	A7J800	BASEBAND GEN REF IN of the rear panel	8121-0643
W15	A10J14	Q bar OUT of the rear panel	8121-0640
W16	A10J13	I bar OUT of the rear panel	8121-0640
W17	A10J12	Q OUT of the rear panel	8121-0640
W18	A10J11	I OUT of the rear panel	8121-0640

**Table 3-19**

<b>Reference Designator</b>	<b>Input</b>	<b>Output</b>	<b>Part Number</b>
W19	A10J10	Q INT LOW on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0641
W20	A10J9	I INT on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0641
W21	A10J6	Q INT HIGH on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0641
W22	A10J5	I INT HIGH on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0641
W23 (Option UN7)	A11J4	BER DATA IN of the rear panel	8121-0659
W24 (Option UN7)	A11J5	BER CLK IN of the rear panel	8121-0659
W25 (Option UN7)	A11J6	BER GATE IN of the rear panel	8121-0659
W26 (Option 005)	ribbon cable, A5 10 GB Hard Drive	A12P3	8121-0685
W27	A21J2	A23J34	8121-0644
W28	A21J3	YTO FM on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0645
W29	A21J4	PH LK on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0645
W30	A21J5	J1 of YTO Interface board	8121-0683
W31	A23J20	SWEEP OUT of rear panel	8121-0658
W32	A23J36	A25P4	8121-0678
W50	semi-rigid cable, output of A14 Extended Frequency Output	input of AT1 High-Power Mechanical Attenuator - passes through the A19 Daughterboard	E4400-60628
W57	ribbon cable, AT1 High-Power Mechanical Attenuator	A23J25	E4400-20132

**Table 3-19**

<b>Reference Designator</b>	<b>Input</b>	<b>Output</b>	<b>Part Number</b>
W58	semi-rigid cable, output of A13 Output	input of A14 Extended Frequency Output - passes through the A19 Daughterboard	E4400-60630
W92 (Option 001/601 or 002/602)	A7J21	A25P1	8121-0690

- a. When comparing cables W6 and W7 between a model with Option 506 and a model with either Option 501, 502, 503, 504, or UNJ, notice that cables W6 and W7 are connected to different (swapped) ports of the A19 Daughterboard.

## Option 506 - Front Panel Cables

**Table 3-20**

<b>Reference Designator</b>	<b>Input</b>	<b>Output</b>	<b>Part Number</b>
W1	I INPUT of front panel	A10J7	8121-0642
W2	Q INPUT of front panel	A10J8	8121-0642
W3	DATA of front panel	A21J21	8121-0661
W4	DATA CLOCK of front panel	A23J23	8121-0661
W5	SYMBOL SYNC of front panel	A23J24	8121-0661
W6 <sup>a</sup>	EXT 1 INPUT of front panel	EXT1 on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0644
W7 <sup>a</sup>	EXT 2 INPUT of front panel	EXT2 on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0644
W8	LF OUTPUT of front panel	LF OUT A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0657

- a. When comparing cables W6 and W7 between a model with Option 506 and a model with either Option 501, 502, 503, 504, or UNJ, notice that cables W6 and W7 are connected to different (swapped) ports of the A19 Daughterboard.

**Option 506 - Standard Cables**  
**(does not include cables for the front panel, Option UN7, or Option 005)**

**Table 3-21**

<b>Reference Designator</b>	<b>Input</b>	<b>Output</b>	<b>Part Number</b>
W10 (Option 001/601 or 002/602)	A7J13	A9J3 (Option 501, 502, 503, 504 with Option 300) or A11J3 (Option UN7) or A25P2	8121-0679
W11	semi-rigid cable, output of A16 Frac-N	input of A15 Sampler - passes through A19 Daughterboard	E4400-60622
W12 (Option 001/601 or 002/602)	A7J800	BASEBAND GEN REF IN of the rear panel	8121-0643
W15	A10J14	Q bar OUT of the rear panel	8121-0640
W16	A10J13	I bar OUT of the rear panel	8121-0640
W17	A10J12	Q OUT of the rear panel	8121-0640
W18	A10J11	I OUT of the rear panel	8121-0640
W19	A10J10	Q INT LOW on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0641
W20	A10J9	I INT on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0641
W21	A10J6	Q INT HIGH on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0641
W22	A10J5	I INT HIGH on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0641
W27	A21J2	A23J34	8121-0644
W28	A21J3	YTO FM on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0645
W29	A21J4	PHLK on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0645



**Table 3-21**

<b>Reference Designator</b>	<b>Input</b>	<b>Output</b>	<b>Part Number</b>
W30	A21J5	J1 of YTO Interface board	8121-0683
W31	A23J20	SWEEP OUT of rear panel	8121-0658
W32	A23J36	A25P4	8121-0678
W50	semi-rigid cable, output of A14 Extended Frequency Output	input of AT1 High-Power Mechanical Attenuator - passes through the A19 Daughterboard	E4400-60628
W57	ribbon cable, AT1 High-Power Mechanical Attenuator	A23J25	E4400-20132
W58	semi-rigid cable, output of A13 Output	input of A14 Extended Frequency Output - passes through the A19 Daughterboard	E4400-60630
W92 (Option 001/601 or 002/602)	A7J21	A25P1	8121-0690

**Option 506 - Only Option UN7, 005, and 001/601 or 002/602 Cables**

**Table 3-22**

<b>Reference Designator</b>	<b>Input</b>	<b>Output</b>	<b>Part Number</b>
W10 (Option 001/601 or 002/602)	A7J13	A9J3 (Option 501, 502, 503, 504 with Option 300) or A11J3 (Option UN7) or A25P2	8121-0679
W11	semi-rigid cable, output of A16 Frac-N	input of A15 Sampler - passes through A19 Daughterboard	E4400-60622
W12 (Option 001/601 or 002/602)	A7J800	BASEBAND GEN REF IN of the rear panel	8121-0643
W23 (Option UN7)	A11J4	BER DATA IN of the rear panel	8121-0659
W24 (Option UN7)	A11J5	BER CLK IN of the rear panel	8121-0659
W25 (Option UN7)	A11J6	BER GATE IN of the rear panel	8121-0659
W26 (Option 005)	ribbon cable, A5 10 GB Hard Drive	A12P3	8121-0685
W92 (Option 001/601 or 002/602)	A7J21	A25P1	8121-0690

**Option 506 - Semi-Rigid Cables  
includes the A21 YTO Driver (Option UNJ or Option 506)  
and the AT1 High-Power Mechanical Attenuator (Option UNB)  
with an A29 DC Blocking Capacitor (Option 506)**

**Table 3-23**

<b>Reference Designator</b>	<b>Input</b>	<b>Output</b>	<b>Part Number</b>
W11	semi-rigid cable, output of A16 Frac-N	input of A15 Sampler - passes through A19 Daughterboard	E4400-60622
W31	A23J20	SWEEP OUT of rear panel	8121-0658
W32	A23J36	A25P4	8121-0678
W37	semi-rigid cable, A15 Sampler - passes through the A19 Daughterboard	top connector of A21 YTO Driver	E4423-20510
W38	semi-rigid cable, A16 Frac-N - passes through the A19 Daughterboard	middle connector of A21 YTO Driver	E4423-20514
W41	semi-rigid cable, output of A16 Frac-N	A13 Output - passes through the A19 Daughterboard	E4400-60623
W42	semi-rigid cable, output of A13 Output	COH CARRIER of the rear panel - passes through the A19 Daughterboard	E4400-60624
W44	semi-rigid cable, output of A18 Reference	A15 Sampler - passes through the A19 Daughterboard	E4400-60626
W50	semi-rigid cable, output of A14 Extended Frequency Output	input of AT1 High-Power Mechanical Attenuator - passes through the A19 Daughterboard	E4400-60628
W54	semi-rigid cable, output of YTO	A22 Coupler	E4400-20560
W55	semi-rigid cable, output of A22 Coupler	bottom connector of A21 YTO Driver	E4400-20561
W56	semi-rigid cable, coupled output of A22 Coupler	input of A14 Extended Frequency Output - passes through A19 Daughterboard	E4400-60629
W57	ribbon cable, AT1 High-Power Mechanical Attenuator	A23J25	E4400-20132
W58	semi-rigid cable, output of A13 Output	input of A14 Extended Frequency Output - passes through the A19 Daughterboard	E4400-60630
W59	semi-rigid cable, output of AT1 High-Power Mechanical Attenuator	input of A29 DC Blocking Capacitor	E4400-20573

**Table 3-23**

<b>Reference Designator</b>	<b>Input</b>	<b>Output</b>	<b>Part Number</b>
W60	connector, output of A29 DC Blocking Capacitor (Option 506)	input of RF OUPUT	1250-1249

**Option 506 - Option 1EM Overall Cables  
(also includes cables for Option UN7, 005, and 001/601 or 002/602)**

**Table 3-24**

<b>Reference Designator</b>	<b>Input</b>	<b>Output</b>	<b>Part Number</b>
W10 (Option 001/601 or 002/602)	A7J13	A9J3 (Option 501, 502, 503, 504 with Option 300) or A11J3 (Option UN7) or A25P2	8121-0679
W11	semi-rigid cable, output of A16 Frac-N	input of A15 Sampler - passes through A19 Daughterboard	E4400-60622
W19	A10J10	Q INT LOW on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0641
W20	A10J9	I INT on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0641
W21	A10J6	Q INT HIGH on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0641
W22	A10J5	I INT HIGH on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0641
W23 (Option UN7)	A11J4	BER DATA IN of the rear panel	8121-0659
W24 (Option UN7)	A11J5	BER CLK IN of the rear panel	8121-0659
W25 (Option UN7)	A11J6	BER GATE IN of the rear panel	8121-0659
W26 (Option 005)	ribbon cable, A5 10 GB Hard Drive	A12P3	8121-0685
W27	A21J2	A23J34	8121-0644
W28	A21J3	YTO FM on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0645
W29	A21J4	PH LK on A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0645

**Table 3-24**

<b>Reference Designator</b>	<b>Input</b>	<b>Output</b>	<b>Part Number</b>
W30	A21J5	J1 of YTO Interface board	8121-0683
W31	A23J20	SWEEP OUT of rear panel	8121-0658
W32	A23J36	A25P4	8121-0678
W50	semi-rigid cable, output of A14 Extended Frequency Output	input of AT1 High-Power Mechanical Attenuator - passes through the A19 Daughterboard	E4400-60628
W57	ribbon cable, AT1 High-Power Mechanical Attenuator	A23J25	E4400-20132
W58	semi-rigid cable, output of A13 Output	input of A14 Extended Frequency Output - passes through the A19 Daughterboard	E4400-60630
W70 <sup>a</sup> (Option 1EM)	EXT 1 of the rear panel	EXT 1 of A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0654
W71 <sup>a</sup> (Option 1EM)	EXT 2 of the rear panel	EXT 2 of A19 Daughterboard - refer to the connector diagram located on the daughterboard cover	8121-0654
W72 (Option 1EM)	I IN of the rear panel	A10J7	8121-0652
W73 (Option 1EM)	Q IN of the rear panel	A10J8	8121-0652
W74 (Option 1EM)	A10J11	I OUT of the rear panel	8121-0653
W75 (Option 1EM)	A10J12	Q OUT of the rear panel	8121-0653
W76 (Option 1EM)	A10J13	I bar OUT of the rear panel	8121-0653
W77 (Option 1EM)	A10J14	Q bar OUT of the rear panel	8121-0653
W78 (Option 1EM and Option 001 or 002)	A7J800	BASEBAND GEN REF IN of the rear panel	8121-0655
W79 (Option 1EM)	A23J20	SWP OUT of the rear panel	8121-0660
W80 (Option 1EM)	DATA IN of the rear panel	A23J21	8121-0660

**Table 3-24**

<b>Reference Designator</b>	<b>Input</b>	<b>Output</b>	<b>Part Number</b>
W81 (Option 1EM)	DATA CLK IN of the rear panel	A23J23	8121-0660
W82 (Option 1EM)	SYMBOL SYNC IN of the rear panel	A23J24	8121-0660
W83 (Option 1EM)	flexible cable A19 Daughterboard	LF OUT of the rear panel	8121-0656
W92 (Option 001/601 or 002/602)	A7J21	A25P1	8121-0690

- a. When comparing cables W70 and W71 between a model with Option 506 and a model with either Option 501, 502, 503, 504, or UNJ, notice that cables W70 and W71 are connected to different (swapped) ports of the A19 Daughterboard.

**Option 506 - Option 1EM Semi-Rigid Cables  
includes the A21 YTO Driver (Option UNJ or Option 506)  
and the AT1 High-Power Mechanical Attenuator (Option UNB)  
with an A29 DC Blocking Capacitor (Option 506)**

**Table 3-25**

Reference Designator	Input	Output	Part Number
W11	semi-rigid cable, output of A16 Frac-N	input of A15 Sampler - passes through the A19 Daughterboard	E4400-60622
W32	A23J36	A25P4	8121-0678
W37	semi-rigid cable, A15 Sampler - passes through the A19 Daughterboard	top connector of A21 YTO Driver	E4423-20510
W38	semi-rigid cable, A16 Frac-N - passes through the A19 Daughterboard	middle connector of A21 YTO Driver	E4423-20514
W41	semi-rigid cable, output of A16 Frac-N	A13 Output - passes through the A19 Daughterboard	E4400-60623
W44	semi-rigid cable, output of A18 Reference	A15 Sampler - passes through the A19 Daughterboard	E4400-60626
W50	semi-rigid cable, output of A14 Extended Frequency Output	input of AT1 High-Power Mechanical Attenuator - passes through the A19 Daughterboard	E4400-60628
W54	output of YTO	A22 Coupler (Option 506)	E4400-20560
W55	semi-rigid cable, output of A22 Coupler	bottom connector of A21 YTO Driver	E4400-20561
W56	semi-rigid cable, coupled output of A22 Coupler	input of A14 Extended Frequency Output - passes through the A19 Daughterboard	E4400-60629
W57	ribbon cable, AT1 High-Power Mechanical Attenuator	A23J25	E4400-20132
W58	semi-rigid cable, output of A13 Output	input of A14 Extended Frequency Output - passes through the A19 Daughterboard	E4400-60630
W79 (Option 1EM)	A23J20	SWP OUT of the rear panel	8121-0660
W84 (Option 1EM)	semi-rigid cable, output of A13 Output	COH CARRIER of rear panel - passes through the A19 Daughterboard	E4400-60631



**Table 3-25**

<b>Reference Designator</b>	<b>Input</b>	<b>Output</b>	<b>Part Number</b>
W91 (Option 1EM)	semi-rigid cable, output of AT1 High-Power Mechanical Attenuator	input of A29 DC Blocking Capacitor	E4400-20574

## Hardware and Other Instrument Parts

This section lists part numbers for hardware and other instrument parts in your signal generator.

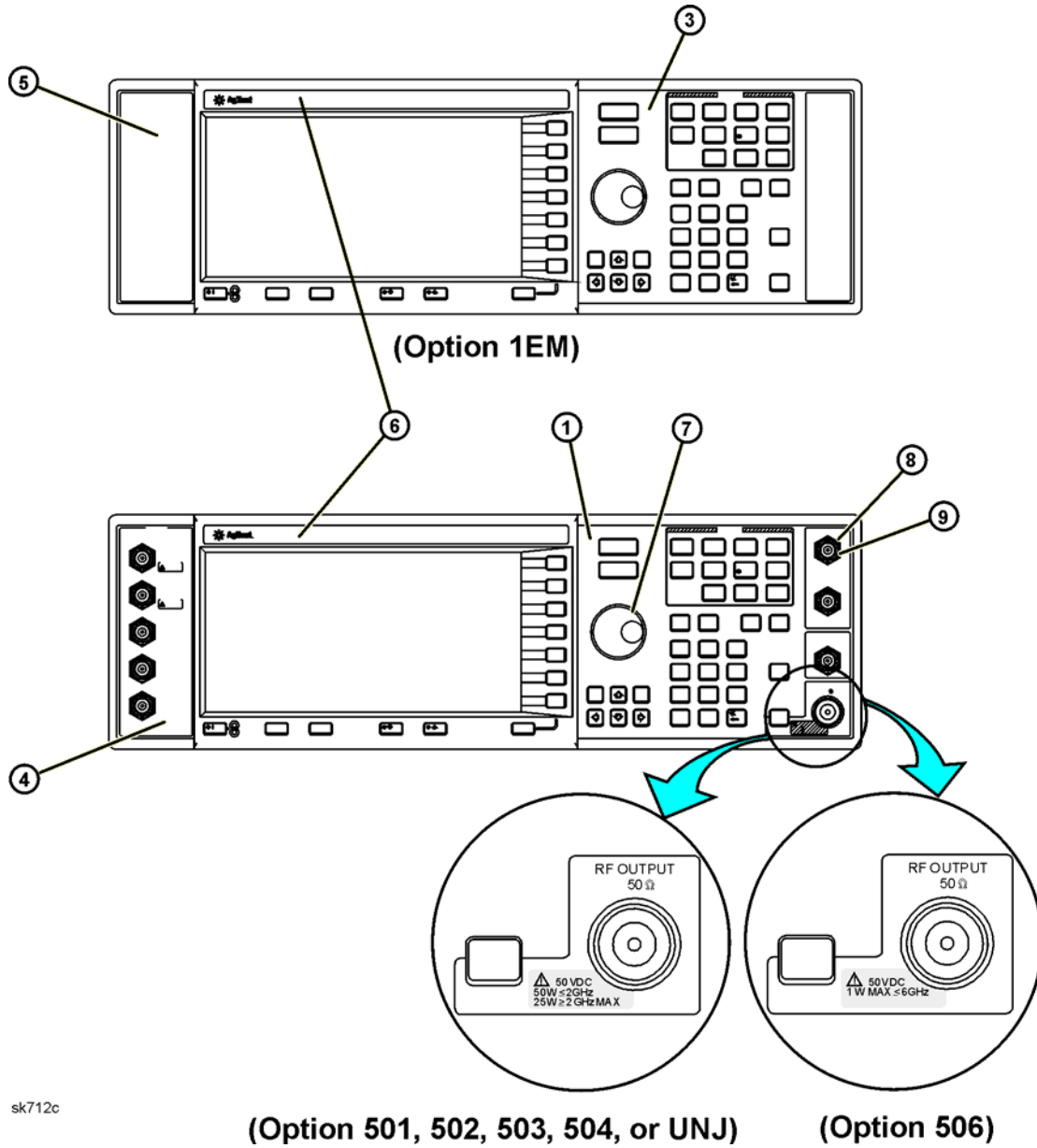
- “Hardware of the Front Panel” on page 3-61
- “Hardware of a Disassembled Front Panel” on page 3-62
- “Hardware of the Rear-Panel (Option 501, 502, 503, 504, Option 300, Option UNJ, or Option 506)” on page 3-66
- “Hardware of the Rear-Panel (Option 1EM)” on page 3-68
- “Hardware of a Disassembled AT1 Electronic Attenuator (Option 501, 502, 503, 504 or Option UNJ)” on page 3-70
- “Hardware of a Disassembled AT1 High-Power Mechanical Attenuator (Option UNB) and A28 Reverse Power Protection (Option 501, 502, 503, 504 or Option UNJ)” on page 3-71
- “Hardware of a Disassembled AT1 High-Power Mechanical Attenuator (Option UNB) on Option 506 Only” on page 3-73
- “Hardware of the A6 Power Supply Shield and Left-Side (Option 501, 502, 503, 504, Option 300, Option UNJ, or Option 506)” on page 3-74
- “Hardware of the A23 Motherboard and Bottom-Side (Option 501, 502, 503, 504, Option 300, Option UNJ, or Option 506)” on page 3-75
- “Hardware of the Outside Instrument Covers and Associated Parts (Option 501, 502, 503, 504, Option 300, Option UNJ, or Option 506)” on page 3-76
- “Hardware of the Inside Instrument Covers and Associated Parts (Option 501, 502, 503, 504, Option 300, Option UNJ, or Option 506)” on page 3-78

## Hardware of the Front Panel

**Table 3-26**

Item No.	Description	Qty	Part Number
1	menu overlay (Option 501, 502, 503, 504)	1	E4400-80509
2	menu overlay (Option 1EM)	1	E4400-80510
3	menu overlay (Option 506)	1	E4400-80535
4	small overlay-connectors (Option 501, 502, 503, 504)	1	E4400-80006
5	small overlay-connectors (Option 1EM)	1	E4400-80011
6	nameplate label 250 kHz to 1 GHz (Option 501)	1	E4400-80504
	nameplate label 250 kHz to 2GHz (Option 502)	1	E4400-80505
	nameplate label 250 kHz to 3GHz (Option 503)	1	E4400-80506
	nameplate label 250 kHz to 4 GHz (Option 504)	1	E4400-80507
	nameplate label 250 kHz to 6 GHz (Option 506)	1	E4400-80500
7	knob-RPG	1	E4400-40003
8	hex-nut	8	2950-0035
9	washer	8	3050-1919

**Figure 3-10 Hardware of the Front Panel**



**Hardware of a Disassembled Front Panel**

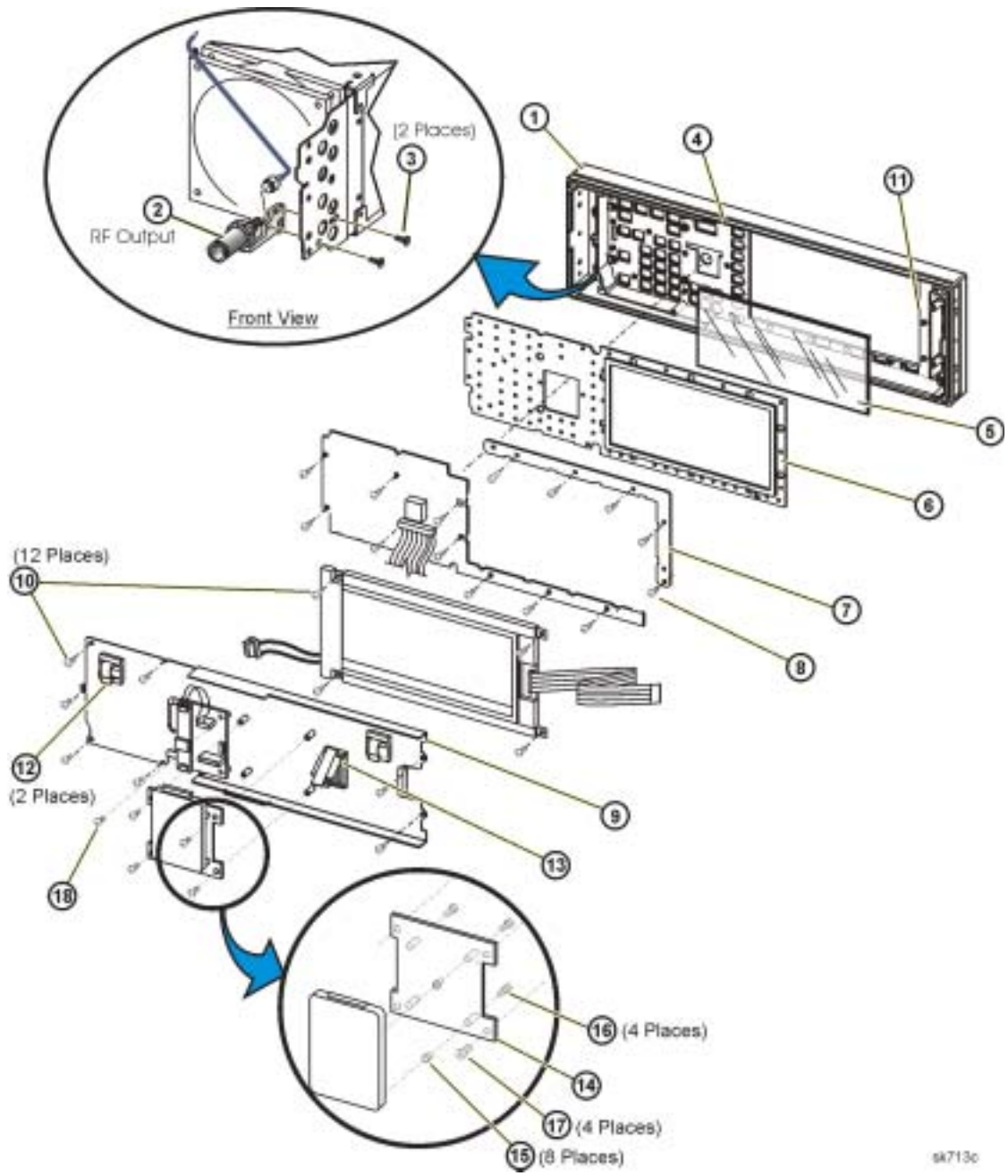
**Table 3-27**

Item No.	Description	Qty	Part Number
1	front panel frame	1	E4400-60522
2	RF OUTPUT connector and bracket	1	E4400-60536

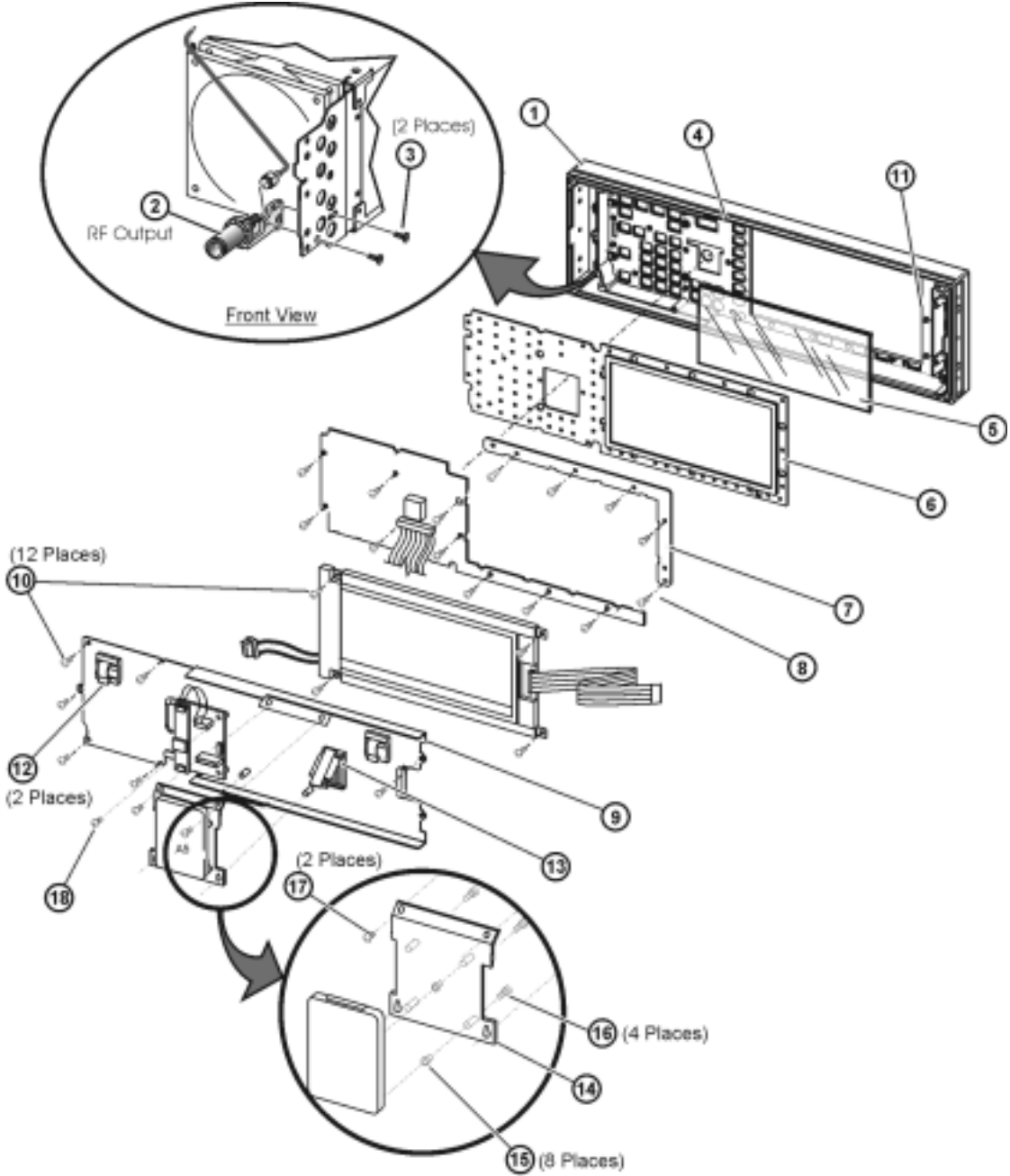
**Table 3-27**

Item No.	Description	Qty	Part Number
3	screw-machine (flathead screws for RF OUTPUT connector bracket to chassis)	2	0515-1227
4	mesh gasket (frame)	1	8160-7023
5	glass filter	1	4330-1711
6	rubber keypad	1	E4400-40502
7	bracket filter	1	E4400-00009
8	screw-machine (flathead screws for filter bracket and front panel board)	14	0515-1227
9	RFI shield	1	E4400-00515
10	screw-machine (pan-head screw for RFI Shield and A2 Display)	12	0515-0372
11	spira gasket (window)	1	8160-0660
12	white-plastic clamp	2	1400-1594
13	plastic clamp	1	1400-2293
14	bracket for A5 Hard Drive (Option 005)	1	E4400-00526
15	bushing for A5 Hard Drive (Option 005)	8	0340-1525
16	screw-machine for A5 Hard Drive (Option 005) Torque to 2.5 in-lb.	4	0515-0681
17	screw-machine A5 Hard Drive (Option 005) to RFI shield, serial prefixes <US4426/MY4426	4	0515-0664
17	screw-machine A5 Hard Drive (Option 005) to RFI shield, serial prefixes ≥US4226/MY4226	2	0515-0372
18	screw-machine A3 Front Panel Auxilary Board to RFI shield	1	0515-0372

**Figure 3-11** Hardware of a Disassembled Front Panel, for instruments with serial prefixes <US4426/MY4426



**Figure 3-12** Hardware of a Disassembled Front Panel, for instruments with serial prefixes  $\geq$ US4426/MY4426



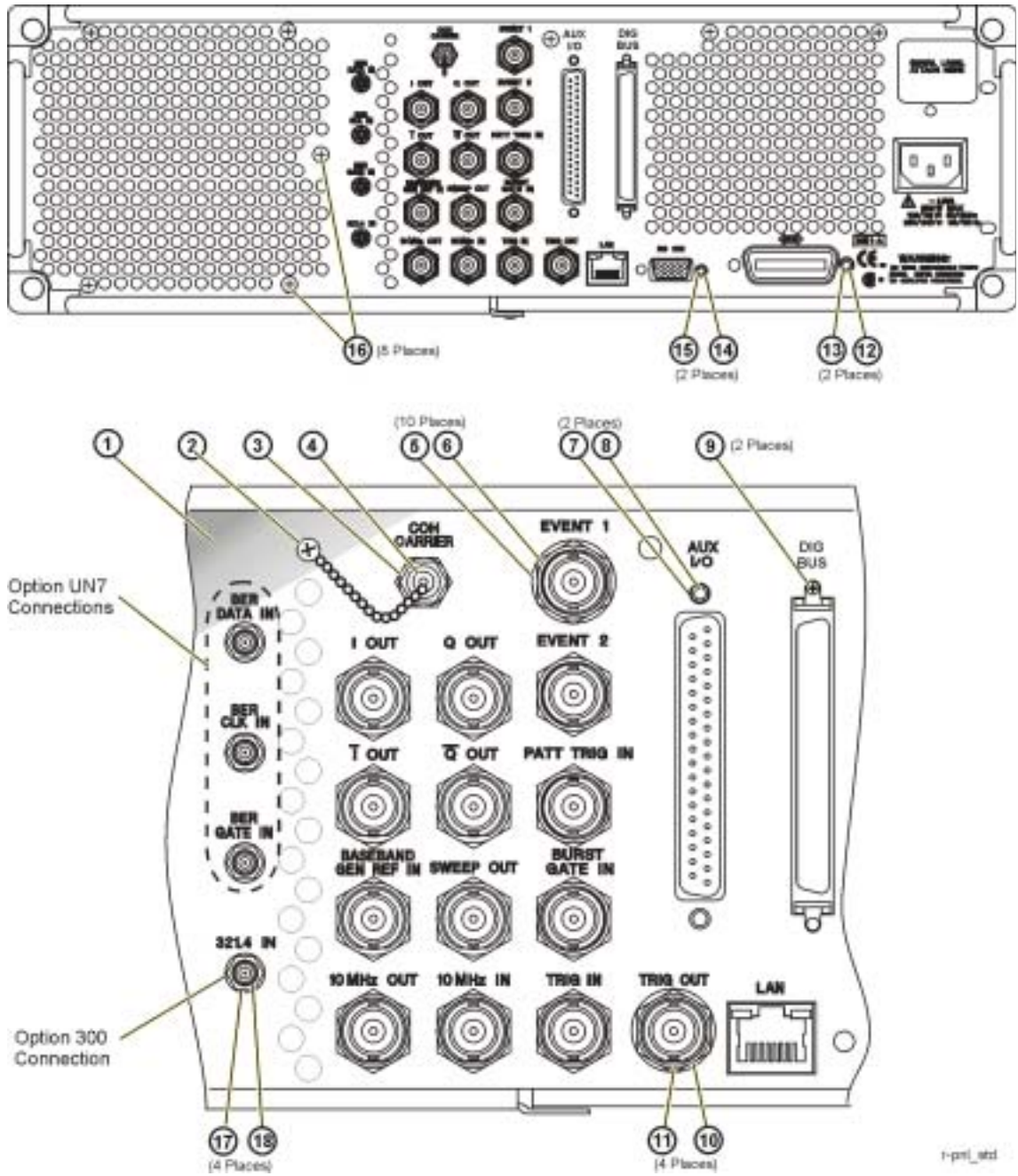
**Hardware of the Rear-Panel  
(Option 501, 502, 503, 504, Option 300, Option UNJ, or Option 506)**

**Table 3-28**

Item No.	Description	Qty	Part Number
1	rear panel	1	E4400-00531
2	screw-machine (coherent carrier)	1	0515-0372
3	connector (coherent carrier)	1	1250-1753
4	dust cap (coherent carrier)	1	1250-2759
5	washer	10	2190-0102
6	hex nut	10	2950-0035
7	washer (Aux I/O connector to rear panel)	2	2190-0003
8	screw (Aux I/O connector to rear panel)	2	0380-2079
9	screw-machine (DIG BUS connector to rear panel)	2	0515-4439
10	washer (BNC connectors from A23 Motherboard)	4	2190-0068
11	hex nut (BNC connector from A23 Motherboard)	4	2950-0054
12	washer (GPIB)	2	2190-0577
13	standoff screw-hex (GPIB)	2	0380-0644
14	washer (RS 232)	2	2190-0003
15	screw (RS 232)	2	0380-2079
16	screw-machine (rear panel to chassis)	8	0515-0372
17	washer (BER DATA IN, BER CLK IN, BER GATE IN, 321.4 IN connectors) UNJ)	3	2190-0124
18	hex nut (BER DATA IN, BER CLK IN, BER GATE IN, 321.4 IN connectors)	3	2950-0078



**Figure 3-13 Hardware of the Rear-Panel  
 (Option 501, 502, 503, 504, Option 300, Option UNJ, or Option 506)**

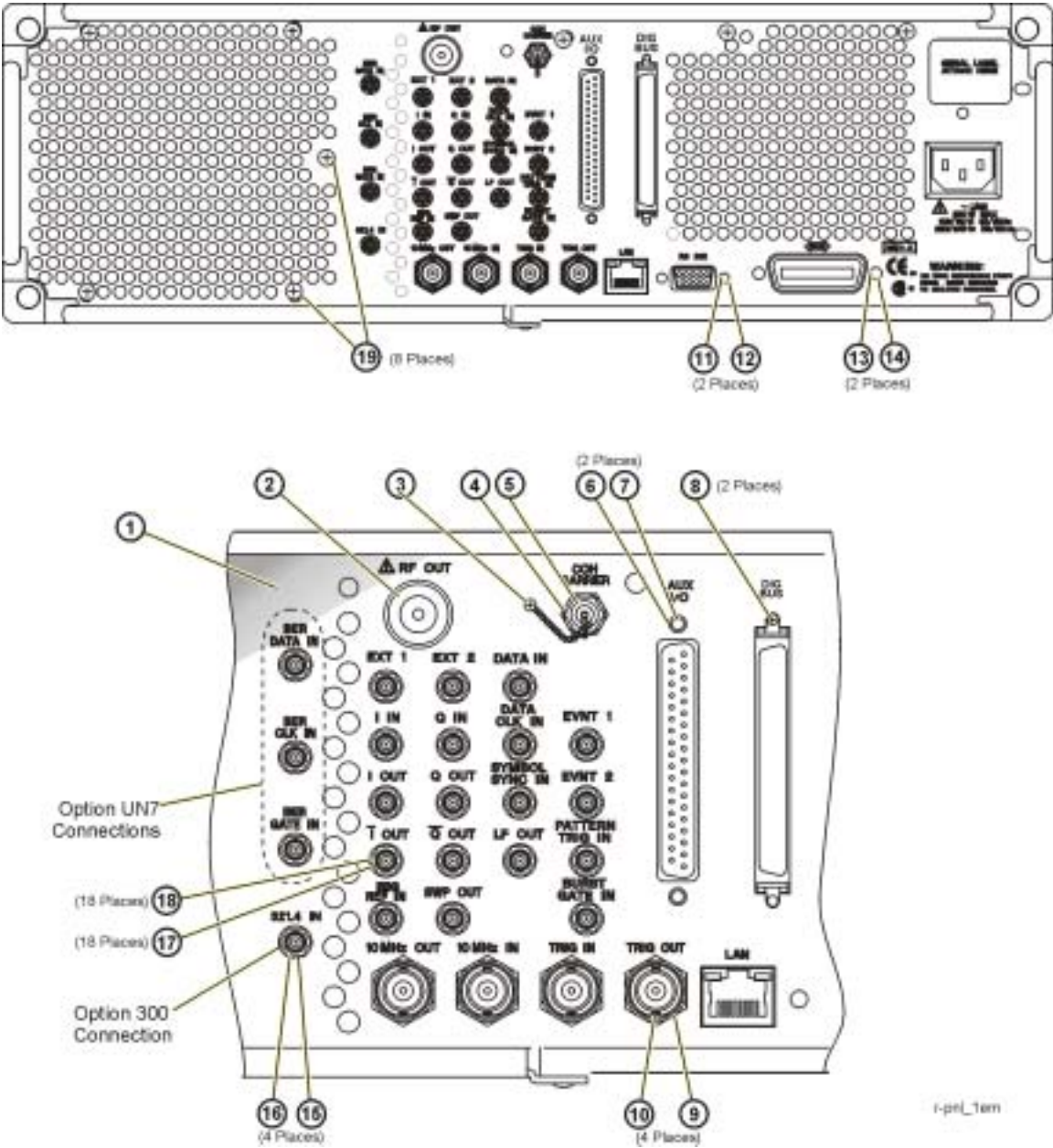


### Hardware of the Rear-Panel (Option 1EM)

**Table 3-29**

Item No.	Description	Qty	Part Number
1	rear panel	1	E4400-00532
2	RF OUTPUT connector and bracket	1	E4400-60542
3	screw-machine (coherent carrier)	1	0515-0372
4	connector (coherent carrier)	1	1250-1753
5	dust cap (coherent carrier)	1	1250-2759
6	washer (Aux I/O connector to rear panel)	2	2190-0003
7	screw (Aux I/O connector to rear panel)	2	0380-2079
8	screw-machine (DIG BUS connector to rear panel)	2	0515-4439
9	washer (BNC connector from A23 Motherboard)	4	2190-0068
10	hex nut (BNC connector from A23 Motherboard)	4	2950-0054
11	washer (RS-232 connector)	2	2190-0003
12	screw-machine (RS-232 connector)	2	0380-2079
13	washer (GPIB connector)	2	2190-0577
14	standoff screw-hex (GPIB connector)	2	0380-0644
15	washer (BER DATA IN, BER CLK IN, BER GATE IN, 321.4 IN connectors)	4	2190-0124
16	hex nut (BER DATA IN, BER CLK IN, BER GATE IN, 321.4 IN connectors)	4	2950-0078
17	washer	18	2190-0102
18	hex nut	18	2950-0035
19	screw-machine (rear panel to chassis)	8	0515-0372

**Figure 3-14** Hardware of the Rear-Panel  
 (Option 1EM)



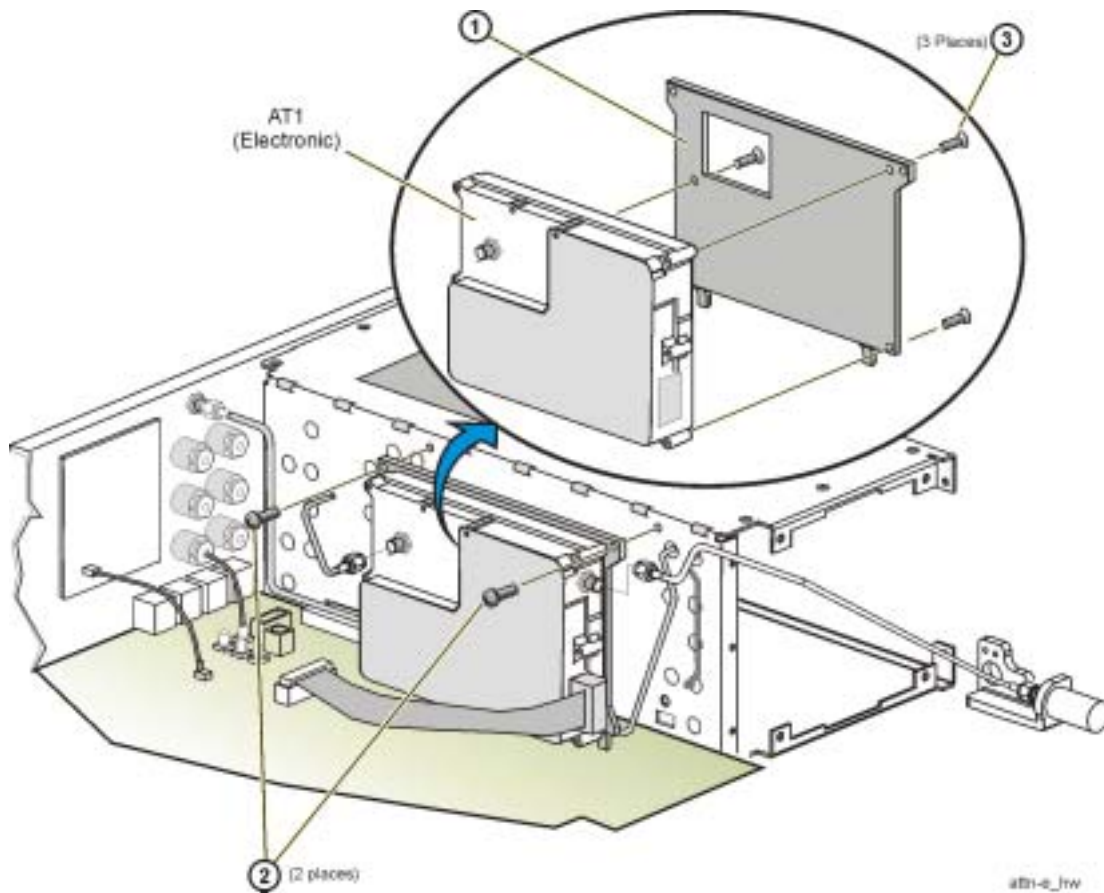
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**Hardware of a Disassembled  
 AT1 Electronic Attenuator (Option 501, 502, 503, 504 or Option UNJ)**

**Table 3-30**

Item No.	Description	Qty	Part Number
1	bracket for AT1 Electronic Attenuator (Option 501, 502, 503, 504 or Option UNJ)	1	E4400-00523
2	screw-machine (pan head screw used to attach the AT1 Electronic Attenuator bracket to the chassis)	2	0515-0433
3	screw-machine (flathead screw used to attach the AT1 Electronic Attenuator to the AT1 Electronic Attenuator bracket)	3	0515-0664

**Figure 3-15 Hardware of a Disassembled  
 AT1 Electronic Attenuator (Option 501, 502, 503, 504 or Option UNJ)**

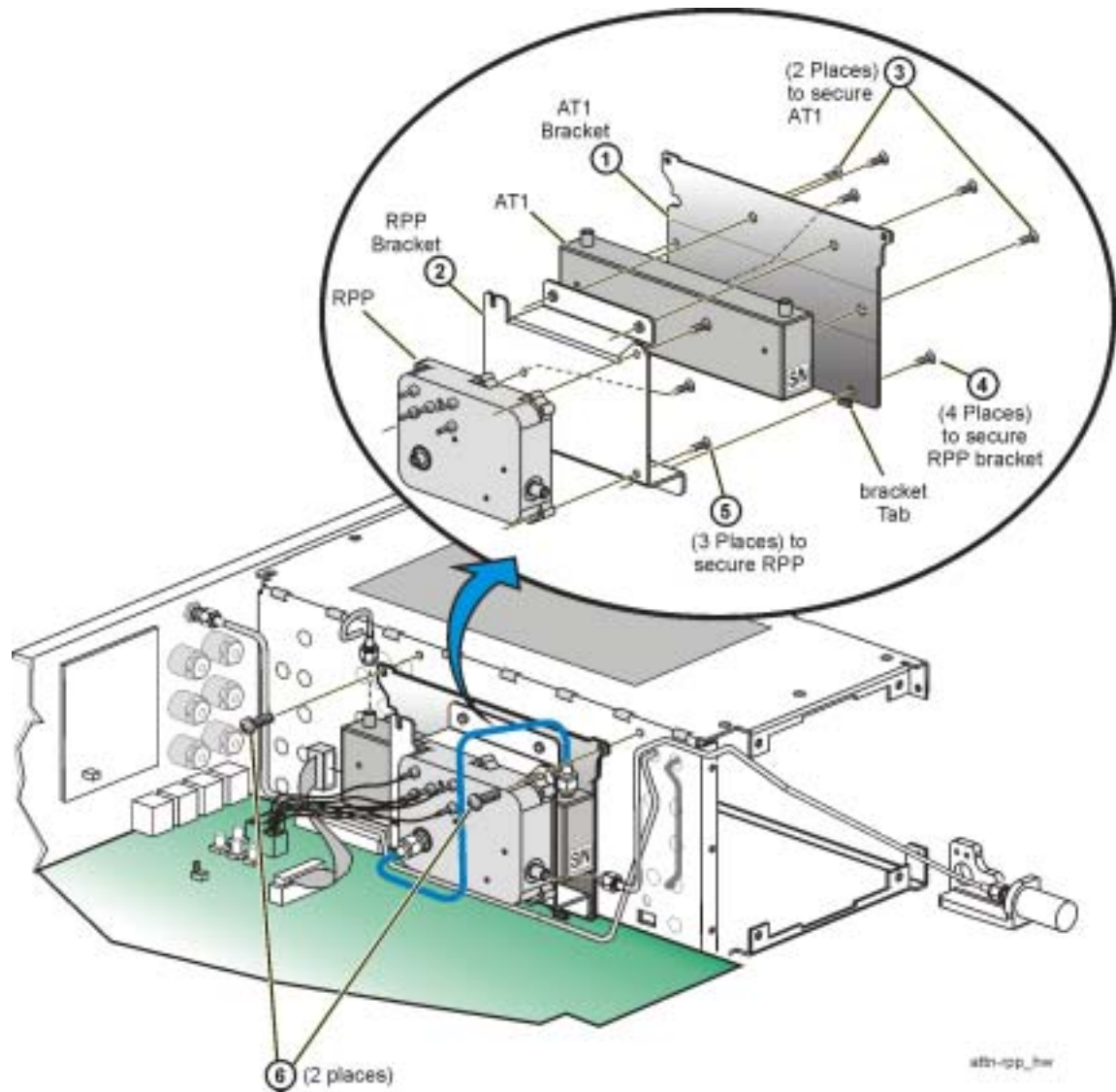


**Hardware of a Disassembled  
AT1 High-Power Mechanical Attenuator (Option UNB)  
and A28 Reverse Power Protection (Option 501, 502, 503, 504 or Option UNJ)**

**Table 3-31**

<b>Item No.</b>	<b>Description</b>	<b>Qty</b>	<b>Part Number</b>
1	bracket for AT1 High-Power Mechanical Attenuator (Option UNB)	1	E4400-00523
2	bracket for A28 Reverse Power Protection (Option 501, 502, 503, 504 or Option UNJ)	1	E4400-00524
3	screw-machine (flathead screw used to attach the AT1 Mechanical Attenuator to AT1 Mechanical Attenuator bracket)	2	0515-1227
4	screw-machine (flathead screw used to attach the AT1 Mechanical Attenuator bracket to the A28 RPP bracket)	4	0515-0664
5	screw-machine (flathead screw used to attach the A28 RPP to the A28 RPP bracket)	4	0515-0664
6	screw-machine (pan head screw used to attach the AT1 Mechanical Attenuator and A28 RPP to the chassis)	2	0515-0430

**Figure 3-16** Hardware of a Disassembled  
**AT1 High-Power Mechanical Attenuator (Option UNB)**  
**and A28 Reverse Power Protection (Option 501, 502, 503, 504, or Option UNJ)**

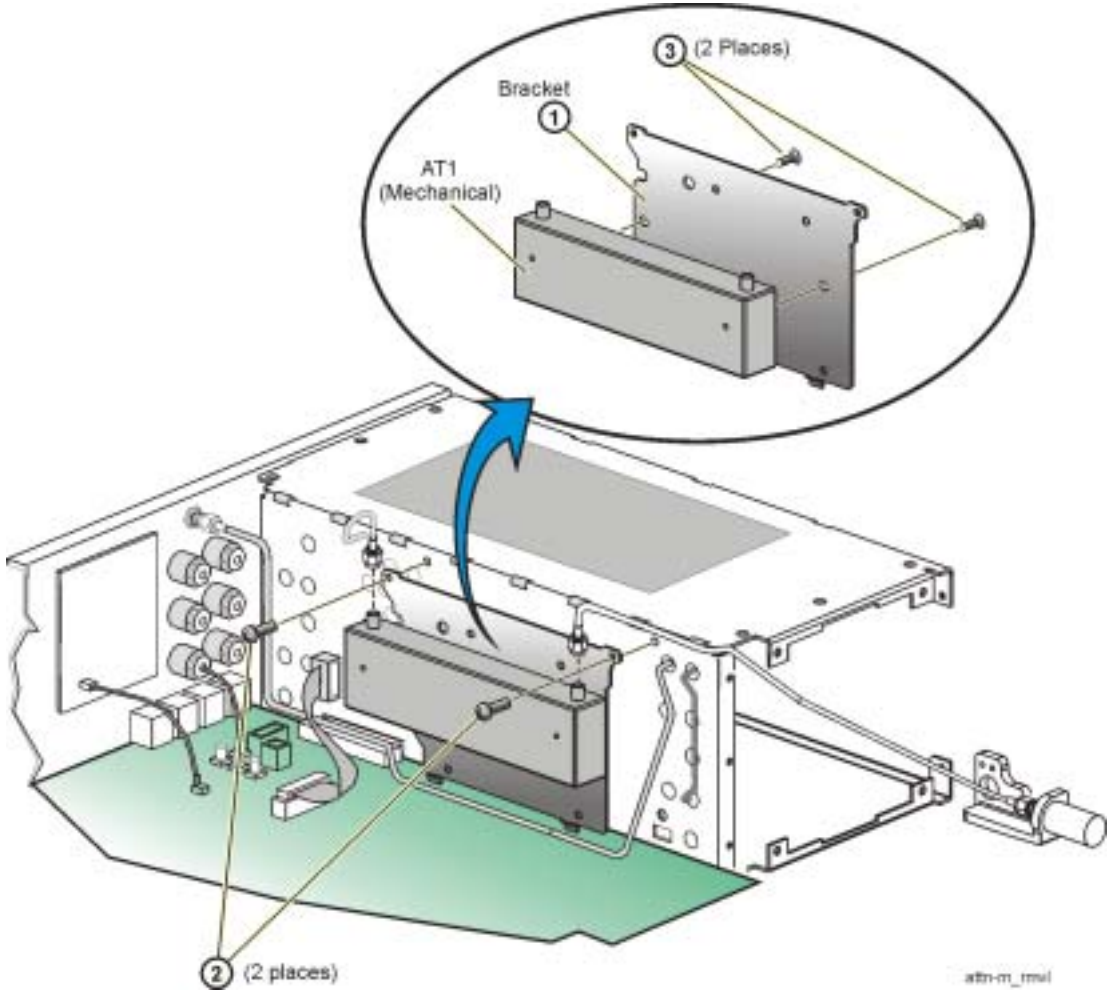


**Hardware of a Disassembled  
 AT1 High-Power Mechanical Attenuator (Option UNB)  
 on Option 506 Only**

**Table 3-32**

Item No.	Description	Qty	Part Number
1	bracket for AT1 High-Power Mechanical Attenuator (Option UNB)	1	E4400-00523
2	screw-machine (pan head screw used to attach the AT1 Mechanical Attenuator bracket to chassis)	2	0515-1227
3	screw-machine (flathead screw to hold the AT1 Mechanical Attenuator to the AT1 Mechanical Attenuator bracket)	2	0515-0664

**Figure 3-17 Hardware of a Disassembled  
 AT1 High-Power Mechanical Attenuator (Option UNB)  
 on Option 506**

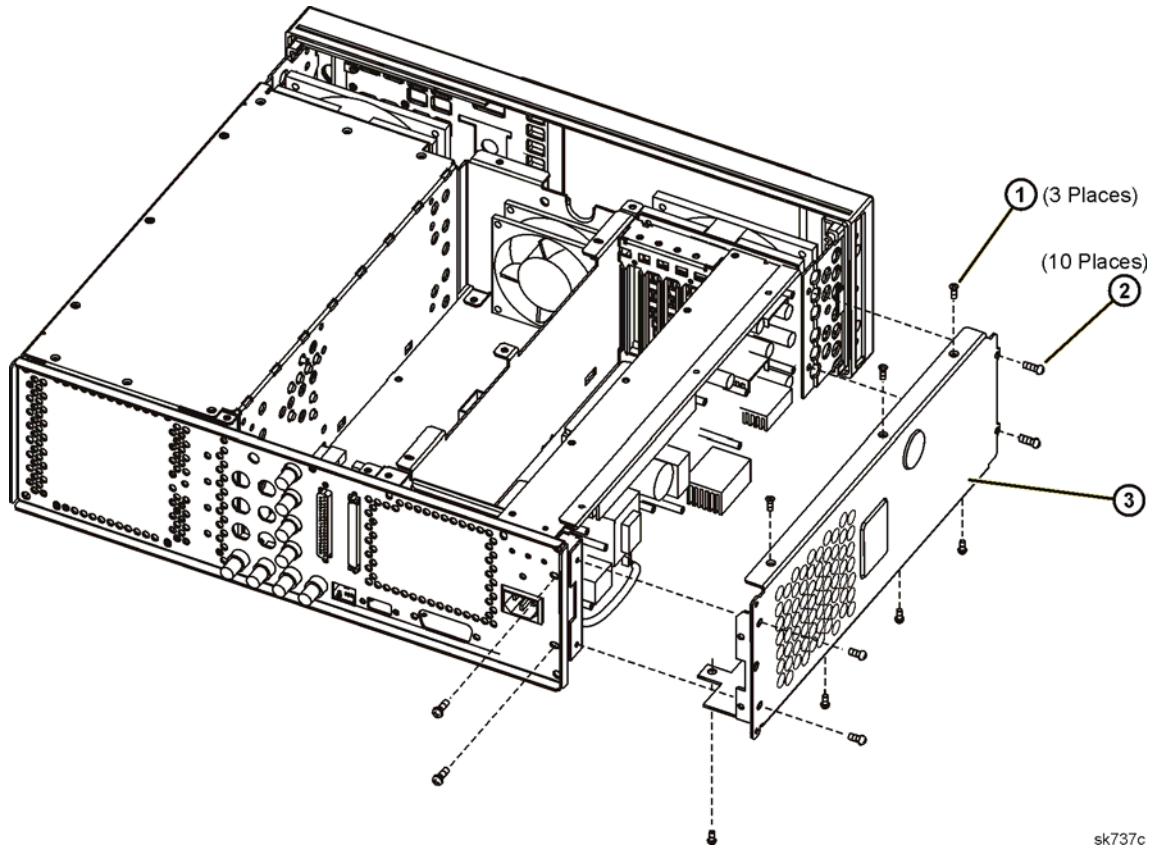


**Hardware of the A6 Power Supply Shield and Left-Side  
 (Option 501, 502, 503, 504, Option 300, Option UNJ, or Option 506)**

**Table 3-33**

Item No.	Description	Qty	Part Number
1	screw-machine M4 x 10 FL-TX	3	0515-1269
2	screw-machine	10	0515-0372
3	shield for A6 Power Supply	1	E4400-00512

**Figure 3-18 Hardware of the A6 Power Supply Shield and Left-Side  
 (Option 501, 502, 503, 504, Option 300, Option UNJ, or Option 506)**



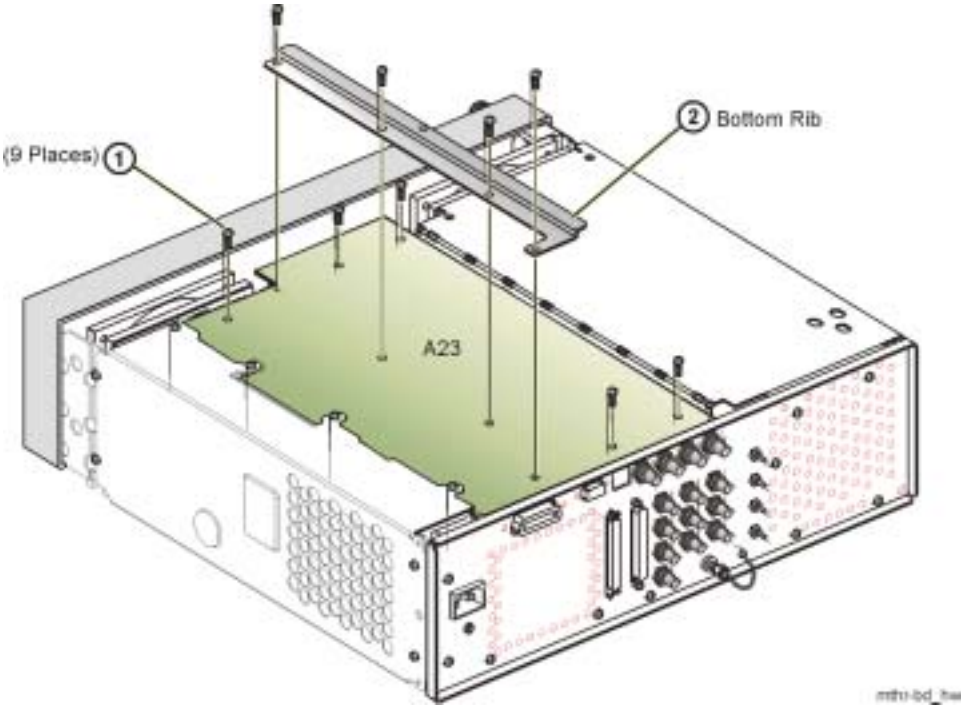


**Hardware of the A23 Motherboard and Bottom-Side  
 (Option 501, 502, 503, 504, Option 300, Option UNJ, or Option 506)**

**Table 3-34**

Item No.	Description	Qty	Part Number
1	screw-machine M3 x 8 CW-PN-TX	9	0515-0372
2	bottom rib-bracket	1	E4400-00527

**Figure 3-19 Hardware of the A23 Motherboard and Bottom-Side  
 (Option 501, 502, 503, 504, Option 300, Option UNJ, or Option 506)**

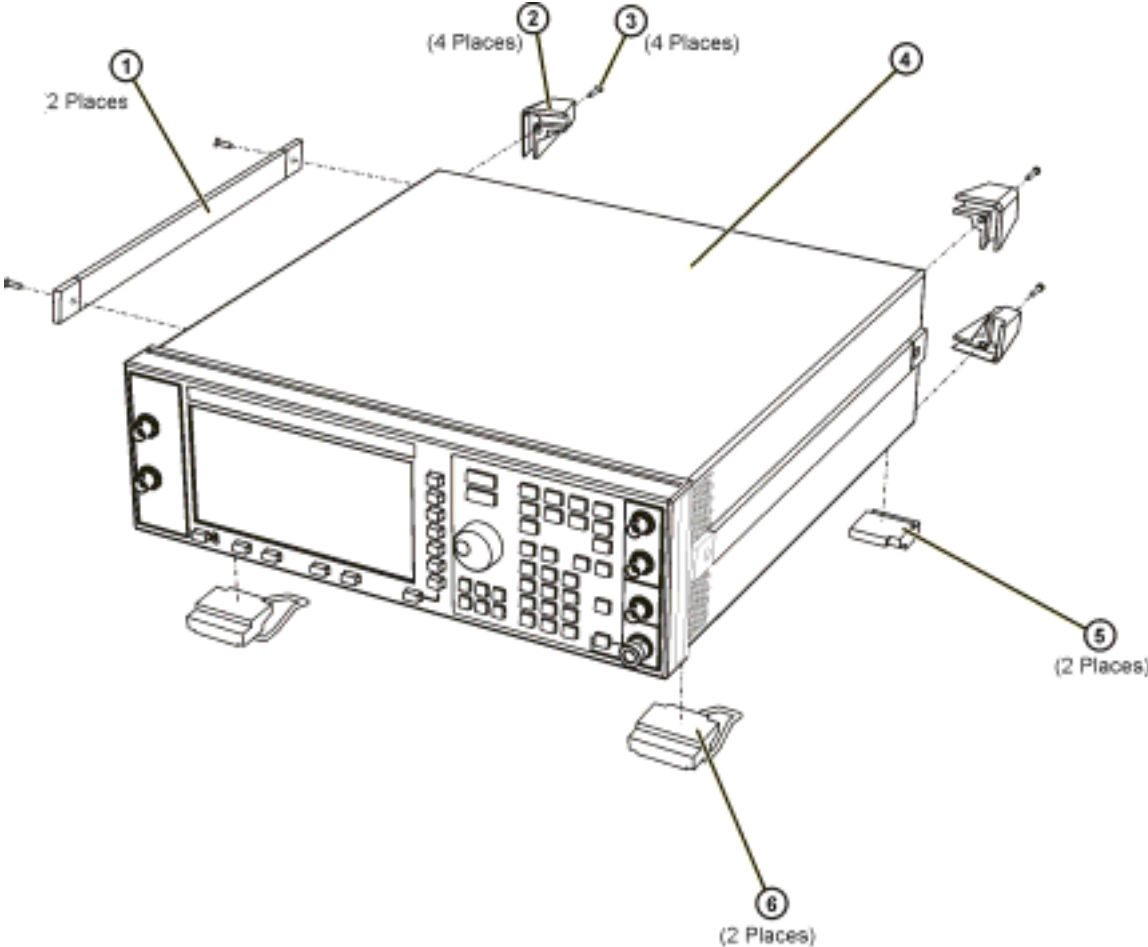


**Hardware of the Outside Instrument Covers and Associated Parts  
 (Option 501, 502, 503, 504, Option 300, Option UNJ, or Option 506)**

**Table 3-35**

Item No.	Description	Qty	Part Number
1	Kit includes the following parts: assembly-strap handle strap assembly ground spring handle retainer end cap screw-machine M5 x 18 FL-TX	Kit 2 2 4 4 4 4	E4400-60026
2	feet-bottom	4	5041-9167
3	feet-bottom screws	4	0515-1619
4	outside instrument cover	1	E4400-00518
5	feet-rear	2	5041-9611
6	tilt stand	2	1460-1345

**Figure 3-20** Hardware of the Outside Instrument Covers and Associated Parts  
(Option 501, 502, 503, 504, Option 300, Option UNJ, or Option 506)



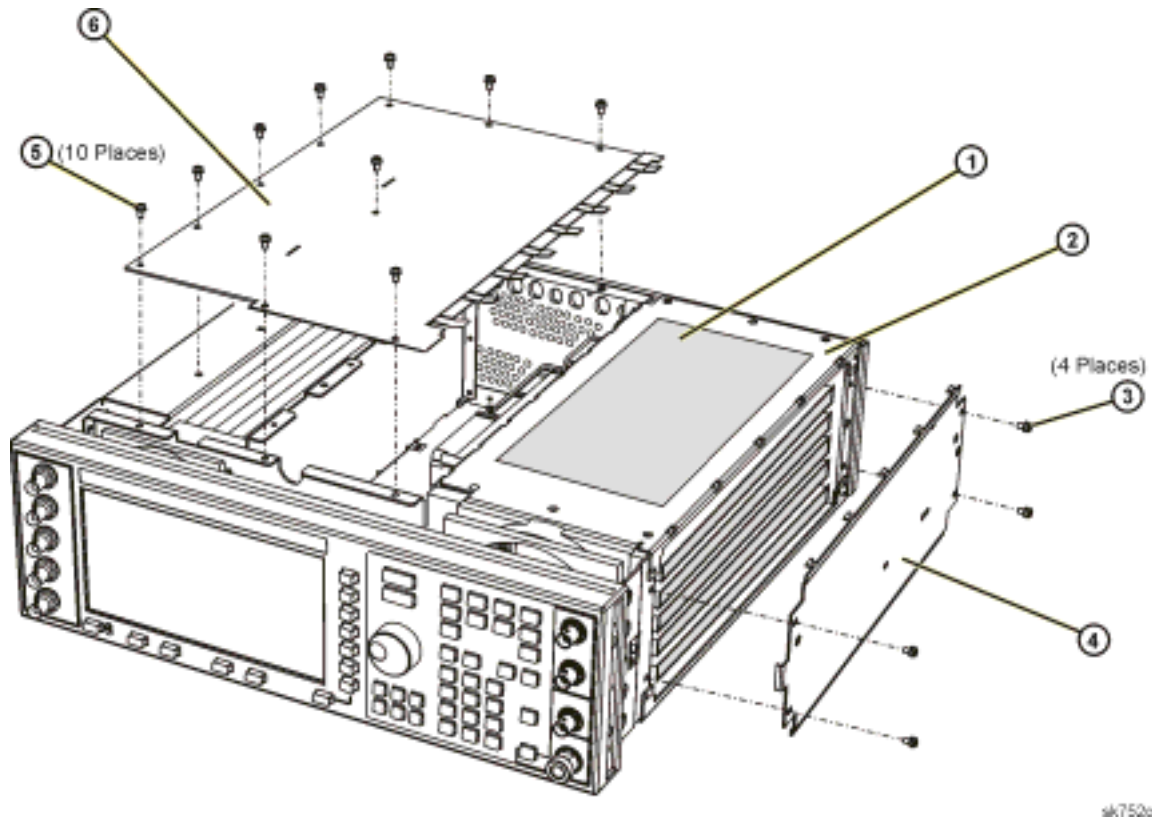
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**Hardware of the Inside Instrument Covers and Associated Parts  
 (Option 501, 502, 503, 504, Option 300, Option UNJ, or Option 506)**

**Table 3-36**

Item No.	Description	Qty	Part Number
1	label-connection diagram (Option 501, 502, 503, 504)	1	E4400-80513
	label-connection diagram (Option 300)	1	E4400-80514
	label-connection diagram (Option UNJ)	1	E4400-80501
2	chassis	1	E4400-00500
3	screw-machine (pan head screw used to attach inside-cover-side to chassis)	4	0515-0287
4	inside cover-side	1	E4400-00514
5	screw-machine (flathead screw used to attach inside cover-top to chassis)	10	0515-0430
6	inside cover-top	1	E4400-00513

**Figure 3-21 Hardware of the Inside Instrument Covers and Associated Parts  
 (Option 501, 502, 503, 504, Option 300, Option UNJ, or Option 506)**



sk752c

## Miscellaneous

This section lists part numbers for miscellaneous items that can be used with your signal generator.

### Accessories

**Table 3-37 Accessories**

Description	Part Number
ESD Connector End Cap Front-Panel RF Output	1401-0247
ESD Connector End Cap-GPIB	1401-5007
ESD Connector End Cap Coherent Carrier Out	1252-4696
Transit Case	9211-1296
Retainer Cable	E4400-40002
Retainer REX	0510-1643

### Documentation

**Table 3-38 Documentation**

Description	Part Number
CD-ROM Documentation	E4400-90501
Installation Guide	E4400-90502
User's Guide	E4400-90503
Key Reference - Volume 1 of 2	E4400-90504
Key Reference - Volume 2 of 2	E4400-90515
Programming Guide	E4400-90505
SCPI Command Reference - Volume 1 of 3	E4400-90506
SCPI Command Reference - Volume 2 of 3	E4400-90535
SCPI Command Reference - Volume 3 of 3	E4400-90594
Error Messages	E4400-90507
Service Guide	E4400-90511
Specifications/Data Sheet	5988-4039EN

## Abbreviations Used in Part Descriptions

This section defines the reference designators, abbreviations, and option numbers used in the part descriptions in this chapter.

<b>Reference Designator</b>	<b>Definition</b>
A	assembly
AT	attenuator
B	fan
DS	lamp
J	electrical connector; jack
W	cable; transmission path; wire

<b>Abbreviation</b>	<b>Definition</b>
BN	buttonhead (screws)
CPU	central processing unit
CW	conical washer (screws)
EXT	external
FL	flathead (screws)
Ft	feet
Hex	hexagonal
GPIB	general purpose interface bus
HX	hexagonal recess (screws)
I	in-phase
LF	low frequency
M	meters or metric hardware
PC	patch lock (screws) or printed circuit
PN	panhead (screws)
Q	quadrature
Qty	quantity
RF	radio frequency
RPP	reverse power protection
SMA	subminiature type-A

<b>Reference Designator</b>	<b>Definition</b>
TX	TORX recess (screws)

Replaceable Parts

**Hardware and Other Instrument Parts**



---

## **4 Post-Repair Procedures**

## Post-Repair Procedures Matrix

Table 4-1 Performance Tests for Assemblies A1 through A16

Performance Tests	Replaced Assembly															
	A1 Keyboard	A2 Display	A2DS1 Display Backlight	A3 Front Panel Auxiliary Board	A4 Inverter	A5 10 GB Hard Drive (Option 005)	A6 Power Supply	A7 Baseband Generator (Option 001/601) or A7 Baseband Generator (Option 002/602)	A9 Digital Demodulator (Option 501, 502, 503, 504 with Option 300)	A10 I/Q Multiplexer (Option 501, 502, 503, 504 or Option UNJ)	A11 Internal Bit Error Rate Analyzer (Option UN7)	A12 CPU	A13 Output (Option 501, 502, 503, 504 or Option UNJ)	A14 Extended Frequency Output (Option 506)	A15 Sampler (Option UNJ or Option 506)	A16 Frac-N (Option UNJ or Option 506)
The following performance tests are listed in the order that they should be performed to minimize changes in test equipment configurations.																
Self Test should be performed each time that any assembly is repaired or replaced.																
Self Test	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
Internal FM Accuracy and Distortion																X
Internal AM Accuracy and Distortion													X	X		
Phase Modulation Accuracy and Distortion																X
FM Frequency Response																X
AM Frequency Response													X	X		
Phase Modulation Frequency Response																X
DCFM Frequency Offset Relative to CW																X
Residual FM (Not Used with Option UNJ or Option 506)							X									
Harmonic, Subharmonic, and Nonharmonic Spurious Signals							X	X					X		X	X
Power Level Accuracy							X						X	X		X

**Table 4-1 Performance Tests for Assemblies A1 through A16**

Performance Tests	Replaced Assembly															
	A1 Keyboard	A2 Display	A2DS1 Display Backlight	A3 Front Panel Auxiliary Board	A4 Inverter	A5 10 GB Hard Drive (Option 005)	A6 Power Supply	A7 Baseband Generator (Option 001/601) or A7 Baseband Generator (Option 002/602)	A9 Digital Demodulator (Option 501, 502, 503, 504 with Option 300)	A10 I/Q Multiplexer (Option 501, 502, 503, 504 or Option UNJ)	A11 Internal Bit Error Rate Analyzer (Option UN7)	A12 CPU	A13 Output (Option 501, 502, 503, 504 or Option UNJ)	A14 Extended Frequency Output (Option 506)	A15 Sampler (Option UNJ or Option 506)	A16 Frac-N (Option UNJ or Option 506)
The following performance tests are listed in the order that they should be performed to minimize changes in test equipment configurations.																
Self Test should be performed each time that any assembly is repaired or replaced.																
Timebase Aging Rate (Manual Test - Option UNJ, Option 506, or Option 1E5 Only)																
Digital Modulation Level Accuracy									X				X	X		
Internal Digital Modulation Quality (Option 402 Only)							X		X				X			
Custom I/Q RF Modulation Quality (Option 402 Only)							X		X				X	X		
Pulse Modulation On/Off Ratio													X	X		
Burst Modulation On/Off Ratio													X	X		

**Table 4-2 Performance Tests for Assemblies A1 through A16**

Performance Tests	Replaced Assembly															
	A1 Keyboard	A2 Display	A2DS1 Display Backlight	A3 Front Panel Auxiliary Board	A4 Inverter	A5 10 GB Hard Drive (Option 005)	A6 Power Supply	A7 Baseband Generator (Option 001/601) or A7 Baseband Generator (Option 002/602)	A9 Digital Demodulator (Option 501, 502, 503, 504 with Option 300)	A10 I/Q Multiplexer (Option 501, 502, 503, 504 or Option UNJ)	A11 Internal Bit Error Rate Analyzer (Option UN7)	A12 CPU	A13 Output (Option 501, 502, 503, 504 or Option UNJ)	A14 Extended Frequency Output (Option 506)	A15 Sampler (Option UNJ or Option 506)	A16 Frac-N (Option UNJ or Option 506)
The following performance tests are listed in the order that they should be performed to minimize changes in test equipment configurations.																
Self Test should be performed each time that any assembly is repaired or replaced.																
CDMA Adjacent Channel Power (Not Used with Option UNB or Option 506)								X		X			X			
WCDMA Adjacent Channel Power (Option 400)								X		X			X			
Phase Noise and Residual FM (Manual Test - Option UNJ or Option 506 Only)						X							X	X	X	X
Dual Arbitrary Waveform Generator Check								X		X						
GSM Loopback BER Check (Option 300 Only)								X	X		X					

**Table 4-3 Performance Tests for Assemblies A17 through A29, B1, B2, B3, and the RF Output Connector**

Performance Tests	Replaced Assembly																
	A17 Synthesizer (Option 501, 502, 503, 504)	A18 Reference (Option 501, 502, 503, 504) or A18 Reference (Option 501, 502, 503, 504 with Option 1E5)	A19 Daughterboard	A20 Downconverter (Option 501, 502, 503, 504 with Option 300)	A21 YTO Driver (Option UNJ or Option 506)	A22 Coupler (Option 506)	A23 Motherboard	A23BT1 Battery	A24 Line Module	A25 Rear Panel Board	A26 Rear Panel LVDS Board (Option 1EM) and A27 Rear Panel SMB Board (Option 1EM)	AT1 Electronic Attenuator (Option 501, 502, 503, 504 or Option UNJ) or AT1 High-Power Mechanical Attenuator (Option UNB) and A28 Reverse Power Protection (Option 501, 502, 503, 504 or Option UNJ)	A29 DC Blocking Capacitor (Option 506)	B1 Power Supply Fan	B2 Small Fan	B3 Daughterboard/Card Cage Fan	RF Output Connector
Self Test	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
Internal FM Accuracy and Distortion	X	X			X		X										
Internal AM Accuracy and Distortion		X					X										
Phase Modulation Accuracy and Distortion	X	X			X		X										
FM Frequency Response	X	X			X		X										
AM Frequency Response		X					X										
Phase Modulation Frequency Response	X	X			X		X										
DCFM Frequency Offset Relative to CW	X	X					X	X									
Residual FM (Not Used with Option UNJ or Option 506)	X	X															
Harmonic, Subharmonic, and Nonharmonic Spurious Signals	X	X			X		X										
Power Level Accuracy	X		X		X	X	X		X	X	X	X	X				X

**Table 4-3 Performance Tests for Assemblies A17 through A29, B1, B2, B3, and the RF Output Connector**

<p><b>Performance Tests</b></p> <p>The following performance tests are listed in the order that they should be performed to minimize changes in test equipment configurations.</p> <p>Self Test should be performed each time that any assembly is repaired or replaced.</p>	<p><b>Replaced Assembly</b></p>																		
<p>Timebase Aging Rate (Manual Test - Option UNJ, Option 506, or Option 1E5 Only)</p>	X																		
<p>Digital Modulation Level Accuracy</p>																			
<p>Internal Digital Modulation Quality (Option 402 Only)</p>						X						X							
<p>Custom I/Q RF Modulation Quality (Option 402 Only)</p>						X													
<p>Pulse Modulation On/Off Ratio</p>	X					X													
<p>Burst Modulation On/Off Ratio</p>	X					X													

**Table 4-4 Performance Tests for Assemblies A17 through A29, B1, B2, B3, and the RF Output Connector**

<p><b>Performance Tests</b></p> <p>The following performance tests are listed in the order that they should be performed to minimize changes in test equipment configurations.</p> <p>Self Test should be performed each time that any assembly is repaired or replaced.</p>	<p><b>Replaced Assembly</b></p>																			
<p>CDMA Adjacent Channel Power (Not Used with Option UNB or Option 506)</p>																				
<p>WCDMA Adjacent Channel Power (Option 400)</p>																				
<p>Phase Noise and Residual FM (Manual Test - Option UNJ or Option 506 Only)</p>		<p>X</p>		<p>X</p>		<p>X</p>														
<p>Dual Arbitrary Waveform Generator Check</p>																				
<p>GSM Loopback BER Check (Option 300 Only)</p>			<p>X</p>																	

**Table 4-5 Adjustments for Assemblies A1 through A16**

Adjustments	Replaced Assembly															
	A1 Keyboard	A2 Display	A2DS1 Display Backlight	A3 Front Panel Auxiliary Board	A4 Inverter	A5 10 GB Hard Drive (Option 005)	A6 Power Supply	A7 Baseband Generator (Option 001/601) or A7 Baseband Generator (Option 002/602)	A9 Digital Demodulator (Option 501, 502, 503, 504 with Option 300)	A10 I/Q Multiplexer (Option 501, 502, 503, 504 or Option UNJ)	A11 Internal Bit Error Rate Analyzer (Option UN7)	A12 CPU	A13 Output (Option 501, 502, 503, 504 or Option UNJ)	A14 Extended Frequency Output (Option 506)	A15 Sampler (Option UNJ or Option 506)	A16 Frac-N (Option UNJ or Option 506)
The following adjustments are listed in the order that they should be performed for proper calibration.																
Self Test should be performed each time that any assembly is repaired or replaced.																
Self Test	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
Internal Reference Oscillator Calibration																
Analog Bus ADC Calibration											X	X	X	X	X	
Pretune Calibration (Option UNJ or Option 506 Only)																
Internal Source Calibration																
VCO Bias Potentiometer Calibration																X
Lock Angle Potentiometer Calibration (Not Used with Option UNJ or Option 506)																
KV versus Frequency Calibration																X
Timebase DAC Calibration																
FM Scale DAC Offset Calibration																X
FM Path Offset Calibration																X
FM In-Band DAC Offset Calibration																X
FM Inverting Amplifier Offset Calibration																X
FM 1/2 Path Ratio Gain Calibration																X
Modulation Source Relative Gain Calibration																X



**Table 4-5 Adjustments for Assemblies A1 through A16**

Adjustments	Replaced Assembly																
	A1 Keyboard	A2 Display	A2DS1 Display Backlight	A3 Front Panel Auxiliary Board	A4 Inverter	A5 10 GB Hard Drive (Option 005)	A6 Power Supply	A7 Baseband Generator (Option 001/601) or A7 Baseband Generator (Option 002/602)	A9 Digital Demodulator (Option 501, 502, 503, 504 with Option 300)	A10 I/Q Multiplexer (Option 501, 502, 503, 504 or Option UNJ)	A11 Internal Bit Error Rate Analyzer (Option UN7)	A12 CPU	A13 Output (Option 501, 502, 503, 504 or Option UNJ)	A14 Extended Frequency Output (Option 506)	A15 Sampler (Option UNJ or Option 506)	A16 Frac-N (Option UNJ or Option 506)	
The following adjustments are listed in the order that they should be performed for proper calibration.																	
Self Test should be performed each time that any assembly is repaired or replaced.																	
FM Out-of-Band Calibration (Not Used with Option UNJ or Option 506)																	
FM/PM Out-of-Band Calibration (Option UNJ or Option 506 Only)																	X
FM/PM YO Frequency Compensation Calibration (Option UNJ or Option 506 Only)															X	X	
DCFM Calibration												X					X
External Input Peak Detector Calibration																	
AM Audio Path Offset Calibration													X	X			
Burst Modulator Calibration													X	X			

**Table 4-6 Adjustments for Assemblies A1 through A16**

Adjustments	Replaced Assembly															
	A1 Keyboard	A2 Display	A2DS1 Display Backlight	A3 Front Panel Auxiliary Board	A4 Inverter	A5 10 GB Hard Drive (Option 005)	A6 Power Supply	A7 Baseband Generator (Option 001/601) or A7 Baseband Generator (Option 002/602)	A9 Digital Demodulator (Option 501, 502, 503, 504 with Option 300)	A10 I/Q Multiplexer (Option 501, 502, 503, 504 or Option UNJ)	A11 Internal Bit Error Rate Analyzer (Option UN7)	A12 CPU	A13 Output (Option 501, 502, 503, 504 or Option UNJ)	A14 Extended Frequency Output (Option 506)	A15 Sampler (Option UNJ or Option 506)	A16 Frac-N (Option UNJ or Option 506)
<b>The following adjustments are listed in the order that they should be performed for proper calibration.</b>																
<b>Self Test should be performed each time that any assembly is repaired or replaced.</b>																
Prelevel Calibration													X	X		X
VBLO Mixer Bias Calibration													X	X		
Digital Gain Adjust Calibration													X	X		
Bypass Gain Adjust Calibration													X	X		
ALC Calibration													X	X		
Power Level Accuracy, High Power Calibration													X	X		X
ALC Modulation Driver Bias Calibration													X	X		
Power Level Accuracy, Low Power Calibration													X	X		
Power Search Calibration													X	X		
AM Gain Calibration													X	X		
I/Q Gain/Offset/Quadrature Calibration								X	X				X	X		
I/Q Impairment Calibration								X	X				X	X		
IQ Timeskew Calibration-Baseband								X								
IQ Timeskew Calibration-RF								X					X			

**Table 4-7 Adjustments for Assemblies A17 through A29, B1, B2, B3, and the RF Output Connector**

Adjustments	Replaced Assembly																
	A17 Synthesizer (Option 501, 502, 503, 504)	A18 Reference (Option 501, 502, 503, 504) or A18 Reference (Option 501, 502, 503, 504 with Option 1E5)	A19 Daughterboard	A20 Downconverter (Option 501, 502, 503, 504 with Option 300)	A21 YTO Driver (Option UNJ or Option 506)	A22 Coupler (Option 506)	A23 Motherboard	A23BT1 Battery	A24 Line Module	A25 Rear Panel Board	A26 Rear Panel LVDS Board (Option 1EM) and A27 Rear Panel SMB Board (Option 1EM)	A28 Electronic Attenuator (Option 501, 502, 503, 504 or Option UNJ) or A21 High-Power Mechanical Attenuator (Option UNB)	A29 DC Blocking Capacitor (Option 506)	B1 Power Supply Fan	B2 Small Fan	B3 Daughterboard/Card Cage Fan	RF Output Connector
Self Test	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
Internal Reference Oscillator Calibration		X															
Analog Bus ADC Calibration	X	X			X		X										
Pretune Calibration (Option UNJ or Option 506 Only)					X												
Internal Source Calibration		X					X										
VCO Bias Potentiometer Calibration	X																
Lock Angle Potentiometer Calibration (Not Used with Option UNJ or Option 506)																	
KV versus Frequency Calibration																	
Timebase DAC Calibration		X															
FM Scale DAC Offset Calibration	X	X			X		X										
FM Path Offset Calibration	X	X			X		X										
FM In-Band DAC Offset Calibration	X	X			X		X										

**Table 4-7 Adjustments for Assemblies A17 through A29, B1, B2, B3, and the RF Output Connector**

Adjustments	Replaced Assembly																
	A17 Synthesizer (Option 501, 502, 503, 504)	A18 Reference (Option 501, 502, 503, 504) or A18 Reference (Option 501, 502, 503, 504 with Option 1E5)	A19 Daughterboard	A20 Downconverter (Option 501, 502, 503, 504 with Option 300)	A21 YTO Driver (Option UNJ or Option 506)	A22 Coupler (Option 506)	A23 Motherboard	A23BT1 Battery	A24 Line Module	A25 Rear Panel Board	A26 Rear Panel LVDS Board (Option 1EM) and A27 Rear Panel SMB Board (Option 1EM)	AT1 Electronic Attenuator (Option 501, 502, 503, 504 or Option UNJ) or AT1 High-Power Mechanical Attenuator (Option UNB)	A29 DC Blocking Capacitor (Option 506)	B1 Power Supply Fan	B2 Small Fan	B3 Daughterboard/Card Cage Fan	RF Output Connector
FM Inverting Amplifier Offset Calibration	X	X															
FM 1/2 Path Ratio Gain Calibration		X					X										
Modulation Source Relative Gain Calibration		X					X										
FM Out-of-Band Calibration (Not Used with Option UNJ or Option 506)	X	X															
FM/PM Out-of-Band Calibration (Option UNJ or Option 506 Only)		X			X												

**Table 4-7 Adjustments for Assemblies A17 through A29, B1, B2, B3, and the RF Output Connector**

Adjustments	Replaced Assembly																
	A17 Synthesizer (Option 501, 502, 503, 504)	A18 Reference (Option 501, 502, 503, 504) or A18 Reference (Option 501, 502, 503, 504 with Option 1E5)	A19 Daughterboard	A20 Downconverter (Option 501, 502, 503, 504 with Option 300)	A21 YTO Driver (Option UNJ or Option 506)	A22 Coupler (Option 506)	A23 Motherboard	A23BT1 Battery	A24 Line Module	A25 Rear Panel Board	A26 Rear Panel LVDS Board (Option 1EM) and A27 Rear Panel SMB Board (Option 1EM)	AT1 Electronic Attenuator (Option 501, 502, 503, 504 or Option UNJ) or AT1 High-Power Mechanical Attenuator (Option UNB)	A29 DC Blocking Capacitor (Option 506)	B1 Power Supply Fan	B2 Small Fan	B3 Daughterboard/Card Cage Fan	RF Output Connector
FM/PM YO Frequency Compensation Calibration (Option UNJ or Option 506 Only)		X			X												
DCFM Calibration	X	X						X									
External Input Peak Detector Calibration		X															
AM Audio Path Offset Calibration		X					X										
Burst Modulator Calibration																	

**Table 4-8 Adjustments for Assemblies A17 through A29, B1, B2, B3, and the RF Output Connector**

Adjustments	Replaced Assembly																
	A17 Synthesizer (Option 501, 502, 503, 504)	A18 Reference (Option 501, 502, 503, 504) or A18 Reference (Option 501, 502, 503, 504 with Option 1E5)	A19 Daughterboard	A20 Downconverter (Option 501, 502, 503, 504 with Option 300)	A21 YTO Driver (Option UNJ or Option 506)	A22 Coupler (Option 506)	A23 Motherboard	A23BT1 Battery	A24 Line Module	A25 Rear Panel Board	A26 Rear Panel LVDS Board (Option 1EM) and A27 Rear Panel SMB Board (Option 1EM)	AT1 Electronic Attenuator (Option 501, 502, 503, 504 or Option UNJ) or AT1 High-Power Mechanical Attenuator (Option UNB)	A29 DC Blocking Capacitor (Option 506)	B1 Power Supply Fan	B2 Small Fan	B3 Daughterboard/Card Cage Fan	RF Output Connector
Prelevel Calibration	X																
Digital Gain Adjust Calibration																	
Bypass Gain Adjust Calibration												X					
ALC Calibration											X	X					
Power Level Accuracy, High Power Calibration	X				X					X	X	X					X
ALC Modulation Driver Bias Calibration											X						
Power Level Accuracy, Low Power Calibration											X						
Power Search Calibration											X						
AM Gain Calibration											X						
I/Q Gain/Offset/Quadrature Calibration																	
I/Q Impairment Calibration																	

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## **5 Safety and Regulatory**

## Safety Notes

The following safety notes are used throughout this manual. Familiarize yourself with each of the notes and its meaning before operating this instrument.

---

**CAUTION** Caution denotes a hazard. It calls attention to a procedure that, if not correctly performed or adhered to, would result in damage to or destruction of the product. Do not proceed beyond a caution sign until the indicated conditions are fully understood and met.

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**WARNING** Warning denotes a hazard. It calls attention to a procedure which, if not correctly performed or adhered to, could result in injury or loss of life. Do not proceed beyond a warning note until the indicated conditions are fully understood and met.

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## General Safety Considerations

The following safety notes apply specifically to signal generators. These notes also appear in other chapters of this service guide as required.

---

**WARNING**    **These servicing instructions are for use by qualified personal only. To avoid electrical shock, do not perform any servicing unless you are qualified to do so.**

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**WARNING**    **The opening of covers or removal of parts is likely to expose dangerous voltages. Disconnect the product from all voltage sources before starting to open.**

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**WARNING**    **The detachable power cord is the instrument disconnecting device. It disconnects the mains circuits from the mains supply before other parts of the instrument. The front panel switch is only a standby switch and is not a LINE switch (disconnecting device).**

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**WARNING**    **The power cord is connected to internal capacitors that may remain live for 5 seconds after disconnecting the plug from its power supply.**

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**WARNING**    **This is a Safety Class 1 Product (provided with a protective earthing ground incorporated in the power cord). The mains plug shall only be inserted in a socket outlet provided with a protective earth contact. Any interruption of the protective conductor inside or outside of the product is likely to make the product dangerous. Intentional interruption is prohibited.**

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**WARNING**    **Danger of explosion if battery if incorrectly replaced. Replace only with the same or equivalent type recommended. Discard used batteries according to manufacturer's instructions.**

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**CAUTION**    **Many of the assemblies in this instrument are very susceptible to damage from ESD (electrostatic discharge). Perform service procedures only at a static-safe workstation and wear a grounding strap.**

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## Lithium Battery Disposal

When the battery on the A14 CPU/Motherboard is exhausted and/or ready for disposal, dispose of it according to your country's requirements. You can return the battery to your nearest Agilent Technologies Sales and Service office for disposal, if required

**Figure 5-1**



## **Warranty**

This Agilent Technologies product is warranted against defects in material and workmanship for a period of three years from date of shipment. During the warranty period, Agilent Technologies will, at its option, either repair or replace products which prove to be defective.

For warranty service or repair, this product must be returned to a service facility designated by Agilent Technologies. Buyer shall prepay shipping charges to Agilent Technologies and Agilent Technologies shall pay shipping charges to return the product to Buyer. However, Buyer shall pay all shipping charges, duties, and taxes for products returned to Agilent Technologies from another country.

Agilent Technologies warrants that its software and firmware designated by Agilent Technologies for use with an instrument will execute its programming instructions when properly installed on that instrument. Agilent Technologies does not warrant that the operation of the instrument, or software, or firmware will be uninterrupted or error-free.

### **Limitation of Warranty**

The foregoing warranty shall not apply to defects resulting from improper or inadequate maintenance by Buyer, Buyer-supplied software or interfacing, unauthorized modification or misuse, operation outside of the environmental specifications for the product, or improper site preparation or maintenance.

NO OTHER WARRANTY IS EXPRESSED OR IMPLIED. AGILENT TECHNOLOGIES SPECIFICALLY DISCLAIMS THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE.

Should Agilent Technologies have a negotiated contract with the user, and should any of the contract terms conflict with these terms, the contract shall control.

### **Exclusive Remedies**

THE REMEDIES PROVIDED HEREIN ARE BUYER'S SOLE AND EXCLUSIVE REMEDIES. AGILENT TECHNOLOGIES SHALL NOT BE LIABLE FOR ANY DIRECT, INDIRECT, SPECIAL, INCIDENTAL, OR CONSEQUENTIAL DAMAGES, WHETHER BASED ON CONTRACT, TORT, OR ANY OTHER LEGAL THEORY.

## **Assistance**

Product maintenance agreements and other customer assistance agreements are available for Agilent Technologies products. For any assistance, contact your nearest Agilent Technologies sales and service office.

## **Notice**

The information contained in this document is subject to change without notice.

Agilent Technologies makes no warranty of any kind with regard to this material, including but not limited to, the implied warranties of merchantability and fitness for a particular purpose. Agilent Technologies shall not be liable for errors contained herein or for incidental or consequential damages in connection with the furnishing, performance, or use of this material.

## **Certification**

Agilent Technologies Company certifies that this product met its published specifications at the time of shipment from the factory. Agilent Technologies further certifies that its calibration measurements are traceable to the United States National Institute of Standards and Technology, to the extent allowed by the Institute's calibration facility, and to the calibration facilities of other International Standards Organization members.

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